Variant/Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>Default</td>
</tr>
</tbody>
</table>

**PCB Label**

- **PCB Label**: Size: 0.65" x 0.20"
- **Variant/Label Table**
  - **Variant**: 001
  - **Label Text**: Default

**Variant Notes**

- **Variant 001**
  - **Note**: These assemblies are ESD sensitive, ESD precautions shall be observed.
  - **Note**: These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
  - **Note**: These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
Used for the EtherCAT platform
normally ESD diodes should be placed

- Couple In: 3-5 and 4-6
- Couple Out: 3-1 and 4-2

Use 2 shunts! Up to 3A!

- B: ChangeMe!
- TIDA-01461_TVS3300.SchDoc
- TIDA-01461_Buck.SchDoc
- TIDA-01461_PowerStage.SchDoc

Signal | Potential | MSP Pin | Cable Color
--- | --- | --- | ---
RX- | Up | 3 | Blue
RX+ | GND | 2 | White
TX- | GND | 4 | Orange
TX+ | GNDs | 1 | Yellow
Shield | - | - | -

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4.7μF optional
might lead to too
high inrush currents

default settings:
DVL0: 22.0V - 23.0V
DVL0: 26.0V - 29.0V

use shunt to change
DVL0: 26.0V - 29.0V

1μF cap in parallel to avoid
accidentally triggering DVL0
in case of surge event

diode helps to switch off faster
in case of surge event
gate voltage = Vout + 12V
now limited to max 33V

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EtherCAT P – One cable for power and EtherCAT

Reference Design Project Title: Designed for: Public Release

Assembly Variant: 001

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Drawn By: Tobias Puetz

Engineer: Tobias Puetz

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