1 Features
- Adjustable Output Voltage Down to –15 V
- 2.7-V to 5.5-V Input Voltage Range
- Up to 360-mA Output Current
- 1000-mA Typical Switch Current Limit
- Up to 84% Efficiency
- Typical 1.4-MHz Fixed-Frequency PWM Operation
- Thermal Shutdown
- Typical –19-V Output Overvoltage Protection
- 1.5-μA Shutdown Current
- Small 3-mm × 3-mm SON-10 Package (DRC)

2 Applications
- Generic Negative Voltage Supply
- Small-to-Medium Size OLED Displays
- Bias Supply

3 Description
The TPS63700 is an inverting DC/DC converter generating a negative output voltage down to –15 V with output currents up to 360 mA, depending on input-voltage to output-voltage ratio. With a peak efficiency of 84%, the device is ideal for portable battery-powered equipment. The input voltage range of 2.7 V to 5.5 V allows the TPS63700 to be directly powered from a Li-ion battery, from 3-cell NiMH/NiCd, from a 3.3-V or 5-V supply rail.

The inverter operates with a fixed-frequency pulse width modulation (PWM) control topology. The device has an internal current limit, overvoltage protection, and a thermal shutdown for highest reliability under fault conditions.

A switching frequency of typically 1.4 MHz allows the use of small external components enabling a small solution size.

The TPS63700 comes in a small 3-mm × 3-mm SON-10 package.

Efficiency vs Output Current

![Efficiency vs Output Current Graph]

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS63700</td>
<td>VSON (10)</td>
<td>3.00 mm x 3.00 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the datasheet.
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4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision C (June 2013) to Revision D

Page

• Added Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section ................................. 1

Changes from Revision B (November 2007) to Revision C

Page

• Deleted Dissipation Ratings table and added Thermal Information table. ......................................................... 4
5 Pin Configuration and Functions

DRC Package
10-Pin VSON With PowerPAD™
Top View

Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>COMP</td>
<td>I/O</td>
<td>Compensation pin for control, connect a 4.7 nF capacitor between this pin and GND</td>
</tr>
<tr>
<td>GND</td>
<td>—</td>
<td>Ground pin</td>
</tr>
<tr>
<td>VIN</td>
<td>I</td>
<td>Supply voltage input for control logic, connect a RC circuit of 10R and 100 nF to filter this supply voltage</td>
</tr>
<tr>
<td>EN</td>
<td>I</td>
<td>Enable pin (EN = GND: disabled; EN = VIN: enabled)</td>
</tr>
<tr>
<td>IN</td>
<td>I</td>
<td>Supply voltage for the power switch</td>
</tr>
<tr>
<td>SW</td>
<td>O</td>
<td>Inverter switch output</td>
</tr>
<tr>
<td>PS_GND</td>
<td>I</td>
<td>Connect to GND for control logic</td>
</tr>
<tr>
<td>OUT</td>
<td>I</td>
<td>Output voltage sense input</td>
</tr>
<tr>
<td>FB</td>
<td>I</td>
<td>Feedback pin for the voltage divider</td>
</tr>
<tr>
<td>VREF</td>
<td>O</td>
<td>Reference voltage output. Connect a 220-nF capacitor to ground. Connect the lower resistor of the negative output voltage divider to this pin.</td>
</tr>
</tbody>
</table>
6 Specifications

6.1 Absolute Maximum Ratings

over operating free-air temperature range unless otherwise noted\(^{(1)}\)

<table>
<thead>
<tr>
<th>Specification</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage at (\text{VIN})(^{(2)})</td>
<td>–0.3</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>Input voltage at (\text{IN})(^{(2)})</td>
<td>(\text{VIN})</td>
<td>(\text{VIN})</td>
<td>V</td>
</tr>
<tr>
<td>Minimum voltage at (\text{OUT})(^{(2)})</td>
<td>–18</td>
<td>(\text{VIN})</td>
<td>V</td>
</tr>
<tr>
<td>Voltage at (\text{EN, FB, COMP, PS_GND})(^{(2)})</td>
<td>–0.3</td>
<td>(\text{VIN} + 0.3)</td>
<td>V</td>
</tr>
<tr>
<td>Differential voltage between (\text{OUT}) to (\text{VIN})(^{(2)})</td>
<td>24</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Operating virtual junction temperature, (T_J)</td>
<td>–40</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature, (T_{\text{stg}})</td>
<td>–65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

\(^{(1)}\) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

\(^{(2)}\) All voltage values are with respect to network ground terminal, unless otherwise noted.

6.2 ESD Ratings

<table>
<thead>
<tr>
<th>(V_{(\text{ESD})}) Electrostatic discharge</th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins(^{(1)})</td>
<td>±2000</td>
<td>V</td>
</tr>
<tr>
<td>Charged device model (CDM), per JEDEC specification JESD22-C101, all pins(^{(2)})</td>
<td>±1000</td>
<td>V</td>
</tr>
</tbody>
</table>

\(^{(1)}\) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

\(^{(2)}\) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Specification</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage range, (\text{VIN})</td>
<td>2.7</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>Operating free-air temperature, (T_A)</td>
<td>–40</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>Operating virtual junction temperature, (T_J)</td>
<td>–40</td>
<td>125</td>
<td>°C</td>
</tr>
</tbody>
</table>

6.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(^{(1)})</th>
<th>TPS63700 DRC (VSON)</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\Psi_{JT}) Junction-to-top characterization parameter</td>
<td>1.2</td>
<td>°C/W</td>
</tr>
<tr>
<td>(\Psi_{JB}) Junction-to-board characterization parameter</td>
<td>16.8</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JAC}}) Junction-to-case(bottom) thermal resistance</td>
<td>4.1</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JAC}_\text{(top)}}) Junction-to-case(top) thermal resistance</td>
<td>62.8</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JAB}}) Junction-to-board thermal resistance</td>
<td>16.6</td>
<td>°C/W</td>
</tr>
<tr>
<td>(R_{\text{JAC}}) Junction-to-ambient thermal resistance</td>
<td>41.2</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

\(^{(1)}\) For more information about traditional and new thermal metrics, see the Semiconductor and IC Package Thermal Metrics application report, SPRA953.
6.5 Electrical Characteristics

−40°C to 85°C, over recommended input voltage range, typical at an ambient temperature of 25°C (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER(1)</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>SUPPLY</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VIN</td>
<td>Input voltage range</td>
<td>Pin VIN, IN</td>
<td>2.7</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>I(Q) Quiescent current</td>
<td>VIN</td>
<td>3.6 V, IOUT = 0, EN = VIN, no switching</td>
<td>330</td>
<td>400</td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>IN</td>
<td>VOUT = −5 V</td>
<td>640</td>
<td>750</td>
<td>μA</td>
</tr>
<tr>
<td>ISD</td>
<td>Shutdown supply current</td>
<td>EN = GND</td>
<td>0.2</td>
<td>1.5</td>
<td>μA</td>
</tr>
<tr>
<td>UVLO</td>
<td>Undervoltage lockout threshold</td>
<td>EN = GND</td>
<td>2.1</td>
<td>2.35</td>
<td>2.7</td>
</tr>
<tr>
<td>TSD</td>
<td>Thermal shutdown temperature</td>
<td>Junction temperature decreasing</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Thermal Shutdown hysteresis</td>
<td></td>
<td>5</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>CONTROL STAGE</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VEN</td>
<td>High level input voltage</td>
<td>EN = VIN or GND</td>
<td>1.4</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>VEN</td>
<td>Low level input voltage</td>
<td>EN = VIN or GND</td>
<td>0.4</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>IEN Input current</td>
<td>EN = VIN or GND</td>
<td>0.01</td>
<td>0.1</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>POWER SWITCH</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ILIM Inverter switch current limit</td>
<td>2.7 V &lt; VIN &lt; 5.5 V</td>
<td>860</td>
<td>1000</td>
<td>1140</td>
<td>mA</td>
</tr>
<tr>
<td>RDS(ON) Inverter switch on-resistance</td>
<td>VIN = 3.6 V</td>
<td>440</td>
<td>600</td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td></td>
<td>VOUT = 5 V</td>
<td>370</td>
<td>500</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DMAX Maximum duty cycle inverting converter</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>87.5%</td>
</tr>
<tr>
<td>DMIN Minimum duty cycle inverting converter</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>12.5%</td>
</tr>
<tr>
<td>IS Oscillator frequency</td>
<td></td>
<td></td>
<td>1250</td>
<td>1380</td>
<td>1500</td>
</tr>
<tr>
<td>OUTPUT</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VOUT Adjusted output voltage range</td>
<td></td>
<td></td>
<td>−15</td>
<td>−2</td>
<td>V</td>
</tr>
<tr>
<td>VOUT DC output accuracy</td>
<td>PWM mode, device switching</td>
<td></td>
<td></td>
<td>±3%</td>
<td></td>
</tr>
<tr>
<td>VREF Reference voltage</td>
<td>IREF = 10 μA</td>
<td></td>
<td>1.2</td>
<td>1.213</td>
<td>1.225</td>
</tr>
<tr>
<td>VOVP Output overvoltage protection</td>
<td></td>
<td></td>
<td>−19</td>
<td></td>
<td></td>
</tr>
<tr>
<td>VFB Negative feedback regulation voltage</td>
<td>VIN = 2.7 V to 5.5 V</td>
<td></td>
<td>−0.024</td>
<td>0</td>
<td>0.024</td>
</tr>
<tr>
<td>IFB Negative feedback input bias current</td>
<td></td>
<td></td>
<td></td>
<td>2</td>
<td>nA</td>
</tr>
</tbody>
</table>

(1) Parameter does not include tolerance of external resistors.
6.6 Typical Characteristics

Figure 1. Maximum Output Current vs Input Voltage
7 Detailed Description

7.1 Overview
The TPS63700 is a DC/DC converter for negative output voltages using buck-boost topology. It operates with an input voltage range of 2.7 V to 5.5 V and generates a negative output voltage down to –15 V. The output is controlled by a fixed-frequency, pulse-width-modulated (PWM) regulator. In normal operation mode, the converter operates at continuous conduction mode (CCM). At light loads it can enter discontinuous conduction mode (DCM).

7.2 Functional Block Diagram

7.3 Feature Description

7.3.1 Enable
Applying GND signal at the EN pin disables the converter, where all internal circuitry is turned off. The device now just consumes low shutdown current flowing into the VIN pin. The output load of the converter is also disconnected from the battery as described in Load Disconnect. Pulling the EN pin to V

7.3.2 Load Disconnect
The device supports complete load disconnection when the converter is disabled. The converter turns off the internal PMOS switch, thus no DC current path remains between load and input voltage source.

7.3.3 Output Overvoltage Protection
The converter has an output overvoltage protection implemented. The output voltage is limited to –19 V in case the feedback connection from the output to the FB pin is open.
Feature Description (continued)

7.3.4 Undervoltage Lockout
An undervoltage lockout prevents the device from starting up and operating if the supply voltage at VIN is lower than the programmed threshold shown in the Electrical Characteristics table. The device automatically shuts down the converter when the supply voltage at VIN falls below this threshold. Nevertheless, parts of the control circuits remain active, which is different than device shutdown using EN inputs. The undervoltage lockout function is implemented to prevent device malfunction.

7.3.5 Overtemperature Shutdown
The device automatically shuts down if the implemented internal temperature detector detects a chip temperature above the programmed threshold shown in the electrical characteristics table. It starts operating again when the chip temperature decreases. A built-in temperature hysteresis avoids undefined operation caused by ringing from over-temperature shutdown.

7.4 Device Functional Modes

7.4.1 Soft-Start
The converter has a soft-start function. When the converter is enabled, the implemented switch current limit ramps up slowly to its nominal value. Soft-start is implemented to limit the input current during start-up to avoid high peak currents at the battery which could interfere with other systems connected to the same battery.

Without soft-start, uncontrolled input peak currents flow to charge up the output capacitors and to supply the load during start-up. This would cause significant voltage drops across the series resistance of the battery and its connections.

7.4.2 PWM Operation
The converter operates in a fixed-frequency, pulse-width-modulated control scheme. The on-time of the switches varies depending on input-to-output voltage ratio and the load. During this on-time, the inductor connected to the converter is charged with current. In the remaining time, the time period set by the fixed operating frequency, the inductor discharges into the output capacitor via the rectifier diode. At medium to heavy loads the inductor current is continuous and the device operates in continuous conduction mode (CCM).

7.4.3 Power Save Mode Operation
As the load current decreases, the converter enters Power Save Mode. Entering Power Save Mode happens at the boundary to discontinuous conduction mode (DCM). During light load, the inductor current of this converter can become discontinuous. In this case, the control circuit of the controller output automatically takes care of these changing conditions to always operate with an optimum control setup.

7.4.4 Control
The controller circuit of the converter is based on a fixed-frequency, multiple-feed-forward controller topology. Input voltage, output voltage, and voltage drop across the switch are monitored and forwarded to the regulator. Changes in the operating conditions of the converter directly affect the duty cycle.

The error amplifier compares the voltage at FB pin with GND to generate an accurate and stable output voltage. The error amplifier is internally compensated. At light loads, the converter operates in discontinuous conduction mode (DCM).

If the load will be further decreased, the energy transmitted to the output capacitor cannot be absorbed by the load and would lead to an increase of the output voltage. In this case, the converter limits the output voltage increase by skipping switch pulses.
8 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information
The TPS63700 DC/DC converter is intended for systems typically powered by a single-cell Li-ion or Li-polymer battery with a terminal voltage between 2.7 V up to 4.2 V. Due to the recommended input voltage going up to 5.5 V, the device is also suitable for 3-cell alkaline, NiCd, or NiMH batteries, as well as regulated supply voltages of 3.3 V or 5 V.

8.2 Typical Application

Figure 2. Circuit for –5-V Output

8.2.1 Design Requirements
The design of the inverter can be adapted to different output voltage and load current needs by choosing external components appropriately. The following design procedure is adequate for the whole $V_{IN}$, $V_{OUT}$ and load current range of TPS63700.

Table 1 shows the list of components for the Application Curves.

Table 1. List of Components

<table>
<thead>
<tr>
<th>REFERENCE</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C2, C3, C4,</td>
<td>X7R/X5R ceramic</td>
</tr>
<tr>
<td>C5</td>
<td>4 x 4.7 μF X7R/X5R ceramic</td>
</tr>
<tr>
<td>D1</td>
<td>SL03/SL02 Vishay</td>
</tr>
<tr>
<td>L1</td>
<td>–5V: TDK VLF4012 4R7, TDK SLF6025-4R7, Coilcraft LPS4018-472, –12V: Sumida CDRH5D18 10 μH</td>
</tr>
</tbody>
</table>
8.2.2 Detailed Design Procedure

8.2.2.1 Programming the Output Voltage: Converter

The output voltage of the TPS63700 converter can be adjusted with an external resistor divider connected to the FB pin. The reference point of the feedback divider is the reference voltage \( V_{\text{REF}} \) with 1.213 V. The typical value of the voltage at the FB pin is 0 V. The minimum recommended output voltage at the converter is –15 V. The feedback divider current should be 10 \( \mu \)A. The voltage across R2 is 1.213 V. Based on those values, the recommended value for R2 should be 120 k\( \Omega \) to 200 k\( \Omega \) in order to set the divider current at the required value. The value of the resistor R3 can then be calculated using Equation 1, depending on the needed output voltage (\( V_{\text{OUT}} \)).

\[
R3 = R2 \times \left( \frac{V_{\text{REF}} - V_{\text{OUT}}}{V_{\text{REF}}} - 1 \right)
\]  

For example, if an output voltage of –5 V is needed and a resistor of 150 k\( \Omega \) has been chosen for R2, a 619-k\( \Omega \) resistor is needed to program the desired output voltage.

8.2.2.1.1 Inductor Selection

An inductive converter normally requires two main passive components for storing energy during the conversion: an inductor and a storage capacitor at the output are required.

The average inductor current depends on the output load, the input voltage \( V_{\text{IN}} \), and the output voltage \( V_{\text{OUT}} \). It can be estimated with Equation 2, which shows the formula for the inverting converter.

\[
I_{\text{avg}} = \frac{V_{\text{IN}} - V_{\text{OUT}}}{V_{\text{IN}} \times 0.8} \times I_{\text{OUT}}
\]

where

- \( I_{\text{avg}} \) = Average inductor current

An important parameter for choosing the inductor is the desired current ripple in the inductor.

A ripple current value between 20% and 80% of the average inductor current can be considered as reasonable, depending on the application requirements. A smaller ripple reduces the losses in the inductor, as well as output voltage ripple and EMI. But in the same way, the inductor becomes larger and more expensive.

Keeping those parameters in mind, the possible inductor value can be calculated using Equation 3.

\[
L = \frac{V_{\text{IN}} \times V_{\text{OUT}}}{\Delta I_L \times (V_{\text{OUT}} - V_{\text{IN}}) \times f}
\]

where

- \( \Delta I_L \) = Peak-to-peak ripple current
- \( f \) = Switching frequency
- \( L \) = Inductor value

With the known inductor current ripple, the peak inductor value can be approximated with Equation 4. The peak current through the switch and the inductor depends also on the output load, the input voltage \( V_{\text{IN}} \), and the output voltage \( V_{\text{OUT}} \). To select the right inductor, it is recommended to keep the possible peak inductor current below the current-limit threshold of the power switch. For example, the current-limit threshold of the TPS63700 switch for the inverting converter is nominally 1000 mA.

\[
I_{\text{LMAX}} = \frac{V_{\text{IN}} - V_{\text{OUT}}}{V_{\text{IN}} \times 0.8} \times I_{\text{OUT}} + \frac{\Delta I_L}{2}
\]

where

- \( I_{\text{LMAX}} \) = Peak inductor current
- \( \Delta I_L \) = Peak-to-peak ripple current

With Equation 5, the inductor current ripple at a given inductor can be approximated.
The following inductor series from different suppliers have been tested with the TPS63700 converter, see Table 2.

### Table 2. List of Inductors

<table>
<thead>
<tr>
<th>Output Voltage</th>
<th>Vendor</th>
<th>SUGGESTED INDUCTOR</th>
</tr>
</thead>
<tbody>
<tr>
<td>−5 V</td>
<td>TDK</td>
<td>VLF4012 4.7 μH</td>
</tr>
<tr>
<td>−5 V</td>
<td>TDK</td>
<td>SLF6025-4.7 μH</td>
</tr>
<tr>
<td>−5 V</td>
<td>Coilcraft</td>
<td>LPS4018 4.7 μH</td>
</tr>
<tr>
<td>−5 V</td>
<td>Coilcraft</td>
<td>LPS3015 4.7 μH</td>
</tr>
<tr>
<td>−12 V</td>
<td>Sumida</td>
<td>CDRH5D18 10 μH</td>
</tr>
<tr>
<td>−12 V</td>
<td>Coilcraft</td>
<td>MOS6020 10 μH</td>
</tr>
</tbody>
</table>

### Capacitor Selection

#### 8.2.2.2.1 Input Capacitor

At least a 10-μF ceramic input capacitor is recommended for a good transient behavior of the regulator, and EMI behavior of the total power supply circuit.

#### 8.2.2.2.2 Output Capacitors

One of the major parameters necessary to define the capacitance value of the output capacitor is the maximum allowed output voltage ripple of the converter. This ripple is determined by two parameters of the capacitor, the capacitance and the ESR. It is possible to calculate the minimum capacitance needed for the defined ripple, supposing that the ESR is zero, by using Equation 6 for the inverting converter output capacitor.

\[
C_{\text{min}} = \frac{I_{\text{OUT}} \times V_{\text{OUT}}}{f_s \times \Delta V \times (V_{\text{OUT}} - V_{\text{IN}})}
\]

where
- \( f_s \) = Switching frequency
- \( \Delta V \) = Maximum allowed ripple
- \( C_{\text{min}} \) = Minimum capacitance

With a chosen ripple voltage in the range of 10 mV, a minimum capacitance of 12 μF is needed. The total ripple is larger due to the ESR of the output capacitor. This additional component of the ripple can be calculated using Equation 7.

\[
\Delta V_{\text{ESR}} = I_{\text{OUT}} \times R_{\text{ESR}}
\]

where
- \( \Delta V_{\text{ESR}} \) = Voltage ripple caused by \( R_{\text{ESR}} \) of capacitor
- \( R_{\text{ESR}} \) = Equivalent series resistance of capacitor
An additional ripple of 2 mV is the result of using a typical ceramic capacitor with an ESR in a 10-mΩ range. The total ripple is the sum of the ripple caused by the capacitance, and the ripple caused by the ESR of the capacitor. In this example, the total ripple is 12 mV. Additional ripple is caused by load transients. When the load current increases rapidly, the output capacitor must provide the additional current until the inductor current has been increased by the control loop by setting a higher on-time at the main switch (duty cycle). The higher duty cycle results in longer inductor charging periods, but the rate of increase of the inductor current is also limited by the inductance itself. When the load current decreases rapidly, the output capacitor needs to store the excessive energy (stored in the inductor) until the regulator has decreased the inductor current by reducing the duty cycle. The recommendation is to use higher capacitance values, as the previous calculations show.

8.2.2.3 Stabilizing the Control Loop

8.2.2.3.1 Feedback Divider

To speed up the control loop, a feed-forward capacitor of 10 pF is recommended in the feedback divider, parallel to R3.

To avoid coupling noise into the control loop from the feed-forward capacitor, the feed-forward effect can be bandwidth-limited by adding series resistor R4. A value in the range of 100 kΩ is suitable. The higher the resistance, the lower the noise coupled into the control loop system.

8.2.2.3.2 Compensation Capacitor

The control loop of the converter is completely compensated internally. However the internal feed-forward system requires an external capacitor. A 4.7-nF capacitor at the COMP pin of the converter is recommended.
8.2.3 Application Curves

Figure 3. Efficiency vs Output Current, VOUT –5 V

Figure 4. Efficiency vs Output Current, VOUT –12 V

Figure 5. Efficiency vs Output Current, VOUT –15 V

Figure 6. Efficiency vs Input Voltage, VOUT –5 V

Figure 7. Efficiency vs Input Voltage, VOUT –12 V

Figure 8. Output Voltage vs Output Current
Figure 9. Output Voltage vs Output Current

V = 3.6 V, I = 200 mA, V = –5 V
IN LOAD OUT
4.2 V 3.6 V
V 500 mV/div, DC IN
V 100 mV/div, DC OUT
t - Time - 2 ms/div

Figure 10. Output Voltage in Discontinuous Conduction Mode

V IN = 3.6 V, I LOAD = 20 mA
V OUT 20 mV/div, AC
V OUT = –5 V I COIL 200 mA/div, DC
t - Time - 500 ns/div

Figure 11. Output Voltage in Continuous Conduction Mode

V IN = 3.6 V, I LOAD = 45 to 150 mA
V OUT 100 mV/div, AC
V OUT = –5 V I LOAD 50 mA/div, DC
t - Time - 2 ms/div

Figure 12. Load Transient Response, –5 V, 45 to 150 mA

V IN = 3.6 V to 4.2 V, I LOAD = 100 mA, V OUT = –5 V
V 500 mV/div, DC
V OUT 100 mV/div, DC
t - Time - 2 ms/div

Figure 13. Line Transient Response, –5 V

V IN = 3.6 V, Load = 22 Ω, V OUT = –5 V
I COIL 200 mA/div, DC
V OUT 2 V/div, DC
t - Time - 500 μs/div

Figure 14. Start-Up After Enable, –5 V
8.3 System Example

![Circuit Diagram for -12-V Output](image)

Figure 15. Circuit for –12-V Output
9 Power Supply Recommendations
The power supply to the TPS63700 needs to have a current rating according to the input supply voltage, output voltage and output current of the TPS63700.

10 Layout

10.1 Layout Guidelines
For all switching power supplies the layout is an important step in the design, especially at high peak currents and high switching frequencies. If the layout is not carefully done, the regulator could show stability problems as well as EMI problems. Therefore, use wide and short traces for the main current paths, and for the power-ground tracks. The input and output capacitors should be placed as close as possible to the IC. The diode need to be connected closest to the SW pin to minimize parasitic inductance. For low noise operation small bypass capacitors $C_{IN\text{ BP}}$ and $C_{OUT\text{ BP}}$ in the nF range can be added close to the IC.

The feedback divider should be placed as close as possible to the $V_{REF}$ pin of the IC. Use short traces when laying out the control ground. Figure 18 shows the layout of the EVM board.

10.2 Layout Example

![Figure 16. Layout Considerations, Top View](image-url)
Layout Example (continued)

Figure 17. Layout Considerations, Bottom View

Figure 18. Layout Circuit
11 Device and Documentation Support

11.1 Device Support

11.1.1 Third-Party Products Disclaimer

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11.2 Community Resources

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**Design Support**  *TI’s Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

11.3 Trademarks

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11.4 Electrostatic Discharge Caution

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.5 Glossary

**SLYZ022 — TI Glossary.**

This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
### PACKAGING INFORMATION

<table>
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<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
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<td>VSON</td>
<td>DRC</td>
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(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substances do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
- **RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) **Lead/Ball Finish** - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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**TAPE AND REEL INFORMATION**

*All dimensions are nominal.*

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**TAPE AND REEL BOX DIMENSIONS**

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*All dimensions are nominal*
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for optimal thermal and mechanical performance.
EXAMPLE BOARD LAYOUT

DRC0010J

VSON - 1 mm max height

PLASTIC SMALL OUTLINE - NO LEAD

NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

5. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
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