

PGA400-Q1

EEPROG Undervoltage Flag

Errata Description

The EEPROG undervoltage (UV) flag is set every time the EEPROM programming sequence is initiated. This occurs because the EEPROM programming voltage (V_EEPROG) is sampled during its ramp-up time, which takes about 250 µs.

System Impact

The EEPROG UV flag does not accurately report a true UV problem during EEPROM programming. This can be worked around using one of the methods in the following section.

System Workaround

Workaround 1:

One solution is to ignore the EEPROG UV flag and to validate the EEPROM write cycle by reading the EEPROM and verifying its contents.

Workaround 2:

If the EEPROG UV flag is cleared after V_EEPROG reaches its full value and before the programming is completed, the EEPROG UV flag is accurate. Because some variance is possible in the V_EEPROG ramp-up time, it is recommended to wait at least 1 ms after initiating the EEPROM programming before clearing the EEPROG UV flag. Pseudo-code of this algorithm follows.

- 1. Initiate EEPROM programming.
- 2. Wait at least 1 ms.
- 3. Clear the EEPROG UV flag.
- 4. Wait until at least 15 ms have elapsed since EEPROM programming was initiated.
- 5. Stop programming the EEPROM.
- 6. Read the EEPROG UV and OV flags to determine if an error occurred during programming.

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