

# TPS613221AEVM-019 Evaluation Module

This user's guide describes the characteristics, operation, and use of the TPS613221AEVM-019 evaluation module (EVM). The EVM contains the TPS613221A device, which is a high performance, high efficiency, synchronous boost converter with only  $6-\mu A$  quiescent current. The user's guide includes the EVM specifications, recommended test setup, test results, schematic diagram, bill of materials, and the board layout.

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Introduction www.ti.com

### 1 Introduction

## 1.1 Performance Specification

Table 1 lists the TPS613221A EVM performance specifications. All specifications are given for an ambient temperature of 25°C.

**Table 1. Performance Specification Summary** 

| SPECIFICATION | TEST CONDITIONS                            | MIN | TYP | MAX | UNIT |
|---------------|--|-----|-----|-----|------|
| VIN           | _  |     | 1.5 |     | V    |
| VOUT          | TPS613221A EVM, VIN = 1.5 V,<br>Io ≤ 0.1 A |     | 3.3 |     | V    |

### 1.2 Modification

The printed-circuit board (PCB) for this EVM is designed to accommodate some modifications by the user. The external component can be changed according to the real application.

## 1.3 Input Capacitor

A 150- $\mu$ F tantalum capacitor, C1, is added as the input capacitor in the EVM. The ESR of the tantalum capacitor is 0.1  $\Omega$ , which helps to damp the ringing of the input voltage when the EVM is powered by a power supply with a long cable. The capacitor is not required for proper operation and can be removed in a real application.

## 1.4 Output Capacitor Selection

A 22-µF ceramic capacitor, C3, is added as the output capacitors. This capacitor can ensure the low output ripple at a heavy load condition.

## 1.5 Schottky Diode Selection

TI recommends adding a Schottky type diode (D1 in Figure 1) with low forward voltage (VF) and low capacitance, to improve efficiency in heavy load conditions. If the diode is used, a snubber circuit of a resistor and a capacitor should be used in parallel to the diode, to stabilize the system operation. The recommended capacitance of the capacitor of the snubber circuit is 3x that of the diode capacitance. The typical values are  $5~\Omega$  for the resistor and 120~pF for the capacitor.

## 2 Test Setup

This section describes how to properly connect, set up, and use the TPS613221AEVM-019 device.

## 2.1 Input/Output Connector Descriptions

This section describes the input/output connector descriptions.

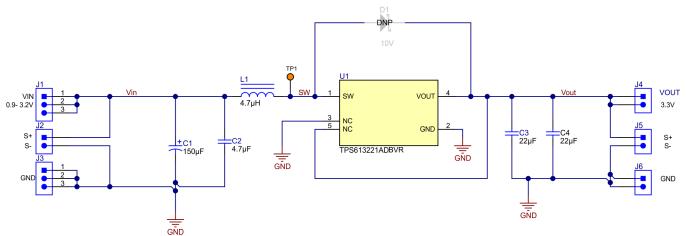
- J1-VIN: Positive input connection from the input supply for the EVM
- J3-GND: Return connection from the input supply for the EVM
- J4-VOUT: Positive connection for the output voltage
- J6-GND: Return connection for the output voltage



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## 3 Schematic

This section provides the TPS613221AEVM-019 device schematic.



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Figure 1. TPS613221AEVM-019 Schematic



Bill of Materials www.ti.com

## 4 Bill of Materials

Table 2 lists the bill of materials (BOM).

## Table 2. Bill of Materials

| Designator        | QTY | Value  | Part Number        | Manufacturer         | Description   | Package                     |
|-------------------|-----|--------|--------------------|----------------------|---|-----------------------------|
| C1                | 1   | 150 μF | T520B157M006ATE070 | Kemet                | Capacitor, TA, 150 $\mu$ F, 6.3 V, $\pm$ 20%, 0.07 $\Omega$ , SMD                   | 3528-21                     |
| C3                | 1   | 22 µF  | GRM188R60J226MEA0D | MuRata               | Capacitor, ceramic, 22 μF, 6.3 V, ± 20%, X5R, 0603                                  | 0603                        |
| C4                | 1   | 22 µF  | GRM21BR61C226ME44L | MuRata               | Capacitor, ceramic, 22 μF, 16 V, ± 20%, X5R, 0805                                   | 0805                        |
| J1, J3            | 2   |        | TSW-103-07-G-S     | Samtec               | Header, 100 mil, 3 x 1, gold, TH  | 3 x 1 header                |
| J2, J4, J5,<br>J6 | 4   |        | TSW-102-07-G-S     | Samtec               | Header, 100 mil, 2 x 1, gold, TH  | 2 x 1 header                |
| L1                | 1   | 4.7 μH | DFE252012F-4R7M=P2 | MuRata Toko          | Inductor, shielded, powdered iron, 4.7 $\mu\text{H},0.15$ A, 0.19 $\Omega,$ SMD     | 2.5 × 1.2 × 2 mm            |
| TP1               | 1   | Orange | 5003               | Keystone             | Test point, miniature, orange, TH   | Orange miniature test point |
| U1                | 1   |        | TPS613221ADBVR     | Texas<br>Instruments | 6.5-μA quiescent current, 1.8-A switch current boost converter, DBV0005A (SOT-23-5) | DBV0005A                    |
| C2                | 0   | 4.7 µF | GRM155R61A475MEAAD | MuRata               | Capacitor, ceramic, 4.7 μF, 10 V, ± 20%, X5R, 0402                                  | 0402                        |
| D1                | 0   | 10 V   | ZLLS410TA          | Diodes Inc.          | Diode, Schottky, 10 V, 0.75 A, SOD-323  | SOD-323                     |



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# 5 Board Layout

Figure 2 and Figure 3 show the board layout.

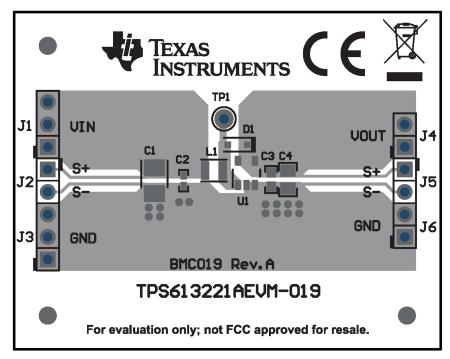


Figure 2. TPS613221AEVM-019 Top-Side Layout

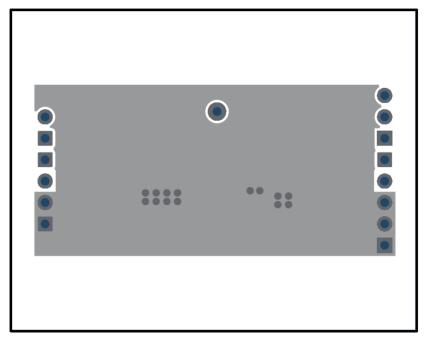


Figure 3. TPS613221AEVM-019 Bottom-Side Layout



Revision History www.ti.com

# **Revision History**

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

| Changes from Original (January 2018) to A Revision |   |   |  |  |
|--|---|---|--|--|
| •  | Added Schottky Diode Selection section          | 2 |  |  |
| •  | Changed TPS613221AEVM-019 Top-Side Layout image | 5 |  |  |

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- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
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Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

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