EVM User's Guide: DP83TG720-EVM-AM2

AM2x Evaluation Module for Automotive Ethernet PHY Add-on Board



Description

The DP83TG720-EVM-AM2 is an Automotive Ethernet PHY add-on board to be used with AM2x series Sitara™ high-performance microcontroller evaluation modules. This add-on board is an excellent choice for initial Ethernet evaluation and prototyping using AM2x EVMs. DP83TG720-EVM-AM2 is equipped with a TI DP82TG720S-Q1 1000BASE-T1 automotive Ethernet PHY with RGMII & SGMII, and a MATEnet connector. DP83TG720-EVM-AM2

is currently supported on TMDSCNCD263P and the AM263Px MCU PLUS SDK.

Features

- DP83TG720S-Q1 1000BASE-T1 automotive Ethernet PHY with RGMII & SGMII
- MATEnet Ethernet networking connector
- Shielded DF40GB 48-pin connector for interfacing with Sitara[™] AM2x series evaluation modules
- · Link status and activity LED indicators
- On-board TLV755P LDO





1 Evaluation Module Overview

Preface: Read This First

1.1.1 Sitara MCU+ Academy

Texas Instruments offers the *MCU+ Academy* as a resource for designing with the MCU+ software and tools on supported devices. The MCU+ Academy features easy-to-use training modules that range from the basics of getting started to advanced development topics.

1.1.2 If You Need Assistance

If you have any feedback or questions, support for the Sitara AM2x MCUs and the AM2x EVM Automotive Ethernet PHY Add-on Board development kit is provided by the TI Product Information Center (PIC) and the TI E2E[™] Forum. Contact information for the PIC can be found on the TI website. Additional device-specific information can be found in Section 5.1.

1.1 Introduction

The AM2x EVM Automotive Ethernet PHY Add-on Board was developed to enable additional Ethernet peripheral support on AM2x EVMs and allow for rapid prototyping of the core SoC for Automotive Ethernet applications. This user's guide details the design of the add-on board and how to properly use the interface. The user's guide also details many important aspects of the board including, but not limited to pin header descriptions, test points, and signal routing.

1.2 Kit Contents

The Sitara AM2x EVM Automotive Ethernet PHY Add-on Board kit contains the following items:

DP83TG720-EVM-AM2 Automotive Ethernet PHY Add-on Board

Not included:

Sitara AM2x EVM

Note

DP83TG720-EVM-AM2 can be available as a virtual bundle with select Sitara AM2x EVMs. Visit the EVM page on ti.com for more information.

1.3 Device Information

The DP83TG720S-Q1 device is an IEEE 802.3bp and Open Alliance compliant automotive Ethernet physical layer transceiver. The DP83TG720S-Q1 provides all physical layer functions needed to transmit and receive data over unshielded and shielded single twisted-pair cables. The device provides xMII flexibility with support for RGMII and SGMII MAC interfaces.

DP83TG720 is compliant to Open Alliance EMC and interoperable specifications over unshielded twisted cable. DP83TG720 is front print compatible to Tl's 100BASE-T1 PHY enabling design scalability with single board for both speeds. This device offers the Diagnostic Tool Kit, with an extensive list of real-time monitoring tools, debug tools and test modes. Within the tool kit is the first integrated electrostatic discharge (ESD) monitoring tool. This tool is capable of counting ESD events on both the xMII and MDI as well as providing real-time monitoring through the use of a programmable interrupt. Additionally, the DP83TG720S-Q1 includes a data generator and checker tool to generate customizable MAC packets and check the errors on incoming packets. This enables systemlevel datapath tests and optimizations without dependency on MAC.

For additional information, refer to the DP83TG720S-Q1 data sheet.

2 Hardware

2.1 Component Identification

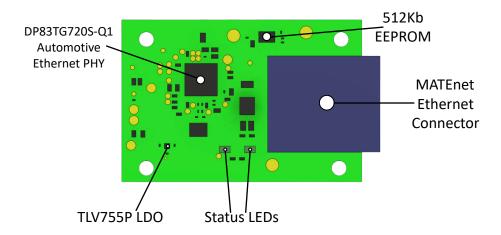


Figure 2-1. DP83TG720-EVM-AM2 Top Side

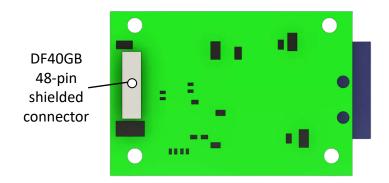


Figure 2-2. DP83TG720-EVM-AM2 Bottom Side

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2.2 Power Requirements

The AM2x EVM Industrial Ethernet PHY Add-on Board is powered from 2.5V and 3.3V inputs from the DF40GB 48-pin connector that interfaces the DP83TG720-EVM-AM2 with the main MCU EVM. There is an on-board LDO that steps the 2.5V input down to 1.0V to supply the DP83TG720S-Q1 VDD inputs. The following sections describe the power distribution network topology that supply the AM2x EVM Industrial Ethernet PHY Add-on Board, supporting components, and reference voltages.

2.2.1 Power Tree

The DP83TG720-EVM-AM2 power is supplied from the main Sitara AM2x EVM via the DF40GB connector.

3.3V (VDDIO) is connected to pin 44 and 46 on the DF40GB connector, and is passed to the 3.3V source inputs on the DP83TG720S-Q1 Automotive Ethernet PHY through a pair of ferrite beads (one per supply net).

2.5V (VDD_2V5) is connected to pins 4 and 6 on the DF40GB connector, stepped down to 1.0V by the TLV755P LDO, and is passed to the 1.0V source inputs on the DP83TG720S-Q1 Automotive Ethernet PHY through a ferrite bead.

Figure 2-3 shows the power connections of DP83TG720-EVM-AM2.

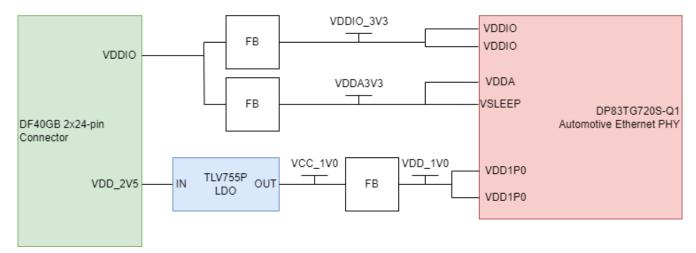


Figure 2-3. Power Tree

2.3 Functional Block Diagram

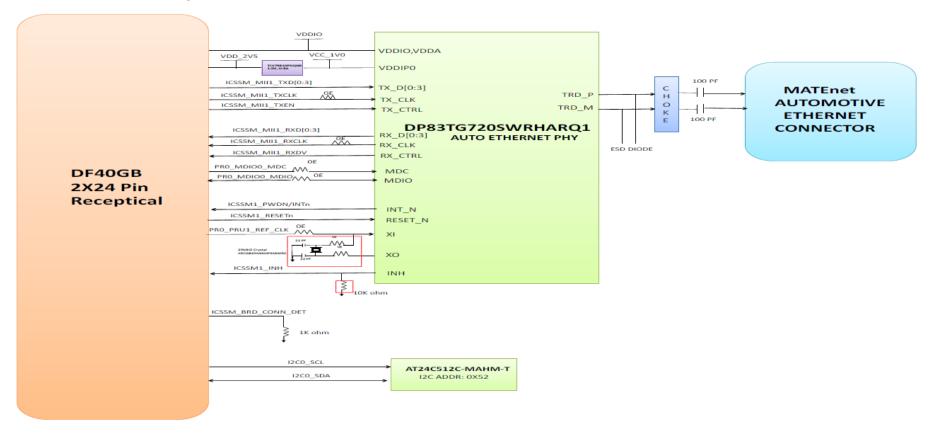


Figure 2-4. AM2x Automotive Ethernet PHY Add-on Board Block Diagram

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2.4 Header Information

The DP83TG720-EVM-AM2 is equipped with a Hirose DF40GB 2x24-pin connector (J2) to connect to Sitara AM2x EVMs. Listed below are the features of this connector relevant to this EVM:

- 2x24 pins
- · Shielded type to support high-speed signals and prevent noise
- · High density mounting

Refer to Table 2-1 for a complete list of the header pins and descriptions.

Table 2-1. DF40GB Header Pinout

Pin#	Signal	Description	Description	Signal	Pin#
1	GND	Ground	PMIC External Voltage Monitor	EXT_VMON2	2
3	TX_CLK	Transmit Clock	2.5V supply	VDD_2V5	4
5	GND	Ground	2.5V supply	VDD_2V5	6
7	TX_D0	Transmit Data 0	Ground	GND	8
9	TX_D1	Transmit Data 1	Interrupt To Ethernet PHY	PWDN/INTn	10
11	TX_D2	Transmit Data 2	Reset input to Ethernet PHY	RESETn	12
13	TX_D3	Transmit Data 3	Collision Detected	COL	14
15	GND	Ground	Ground	GND	16
17	GND	Ground	Ground	GND	18
19	RX_CLK	Receive Clock	MDIO Clock	MDIO_MDC	20
21	GND	Ground	MDIO Data	MDIO_MDIO	22
23	RX_D0	Receive Data 0	Ground	GND	24
25	RX_D1	Receive Data 1	Inhibit	INH	26
27	RX_D2	Receive Data 2	PRUx Reference Clock	REF_CLK	28
29	RX_D3	Receive Data 3	Carrier Sense	CRS	30
31	GND	Ground	Ground	GND	32
33	GND	Ground	Ground	GND	34
35	TXEN	Transmit Enable	Board Connection Detect	BRD_CONN_DET	36
37	GND	Ground	Test Point (open)	TP	38
39	RX_ER	Receive Data Error	I2C Clock	I2C_SCL	40
41	GND	Ground	I2C Data	I2C_SDA	42
43	RX_LINK	Receive Indicator	IO Voltage Supply	VDDIO	44
45	RXDV	Receive Data Valid	IO Voltage Supply	VDDIO	46
47	NC	No Connect	No Connect	NC	48

2.5 Test Points

DP83TG720-EVM-AM2 is equipped with multiple test points for hardware debug and bench testing. DP82TG720-EVM-AM2 Test Pointsshows the test points on the board and their associated signal net.

Table 2-2. DP83TG720-EVM-AM2 Test Points

Test Point	Signal	Description
TP1	GND	Ground
TP2	GND	Ground
TP3	GND	Ground
TP4	RXCLK	Receive Clock
TP5	RXD1	Receive Data 1
TP6	RXD3	Receive Data 3
TP7	TXCLK	Transmit Clock
TP8	RXD0	Receive Data 0
TP9	RXD2	Receive Data 2
TP10	CLK_OUT2	DP83TG720S-Q1 25-MHz reference clock output
TP11	GND	Ground
TP12	TXEN	Transmit Enable
TP13	TXD3	Transmit Data 3
TP14	GND	Ground
TP15	RXDV	RGMII Receive Control
TP16	TXD1	Transmit Data 1
TP17	TXD2	Transmit Data 2
TP18	TXD0	Transmit Data 0
TP19	RXER	Receive Data Error
TP20	J2-38	DF40GB add-on board connector pin 38
TP21	RXLINK	Receive Indicator
TP22	MDIO_MDIO	MDIO Data
TP23	INH	Inhibit
TP24	RESETn	PHY Reset
TP25	CRS	Carrier Sense
TP26	MDIO_MDC	MDIO Clock
TP27	REF_CLK	Reference Clock Input
TP28	COL	Collision Detected
TP29	EXT_VMON	PMIC External Voltage Monitor
TP30	LED1	Link Status and BLINK for TX/RX Activity
TP31	VCC_1V0	1.0V PHY supply
TP32	GND	Ground

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2.6 Interfaces

2.6.1 Automotive Ethernet PHY

The AM2x EVM Automotive Ethernet PHY Add-on Board uses one port of RGMII signals and the PRUx core of the PRU-ICSS to be connected to a 48-pin Ethernet PHY (DP83TG720SWRHARQ1). The PHY is configured to advertise 1-Gb operation. The Ethernet data signals of the PHY are terminated to a MATEnet connector. LEDs are used to indicate link status and activity.

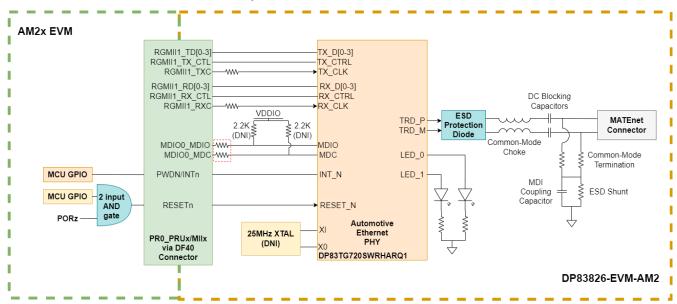


Figure 2-5. Automotive Ethernet PHY

The Ethernet PHY requires three power sources, VDDIP0 (1.0V), VDDIO (3.3V) and VDDA (3.3V) which are supplied through the DF40GB connector (J2) and an on-board LDO (U4).

Note

DP83TG720-EVM-AM2 is configured for VDDIO=3.3V, but can be supplied using VDDIO=2.5V or VDDIO=1.8V

On some AM2x EVMs, The RGMII port of the CPSW signals are internally muxed on the same balls of the MCU as the PRU-ICSS Ethernet signals. To use RGMII, the balls must be set to the appropriate mux mode for RGMII.

The MDIO and Interrupt signals from the main EVM SoC to the PHY require 2.2KΩ pull up resistors to the I/O supply voltage for proper operation. These resistors are not assembled by default on the DP83TG720-EVM-AM2, but there are footprints if the main EVM does not have these signals pulled up. The interrupt signal is driven by a GPIO signal that is mapped from the main EVM SoC.

The reset signal for the Ethernet PHY is most often driven by a 2-input AND gate. The AND gate's inputs are a GPIO signal that is generated by the main SoC EVM and a power-on reset signal on the main EVM.

2.6.2 Automotive Ethernet PHY Strapping Resistors

The Ethernet PHY uses many functional pins as strap option to place the device into specific modes of operation.

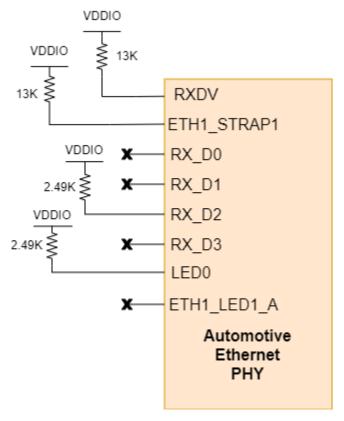


Figure 2-6. Automotive Ethernet PHY Strapping Resistors

Table 2-3. Recommended 3-level Strap Resistor Ratios

MODE	IDEAL RH (kΩ) for VDDIO = 3.3V	
1	OPEN	
2	13	
3	4.5	

Table 2-4. Recommended 2-level Strap Resistor

MODE	IDEAL RH (kΩ)	
1	OPEN	
2	2.49	

Table 2-5. Industrial 1 Gbit Ethernet PHY Strapping Resistors

Functional Pin	Default Mode	Mode on DP83TG720- EVM-AM2	Pull-Up	Function
RX_D0	1	1	OPEN	MAC Interface: RGMII (Align mode)
RX_D1	1	1	OPEN	
RX_D2	1	2 (2-level)	2.49kΩ	
RX_CTRL		2 (3-level)	13kΩ	PHY address: 0xC (0b01100)
STRP_1		2 (3-level)	13kΩ	
LED_0	1	2 (2-level)	2.49kΩ	MS=0
LED_1	1	1	OPEN	Autonomous

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2.7 Integration Guide

The Sitara AM2x Ethernet Add-on Board ecosystem is not limited to the DP83TG720x Automotive Ethernet PHY. A wide variety of Automotive Ethernet PHYs with Sitara AM2x MCU compatible signals can be designed onto add-on boards to be used across Sitara AM2x MCU EVMs. This section details the mechanical information and provide the necessary dimensions for designing an Automotive Ethernet PHY add-on board.

Note All dimensions are measured in inches.

2.7.1 Board Dimensions

Figure 2-7 shows the proper PCB dimensions for an Industrial Ethernet PHY add-on board to be compatible with Sitara AM2x EVMs.

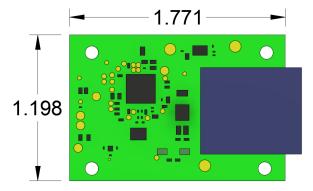


Figure 2-7. Automotive Ethernet PHY Add-on Board Dimensions

Figure 2-8 shows the side profile of the PCB.

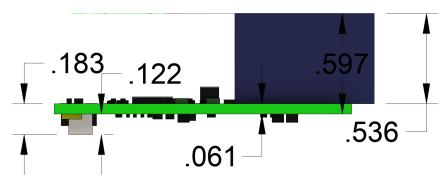


Figure 2-8. Automotive Ethernet PHY Add-on Board Side Profile

2.7.2 DF40GB Connector

TI recommends the 48-pin (2x24) Hirose DF40GB high-density, shielded connector for Ethernet PHY Add-on Boards interfacing with Sitara AM2x EVMs. Figure 2-9 shows the mounting position of the DF40GB connector on the DP83TG720-EVM-AM2.

The origin of the connector is to be placed at (x,y)=(0.157,0.537).

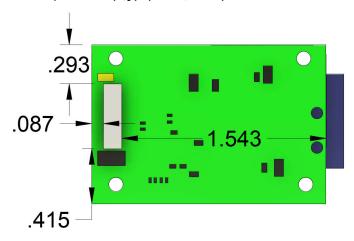


Figure 2-9. DF40GB Connector Mounting Position

2.7.3 Mounting Holes

The DP83TG720-EVM-AM2 is designed with mounting holes to securely attach to the main Sitara AM2x MCU EVM. The compatible Sitara AM2x EVMs are designed to have matching mounting holes for an Ethernet Add-on Board to connect to. Screws and spacers can be inserted into the mounting holes for a more permanent configuration. Figure 2-10 shows the positions of the mounting holes on the DP83TG720-EVM-AM2.

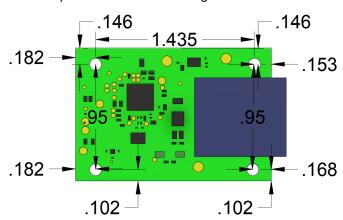


Figure 2-10. Mounting Hole Positions

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2.7.4 MATEnet Ethernet Connector

The DP83TG720-EVM-AM2 has a MATEnet Ethernet Connector for sending and receiving signals from the DP83TG720S-Q1 Ethernet PHY. Different Automotive Ethernet PHYs utilize the same connector, and must be used on custom Automotive Ethernet PHY Add-on Boards. Figure 2-11 shows the position of the MATEnet connector on the DP83TG720-EVM-AM2.

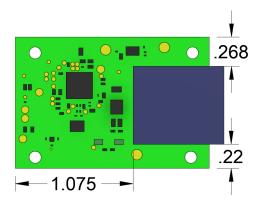


Figure 2-11. MATEnet Connector Position

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3 Hardware Design Files

To download the zip file containing the latest design files for the EVM, go to the product folder DP83TG720-EVM-AM2.

4 Additional Information

4.1 Trademarks

Sitara[™] and E2E[™] are trademarks of Texas Instruments. All trademarks are the property of their respective owners.

5 References

5.1 Reference Documents

In addition to this document, the following references are available for download at www.ti.com.

- AM263P controlCARD Evaluation Module Tool Folder
- AM263P controlCARD Evaluation Module User's Guide
- Texas Instruments Code Composer Studio

5.2 Compatible Sitara™ MCU AM2x EVMs

This Ethernet add-on board is compatible with the following EVMs:

TMDSCNCD263P

5.3 Other TI Components Used in This Design

This Ethernet Add-on board uses various other TI components for the functions. A consolidated list of these components with links to the TI product pages is shown below.

- TPD2E2U06-Q1 Automotive Dual 1.5-pF, 5.5-V, ±25-kV ESD protection diode for USB & High Speed Interfaces
- TLV755P 500-mA high-PSRR low-IQ low-dropout voltage regulator with enable

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 - 2.3 Tl's sole liability shall be at its option to repair or replace EVMs that fail to conform to the warranty set forth above, or credit User's account for such EVM. Tl's liability under this warranty shall be limited to EVMs that are returned during the warranty period to the address designated by Tl and that are determined by Tl not to conform to such warranty. If Tl elects to repair or replace such EVM, Tl shall have a reasonable time to repair such EVM or provide replacements. Repaired EVMs shall be warranted for the remainder of the original warranty period. Replaced EVMs shall be warranted for a new full ninety (90) day warranty period.

WARNING

Evaluation Kits are intended solely for use by technically qualified, professional electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems, and subsystems.

User shall operate the Evaluation Kit within TI's recommended guidelines and any applicable legal or environmental requirements as well as reasonable and customary safeguards. Failure to set up and/or operate the Evaluation Kit within TI's recommended guidelines may result in personal injury or death or property damage. Proper set up entails following TI's instructions for electrical ratings of interface circuits such as input, output and electrical loads.

NOTE:

EXPOSURE TO ELECTROSTATIC DISCHARGE (ESD) MAY CAUSE DEGREDATION OR FAILURE OF THE EVALUATION KIT; TI RECOMMENDS STORAGE OF THE EVALUATION KIT IN A PROTECTIVE ESD BAG.

3 Regulatory Notices:

3.1 United States

3.1.1 Notice applicable to EVMs not FCC-Approved:

FCC NOTICE: This kit is designed to allow product developers to evaluate electronic components, circuitry, or software associated with the kit to determine whether to incorporate such items in a finished product and software developers to write software applications for use with the end product. This kit is not a finished product and when assembled may not be resold or otherwise marketed unless all required FCC equipment authorizations are first obtained. Operation is subject to the condition that this product not cause harmful interference to licensed radio stations and that this product accept harmful interference. Unless the assembled kit is designed to operate under part 15, part 18 or part 95 of this chapter, the operator of the kit must operate under the authority of an FCC license holder or must secure an experimental authorization under part 5 of this chapter.

3.1.2 For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant:

CAUTION

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

3.2 Canada

3.2.1 For EVMs issued with an Industry Canada Certificate of Conformance to RSS-210 or RSS-247

Concerning EVMs Including Radio Transmitters:

This device complies with Industry Canada license-exempt RSSs. Operation is subject to the following two conditions:

(1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concernant les EVMs avec appareils radio:

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concerning EVMs Including Detachable Antennas:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication. This radio transmitter has been approved by Industry Canada to operate with the antenna types lated in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante. Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur

3.3 Japan

- 3.3.1 Notice for EVMs delivered in Japan: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_01.page 日本国内に輸入される評価用キット、ボードについては、次のところをご覧ください。
 - https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-delivered-in-japan.html
- 3.3.2 Notice for Users of EVMs Considered "Radio Frequency Products" in Japan: EVMs entering Japan may not be certified by TI as conforming to Technical Regulations of Radio Law of Japan.

If User uses EVMs in Japan, not certified to Technical Regulations of Radio Law of Japan, User is required to follow the instructions set forth by Radio Law of Japan, which includes, but is not limited to, the instructions below with respect to EVMs (which for the avoidance of doubt are stated strictly for convenience and should be verified by User):

- 1. Use EVMs in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
- 3. Use of EVMs only after User obtains the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to EVMs. Also, do not transfer EVMs, unless User gives the same notice above to the transferee. Please note that if User does not follow the instructions above. User will be subject to penalties of Radio Law of Japan.

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- 3.3.3 Notice for EVMs for Power Line Communication: Please see http://www.tij.co.jp/lsds/ti_ja/general/eStore/notice_02.page 電力線搬送波通信についての開発キットをお使いになる際の注意事項については、次のところをご覧ください。https://www.ti.com/ja-jp/legal/notice-for-evaluation-kits-for-power-line-communication.html
- 3.4 European Union
 - 3.4.1 For EVMs subject to EU Directive 2014/30/EU (Electromagnetic Compatibility Directive):

This is a class A product intended for use in environments other than domestic environments that are connected to a low-voltage power-supply network that supplies buildings used for domestic purposes. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

- 4 EVM Use Restrictions and Warnings:
 - 4.1 EVMS ARE NOT FOR USE IN FUNCTIONAL SAFETY AND/OR SAFETY CRITICAL EVALUATIONS, INCLUDING BUT NOT LIMITED TO EVALUATIONS OF LIFE SUPPORT APPLICATIONS.
 - 4.2 User must read and apply the user guide and other available documentation provided by TI regarding the EVM prior to handling or using the EVM, including without limitation any warning or restriction notices. The notices contain important safety information related to, for example, temperatures and voltages.
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