# Technical Article Improve Printed Circuit Board (PCB) Quality with 3D Inspection

In my two decades with DLP Products, I've seen a wide variety of applications benefit from the use of DLP technology. The migration from 2D Automated Optical Inspection (AOI) to 3D AOI for printed circuit board manufacturing and inspection is no exception. The continuing miniaturization of consumer electronics offers new opportunities to improve inspection methods, like 3D AOI tactics, that can equate to manufacturing cost savings. If you are an AOI equipment designer, you may not know that you can leverage DLP solutions to help create the robust, high-speed, high resolution, high precision 3D imaging system necessary for successful inspection of PCB solder paste, solder joints, and accurate component placements.

#### The Need for 3D Inspection

3D AOI and Solder Paste Inspection (SPI) methods can help detect defects early in the assembly process, and combined with final inspection, help assure quality and reliability in the PCB manufacturing process with lower manufacturing costs.

As consumer electronics continue to reduce in size, it drives component sizes to shrink and PCB land geometries to increase in density. These properties may require additional 3D AOI or SPI capabilities to adequately assure final product quality.



3D AOI and SPI provide benefits at multiple stations in a PCB assembly factory including:

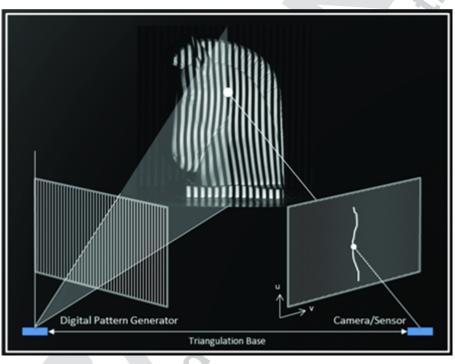
TEXAS INSTRUMENTS



- 1. Inline after solder paste application: Detection and correction of solder paste volume and shape deficiencies earlier in the line prevents expensive rework later.
- 2. Inline after component placement: Verification of component installation and proper orientation helps prevent rework later.
- 3. Inline at final inspection: Verifies all components are installed properly, components have not moved, component leads have not lifted, and all solder joints have proper amounts of solder, wicking without bridging, shorts, or opens.
- 4. Offline inspection: Stand-alone unit to inspect reworked or manually assembled boards.

#### How Does 3D AOI Work?

Many 3D AOI systems use DLP technology to quickly project up to 400 pre-determined patterns across a small field-of-view (FOV) on the target PCB. Monochrome or color cameras mounted to the side capture each projected pattern as it is deformed by the z-axis surface variation of the PCB components. The 3D shapes of the components are then derived through geometric triangulation analysis. The result is a 3D point cloud that a processor can compare to a reference model of "desired" physical attributes. The processor can then determine if any FOV contains attributes outside the expected norm, logs them as a fail condition, and provides the user the fail condition and location. Using this type of 3D structured light capability provides flexible, high precision 3D scanning to enable high speed inline inspection.



## **3D AOI Solutions Using DLP Technology**

DLP technology brings many enhancements to AOI applications. For example, the DLP6500 digital micromirror device (DMD) is a 1920 x 1080 array of individually controlled mirrors which provides the high resolution, fast pattern rates, and programmability necessary for next generation 3D AOI applications. When coupled with the DLPC900 Controller, the two provide the following benefits:

- 1. 8-bit phase shift pattern rates to nearly 250 patterns per second, enabling high speed data capture for incredibly fast PCB 3D AOI scan times.
- 2. Programmable pattern selection and re-ordering ensure the "right" pattern gets applied to extract the most information for any FOV.
- 3. DMD multi-wavelength support enabling multi-color AOI systems for optimized reflectivity and use with either monochrome or color cameras.
- 4. Programmable pattern duration settings enable the software to control the "on-time" for each pattern to optimize the amount of light being reflected from the object.



5. Triggers in and out allow the DLPC900 to trigger cameras for capture or to be triggered by cameras, processors, or other hardware.

DLP Products has a proven track record of MEMS technology leadership spanning more than 20 years. Multiple DMD resolutions and sizes support tiered AOI product positioning based on various scan speeds, resolutions, and price. In addition, our DLP Design Network provides expertise in designing and manufacturing solutions based on DLP technology in order to make product development easier and help customers accelerate their design process.

### **Related Resources**

- Learn about machine vision technology using DLP technology
- Discover the TI Designs for High Resolution 3D scanner based on the DLP LightCrafter 6500 EVM and accurate point cloud generation for 3D machine vision applications using DLP Technology
- Ask your design questions to engineers on our E2E<sup>™</sup> community

3

# IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2023, Texas Instruments Incorporated

# IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2023, Texas Instruments Incorporated