



**RevB:**

- 1) adjusted loop & RC snubber R7
- 2) added additional input caps externally
- 3) added C200

**ASSEMBLY:**

- 1) C6, C21, D1, J5, R23, R13 OPEN
- 2) L100 mounted externally, scratch trace at pad C6
- 3) pull up R100 mounted externally

**NOTES:**

- 1) R18 for test purposes only
- 2) R2, R3 causes 50uA bias each
- 3) built on PCB PMP5638RevA

TEXAS INSTRUMENTS		
Title TPS40170, 5V Sync Buck		
Size <b>B</b>	Number PMP7165	Rev A
Date 6/18/2012	Drawn by B.Geck	
Engineer B.Geck	Filename PMP7165RevB.sch	Sheet 1 of 1

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