Layer Name Thickness Constant Board Layer Stack Material Top Overlay 0.40mil Top Solder Top Layer Copper 1.40mil | Dielectric1 FR-4 High TG 10.00mil 4.2 1.40mil **FABRICATION NOTES** Signal Layer 1 | Copper Dielectric 3 FR-4 High TG 36.00mil 4.2 1. Top Layer contains 90 ohm differential using 8.5 mil trace and 5 mil spacing. Signal Layer 2 Copper 1.40mil Dielectric 2 FR-4 High TG 10.00mil 2. Top Layer contains 50 ohm controlled impedance using 18 mil trace. 4.2 Bottom Layer Copper 1.40mil Bottom Solder 0.40mil Bottom Overlay DESIGN INFORMATION MIN. TRACK WIDTH: 8.5 MIL MIN. CLEARANCE: <u>0.2</u> mm MIN. VIA PAD SIZE: \_24\_ MIL MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES+/- 3 MIL FR-408 X FR-4 High Tg OTHER THICKNESS: X 62 MIL (1.6mm) +/-10% OTHER\_ X ANSI IPC-6012 TYPE 3 CLASS 2 OTHER +/-BOW & TWIST: X ANSI IPC-6012 TYPE 3 CLASS 2 OTHER +/-REFERENCE: X AS SHOWN X NC\_DRILL FILES PTH MIN COPPER THICKNESS: X 1MIL OTHER BOARD FINISH: X TOP X BOTTOM SILKSCREEN: SILKSCREEN COLOR: X WHITE OTHER SOLDER RESIST COLOR: X GREEN BLUE OTHER SURFACE FINISH: X IMMERSION GOLD (ENIG) ENEPIG Finished Hole Size | Plated | Hole Type IMM. TIN/SILVER OR EQUIV OTHER \_ ARRAY/PANEL: CUT AND TRIM PER MECH LAYER 1 N.C. ROUTE X V. SCORE CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS
TO MEET OR EXCEED THE REQUIREMENTS OF:  $\bowtie$ X ANSI IPC-A-600F CLASS -> 1 X 2 3X UL 94V-0 X RoHS OTHER PER ORDER ADDITIONAL REQUIREMENTS:
MICROSECTION: YES BARE BOARD ELEC. TEST: NONE X REQUIRED PER ORDER Ħ MANUFACTURER'S UL: RAIL METAL X SILK M M M TEXAS INSTRUMENTS PROJECT TITLE: CAN to Wi-Fi Gateway DESIGNED FOR: 2700.00mi Public Release ISE4040\_TIDA00380\_CANtoWiFi.PcbDoc Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification ALL ARTWORK VIEWED FROM TOP SIDE BOARD #: ISE4040 REV: E2 SUN REU: Not In VersionControl r any information contained therein. TI and/or its licensors do not warrant that this design will meet M. Knapp Krypton Solution LLC the specifications, will be suitable for your application or fit for any particular purpose, or will operate in TID #: TIDA-00380 ALTIUM DESIGNER VERSION: an implementation. TI and∕or its licensors do not warrant that the design is production worthy. You should SCALE: 1.00 14.3.18.45973 PLOT NAME = Fabrication Drawing GENERATED : 9/22/2015 10:26:52 AM TEXAS INSTRUMENTS completely validate and test your design implementation to confirm the system functionality for your application. 2 3

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