

	1	2	3		4	]
A		2 520mA peak / 435mA rms L1 6.8µH U1 TPS61071-Q1 6 VBAT SW 1	3 5.0V @ 250mA	Revision A B C	4 Revision History Notes * Paper design * Changed Boost Converter, Layout * Changed Boost Converter, Built & Tested	A
В	$\begin{array}{c} C1 \\ C1 \\ 22\mu F \\ 0V \\ \overline{GND} \\ 0FF \\ \overline{GND} \\ G$	$\begin{array}{c} 3 \\ \hline \\ 2 \\ \hline \\ \\ \\ \\$	GND U U U U U U U U U U U U U			в
С		U3 TLV71325-Q1 OUT NC GND C GND C GND C C7 UF C7 UF C7 UF C7 UF C7 UF C7 UF C7 UF C7 UF C7 C7 C7 C7 C7 C7 C7 C7 C7 C7 C7 C7 C7	$J_3$ $T_1$ $V_{IN}$ $T_N$	U2 V71310-Q1 OL M	$NC = \frac{4}{1\mu F} = \frac{2}{1\mu F} GND$	С
D	Texas Instruments and/or its licensors do not warrant the accuration for the licensors do not warrant that this de particular purpose, or will operate in an implementation. Texas In You should completely validate and test your design implementation.	cy or completeness of this specification or any information contained the sign will meet the specifications, will be suitable for your application or fit istruments and/or its licensors do not warrant that the design is productio tion to confirm the system functionality for your application.	Orderable: EVM_orderable Designed for: Public GND Orderable: EVM_orderable Designed for: Public TID #: TID Project Title: Automa rein. Number: PMP11757 Rev: C Sheet Title: to rany (SVR Rev: Version control disabled Assembly Variant: If	otive Power Solutio No Variations] C - Boost and LDO	Mod. Date: 12/16/2015 on Sheet: 2 of 2	

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