

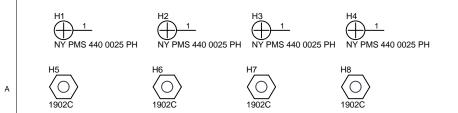
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			L2		V <u>QU</u> T	54\ ●	′out @	2.5A
NPC22 DNP	DNPC23 DNP	DNPC24 DNP	• 1uH	\mathbf{T}	C20 80V 47uF	VOUT 54V @ 2 TP8 GND	VOUT .5A TP4 GND	

ə!	Designed for: Public Relea	se	Mod. Date:	11/29/2017	
4	Project Title: LM5122 Boos	st			TEXAS
Rev: A	Sheet Title:				
ontrol disabled	Assembly Variant: 001		She	et: 1 of 2	
	File: PMP21274_REVA.Sc	hDoc		Size: B	http://www.ti.com
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DNP FID1 DNP DNP FID2 FID3

> PCB Number: PMP21274 PCB Rev: A

В

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Variant/Label Table			
Variant	Label Text		
001			

ZZ1 Label Assembly Note This Assembly Note is for PCB labels only

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ZZ2 Assembly Note This Assembly Note will show in the PcbDoc and associated outputs

ZZ3 Assembly Note This Assembly Note will show in the PcbDoc and associated outputs

ZZ4 Assembly Note This Assembly Note will show in the PcbDoc and associated outputs

Orderable: ChangeMe!	Designed for: Public Relea	se Mod. D	Date: 11/15/2017	
TID #: PMP21274 Project Title: LM5122 Boost			TEXAS	
Number: PMP21274 Rev: A	Sheet Title:			
SVN Rev: Version control disabled	Assembly Variant: 001		Sheet: 2 of 2	In STREIMERTS
Drawn By:	File: PMP21274_REVA_Hardware.SchDoc Size: B		http://www.ti.com	
Engineer: Xinyu Dai	Contact: http://www.ti.com/support		© Texas Instruments 2017	
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licensors do not warrant that the desig	n is production worthy. You should completely validate and test your des	ign implementation to confirm the system functionality for your application	on. Engin
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