

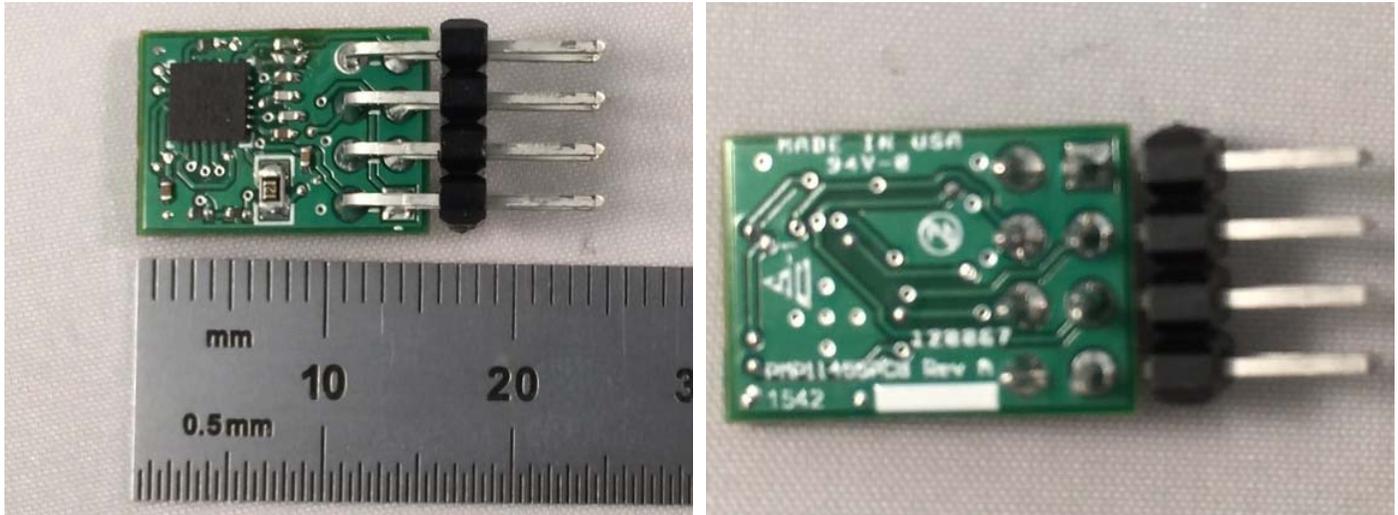
PMP11455 Rev A
Daughter Card for 5V/12V/20V
USB Type C Systems
Test Results

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1 Photos

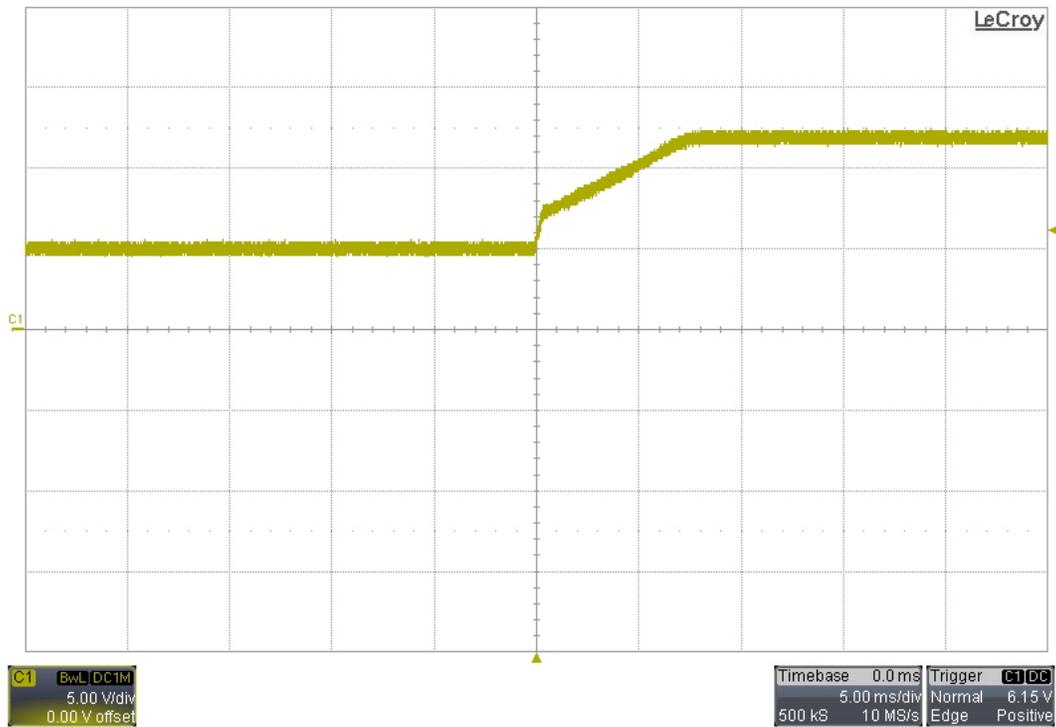
The photographs below show the PMP11455 Rev A prototype assembly.

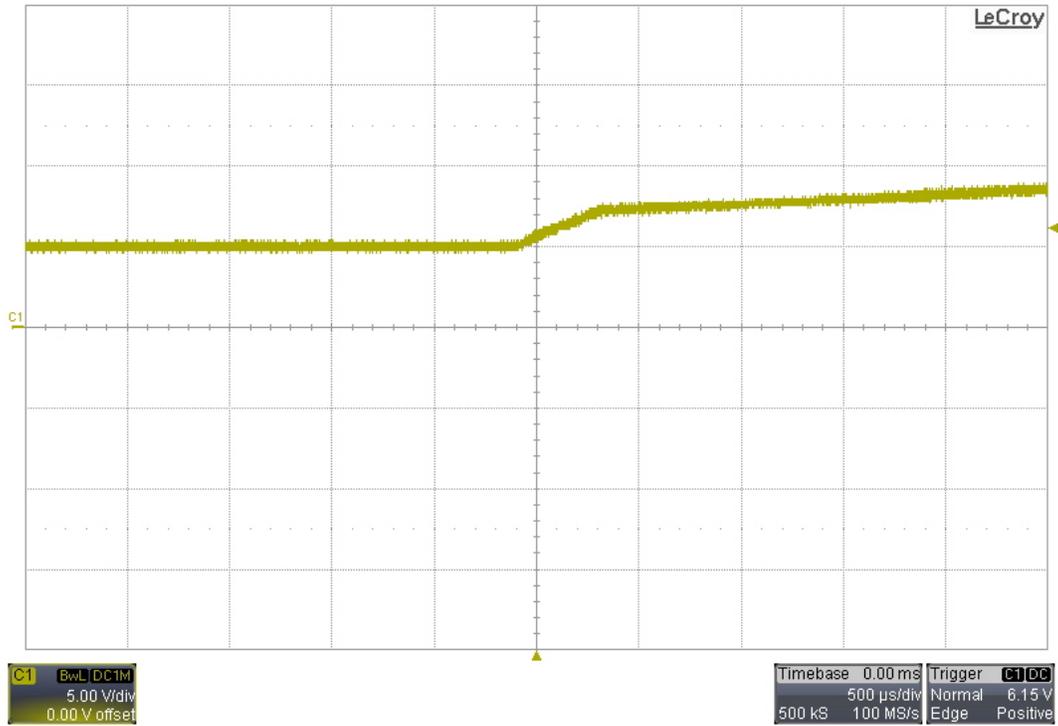


2 Output Voltage Transitions

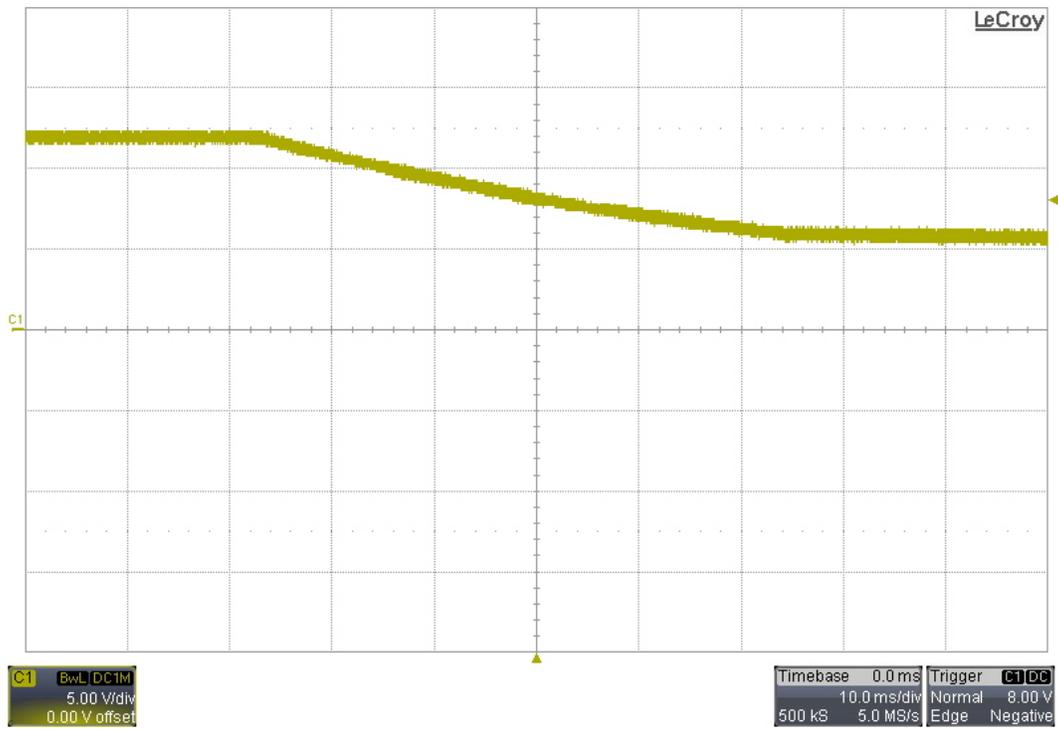
The following screenshots below demonstrate the transitions between 5V, 12V, and 20V. No load is applied. Waveforms were measured using a PMP11451 mother board.

2.1 5V to 12V

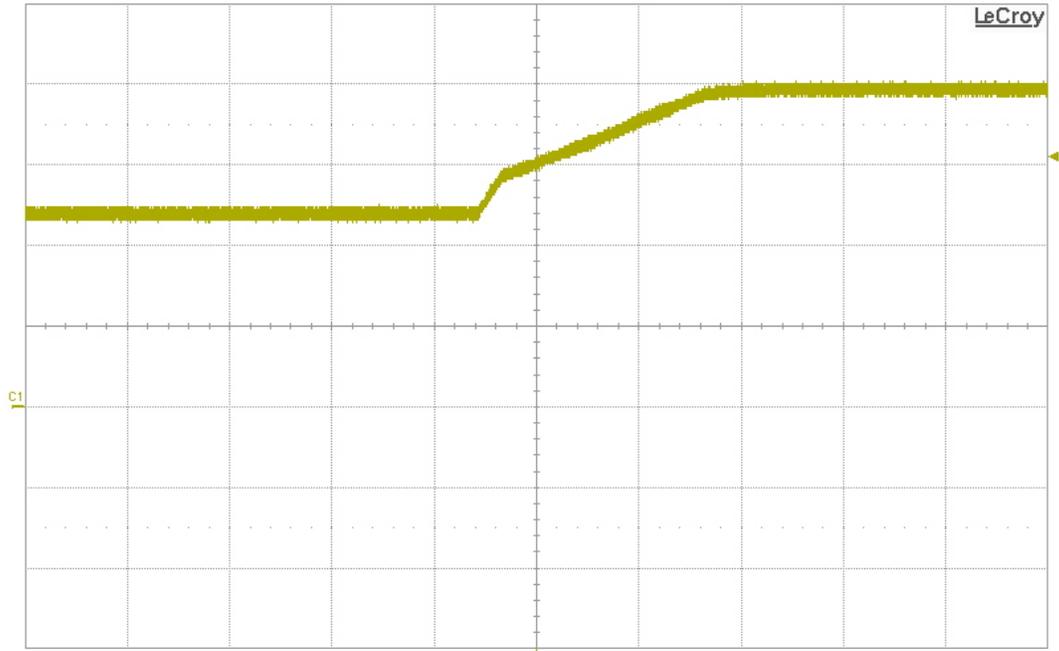




2.2 12V to 5V

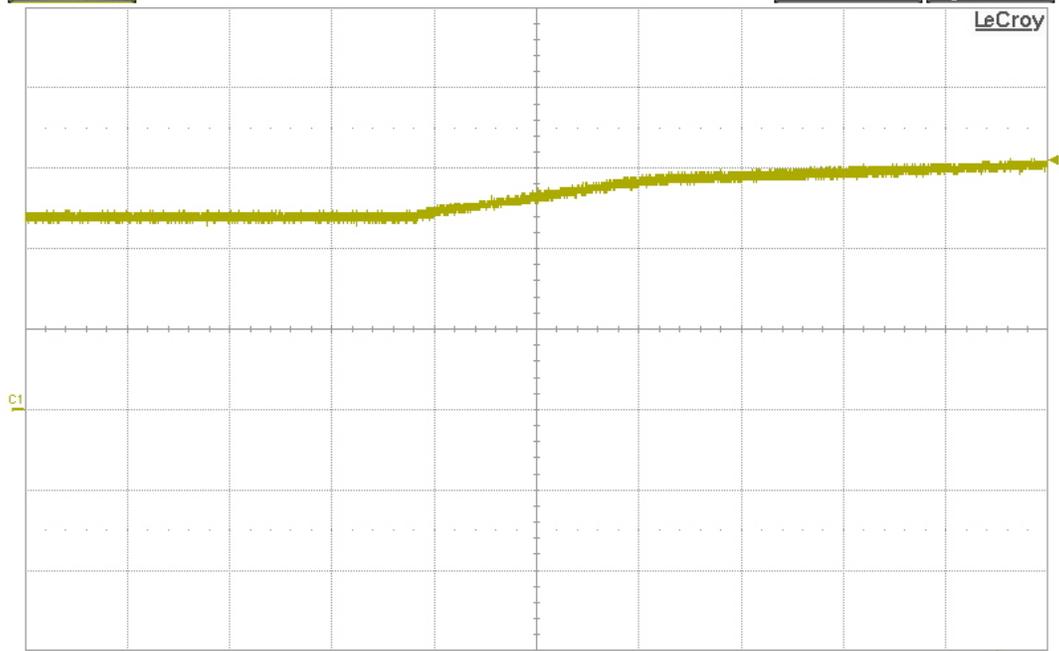


2.3 12V to 20V



C1 BwL DCTM
5.00 V/div
-5.00 V ofst

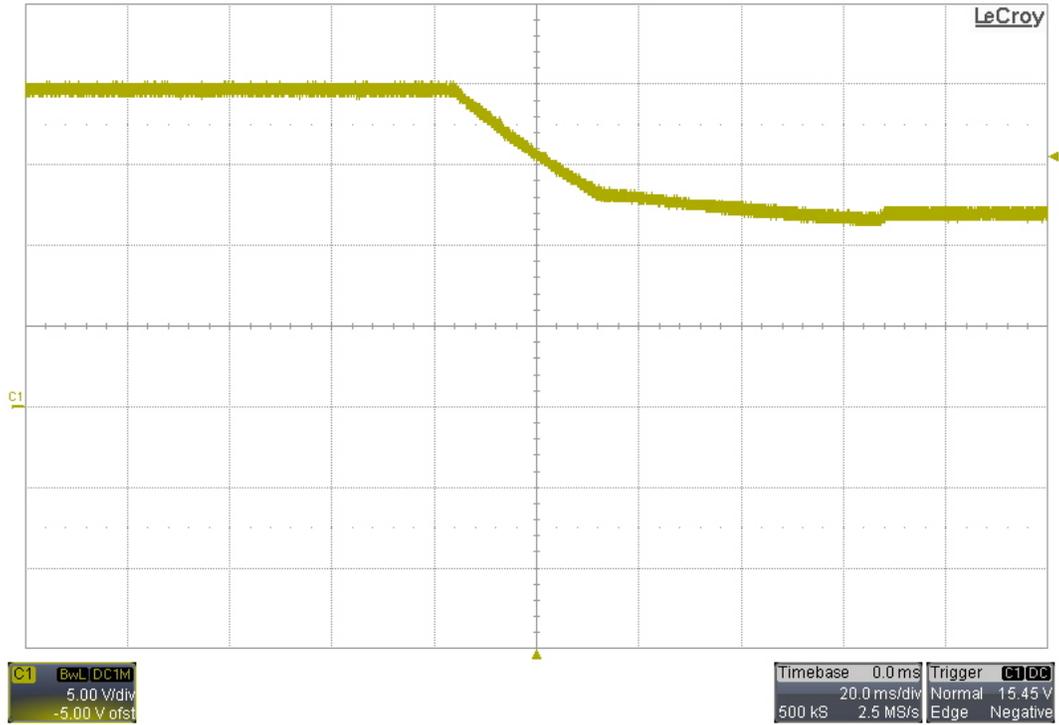
Timebase 0.00 ms Trigger C1 DC
2.00 ms/div Normal 15.45 V
500 kS 25 MS/s Edge Positive



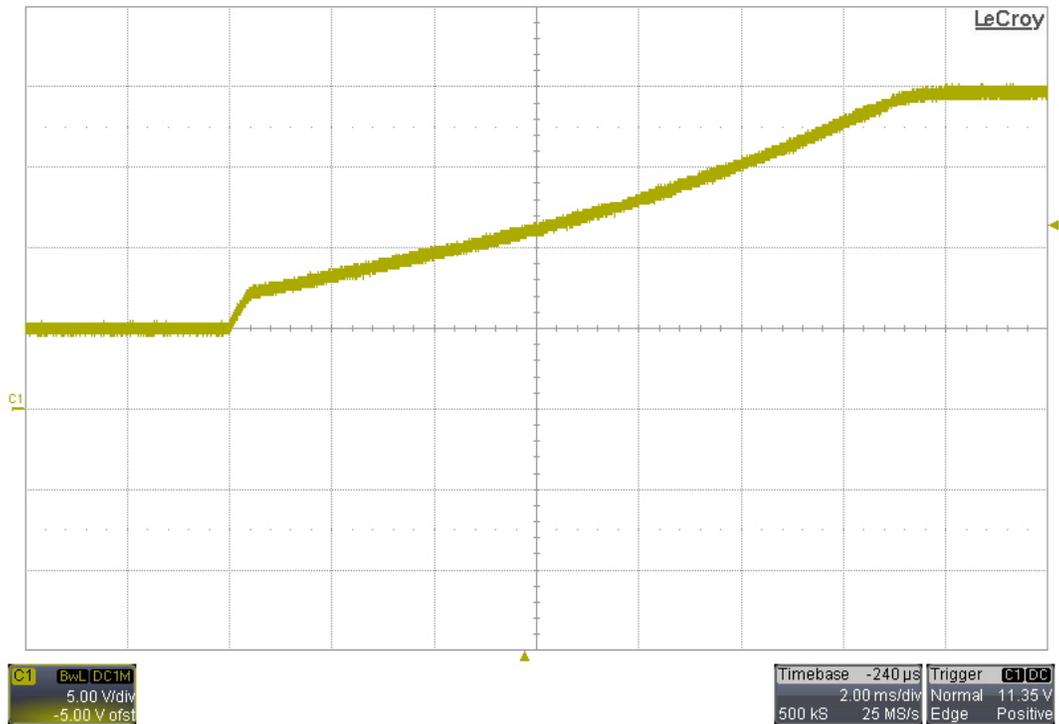
C1 BwL DCTM
5.00 V/div
-5.00 V ofst

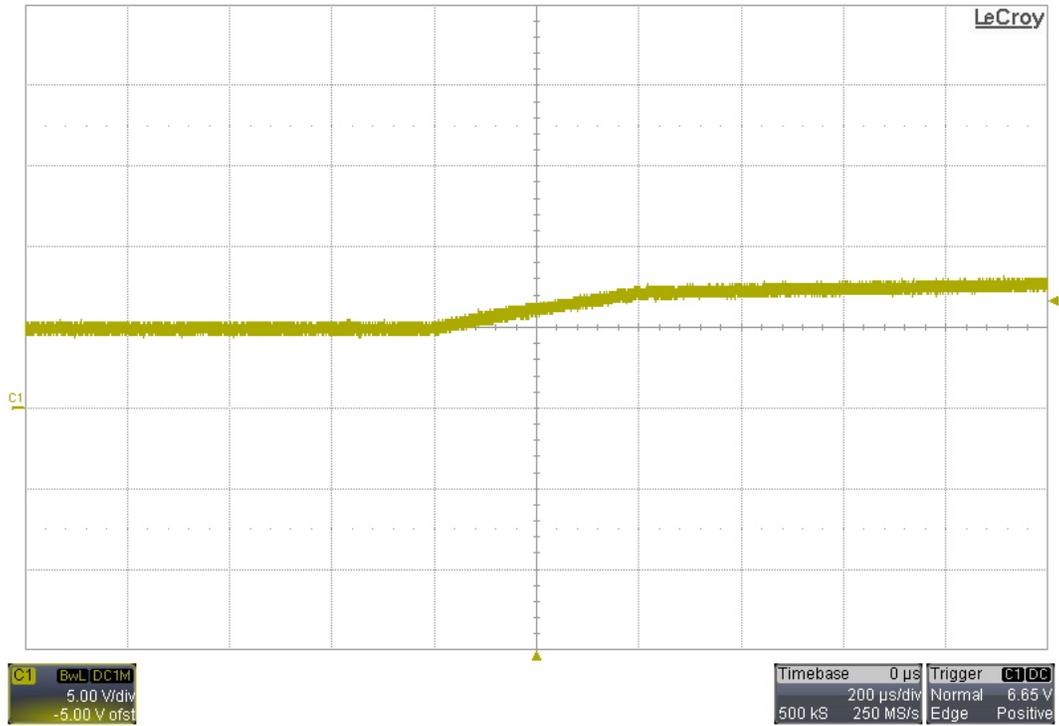
Timebase 900 μ s Trigger C1 DC
200 μ s/div Normal 15.45 V
500 kS 250 MS/s Edge Positive

2.4 20V to 12V

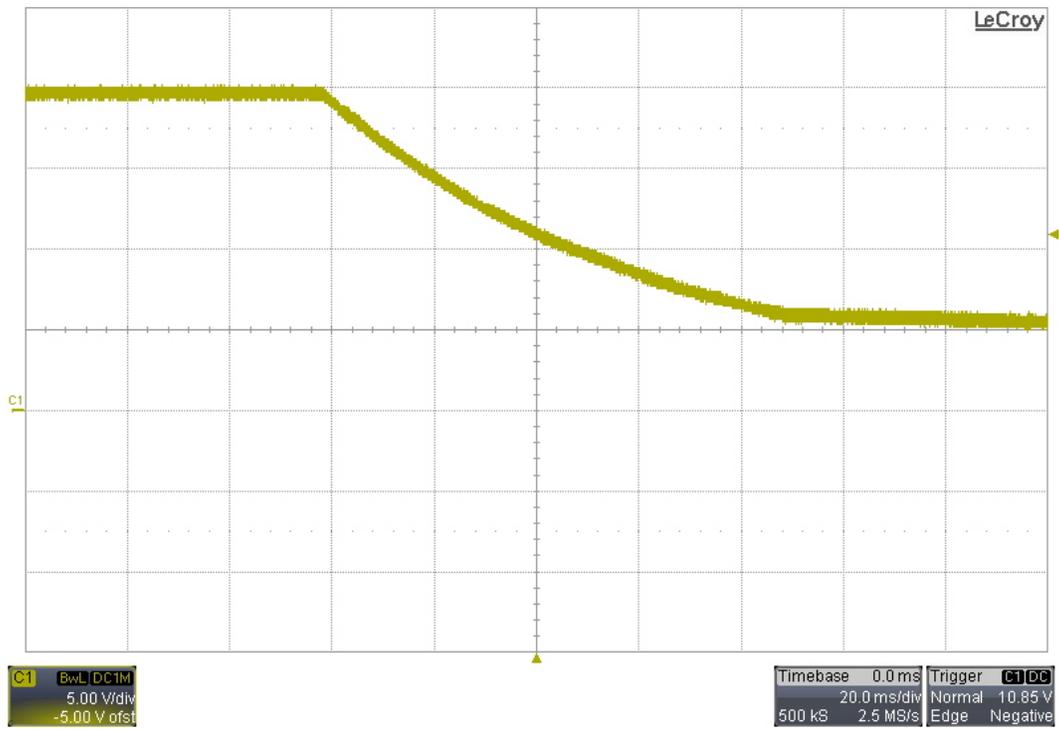


2.5 5V to 20V





2.6 20V to 5V



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