**description**

The SPIRIT Caller ID type I and II algorithm supports a wide range of standards and can be used in all applications where it is necessary to transmit the number of the caller to the subscriber.

**resource requirements**

<table>
<thead>
<tr>
<th>ALGORITHM</th>
<th>PEAK MIPS</th>
<th>PROGRAM MEMORY (KWORDS)</th>
<th>CONSTANT MEMORY (KWORDS)</th>
<th>DYNAMIC MEMORY (KWORDS)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CID Object</td>
<td>1.4</td>
<td>2.661</td>
<td>0.25</td>
<td>0.044</td>
</tr>
<tr>
<td>CID Message Parser</td>
<td>–</td>
<td>0.74</td>
<td>0.6</td>
<td>0.26</td>
</tr>
</tbody>
</table>

NOTE: CID Message Parser is optional, needed only for text parsing of received message. Dynamic memory of CID Parser includes message buffers. Program memory can significantly vary depending on the options used. The requirements are for standalone variants of this object. In actual systems it is possible to reduce memory requirements by sharing common resources with other algorithms provided by SPIRIT.

**availability**

The SPIRIT Caller ID I & II is available in four forms:

- eXpressDSP-compliant object code for TMS320C54x
- Fully functional eXpressDSP evaluation object at extremely low price
- Portable C code
- Assembly code

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.
availability (continued)

The algorithm is supplied with test environment and integration example code. Detailed product annotation and user guide documents describing testing procedures, interface and integration of this product, as well as PC-based and DSP-based (TI TMS320VC5406 EVM and TMS320VC5402 DSK) demos are available for evaluation upon request. To get additional information on CST software, go to www.spiritdsp.com/CST.

performance

<table>
<thead>
<tr>
<th>SPECIFICATION</th>
<th>VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supported standards</td>
<td>Bellcore (GR–30–CORE and SR–TSV–002476)</td>
</tr>
<tr>
<td></td>
<td>British Telecom (SIN227 and SIN242)</td>
</tr>
<tr>
<td></td>
<td>CCA TW/P&amp;E/312</td>
</tr>
<tr>
<td></td>
<td>ETSI (ETS 300 659, ETS 300 778)</td>
</tr>
<tr>
<td></td>
<td>Mercury Communications (MNR19)</td>
</tr>
<tr>
<td></td>
<td>Dutch Telecom (DTMF-Based)</td>
</tr>
<tr>
<td></td>
<td>China and Australia Standards</td>
</tr>
</tbody>
</table>

DUAL TONE SIGNAL (DT-AS) DETECTOR

- ‘Low’ tone nominal frequency: 2130 ± 30 Hz
- ‘High’ tone nominal frequency: 2750 ± 30 Hz
- Acceptable twist: −6...+6 dBc (can be configured)
- Acceptable levels: −2...−35 dB (can be configured)
- Minimum burst duration: 45 msec
- Maximum response time: 20 msec

FSK RECEIVER

- Transmission rate: 1200 ± 10% baud
- Mark (logical 1) frequency: 1188...1320 Hz
- Space (logical 0) frequency: 2068...2222 Hz
- Acceptable SNR: 10 dB
- Acceptable levels: 0...−45 dB (can be configured)

SUBSCRIBER ALERT SIGNAL (SAS) GENERATOR

- Output level: 0...−30 dB (can be configured)
- Frequency tolerance: better than 1 Hz
- Spurious level: <−60 dBc

TE ACKNOWLEDGMENT (T-ACK) GENERATOR

- Output level: 0...−30 dB (can be configured)
- Frequency tolerance: better than 1 Hz
- Spurious level: <−60 dBc
- Row/Column tones ration: 2 ± 0.5 dB
- Tone time: 60 msec
- Pause time: 40 msec
IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third–party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Mailing Address:

Texas Instruments
Post Office Box 655303
Dallas, Texas 75265

Copyright © 2003, Texas Instruments Incorporated