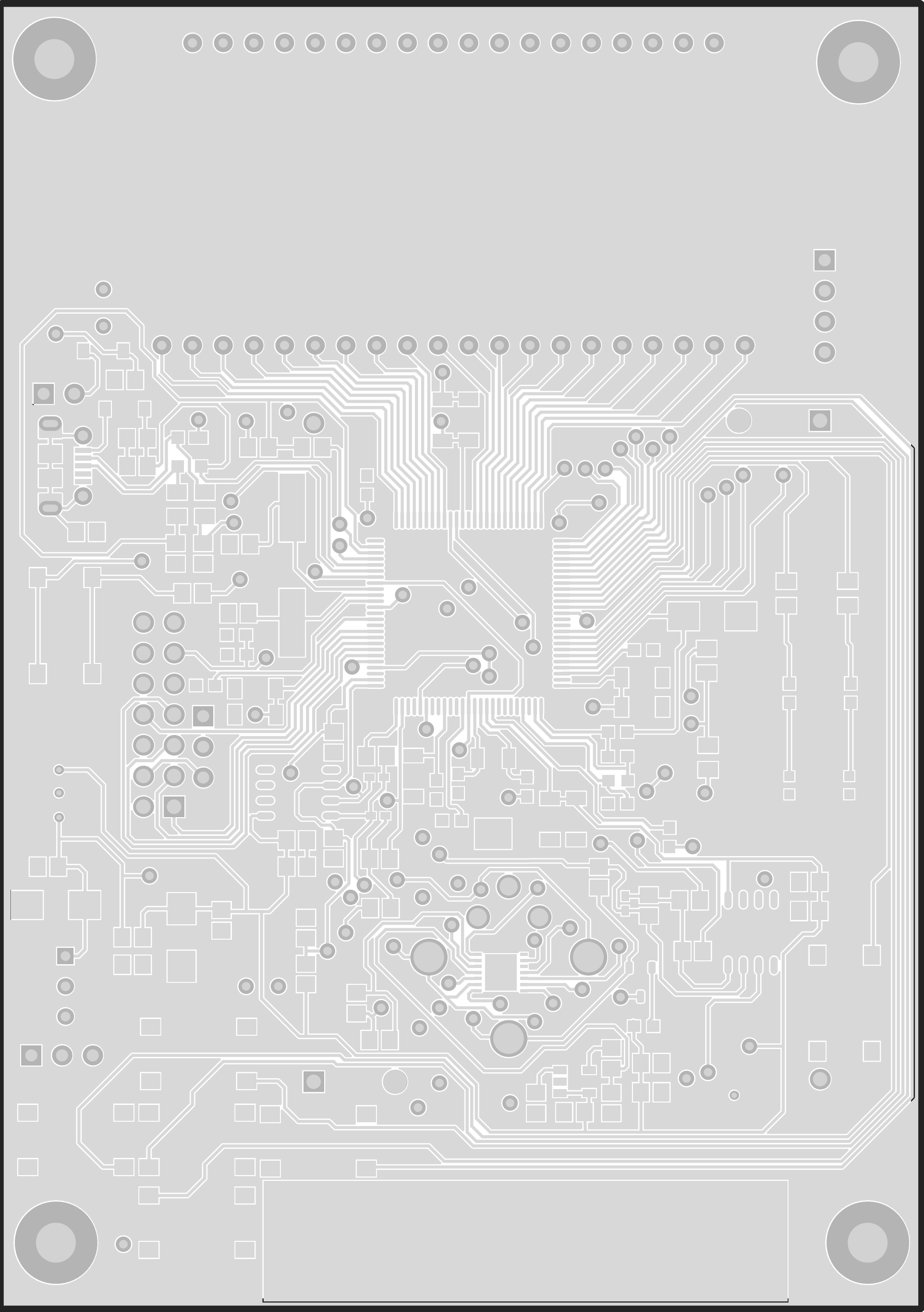


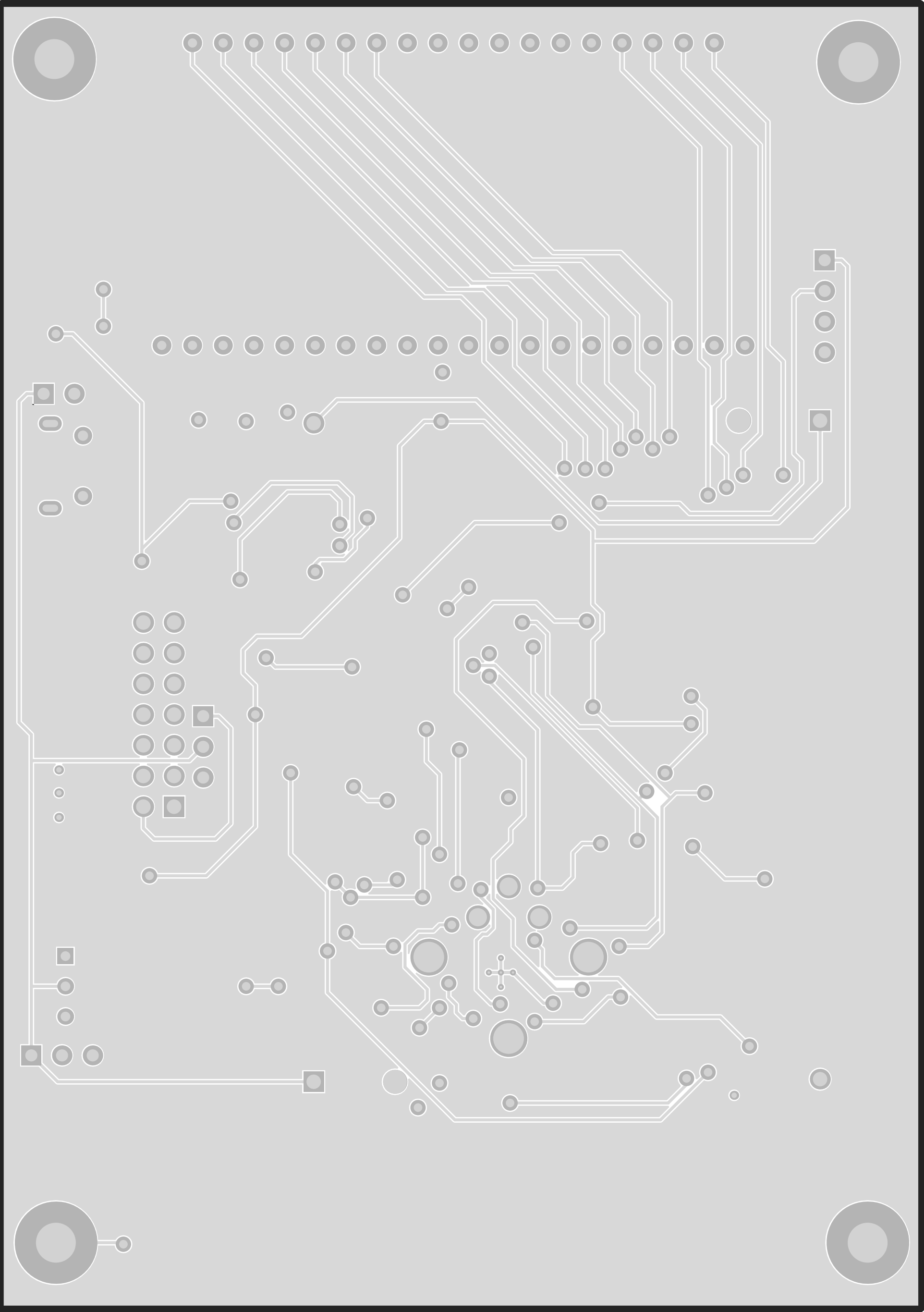
M156amp001155 Top



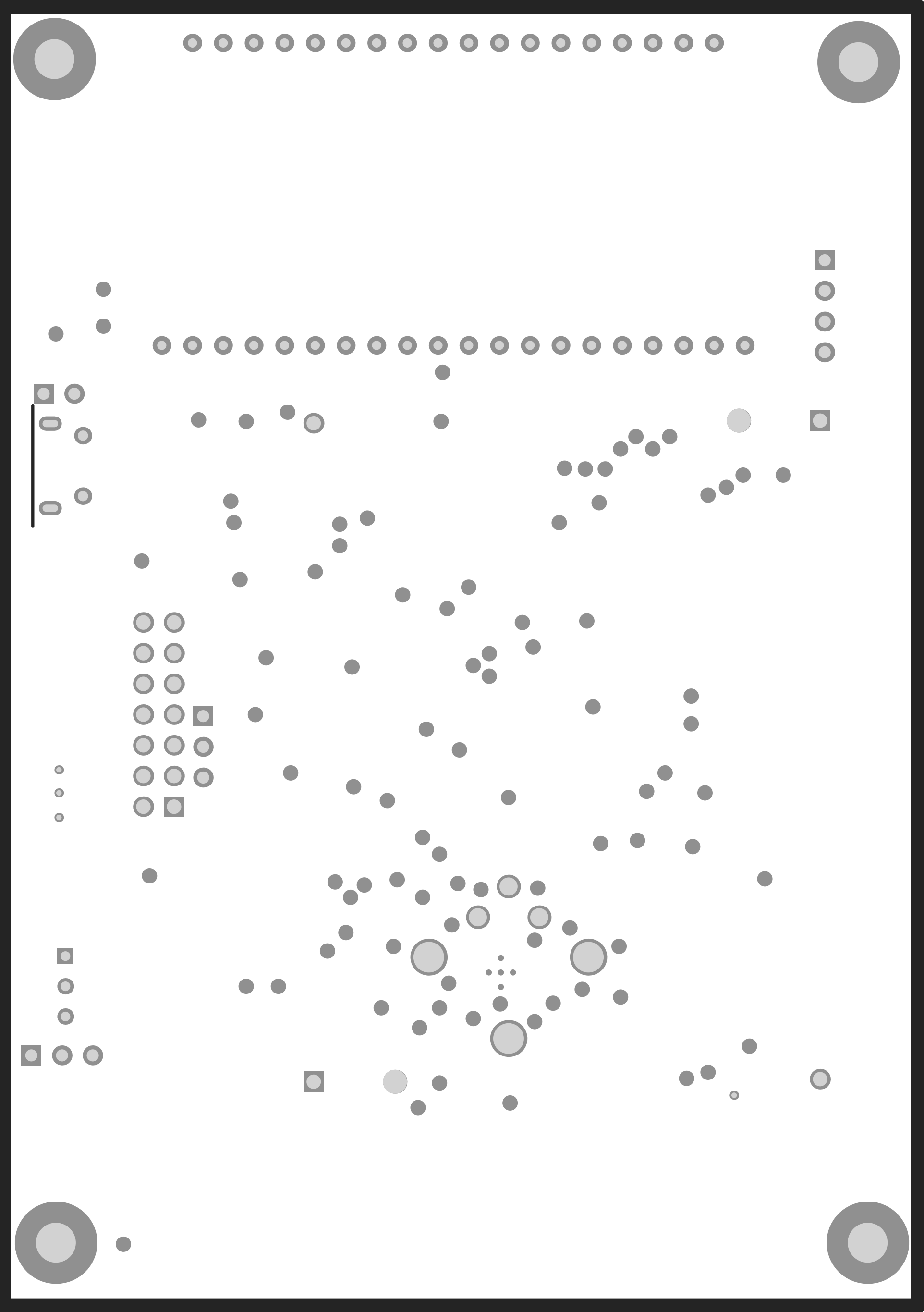
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/12/2016 10:20:55 AM		TEXAS INSTRUMENTS



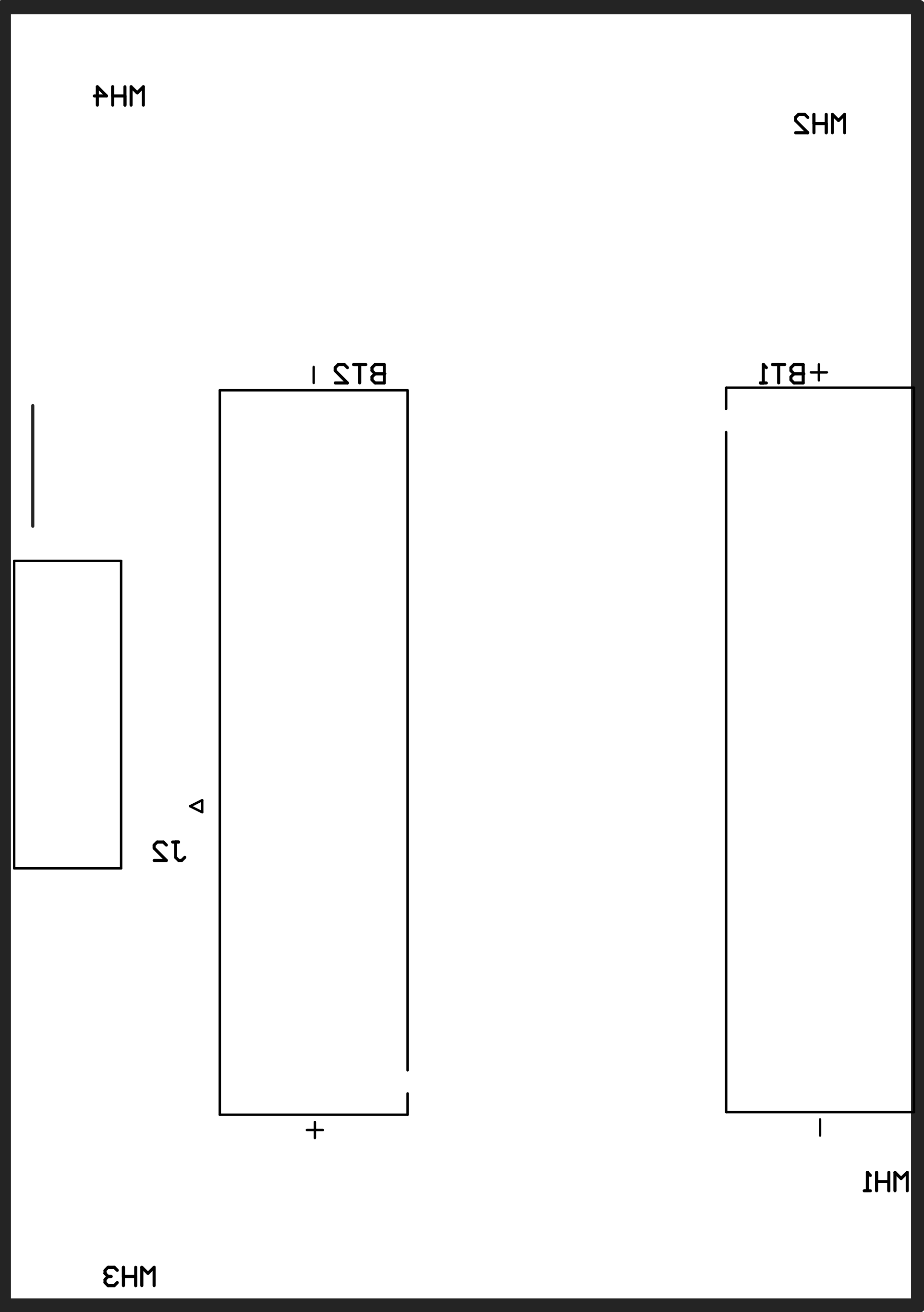
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/12/2016 10:20:56 AM		TEXAS INSTRUMENTS



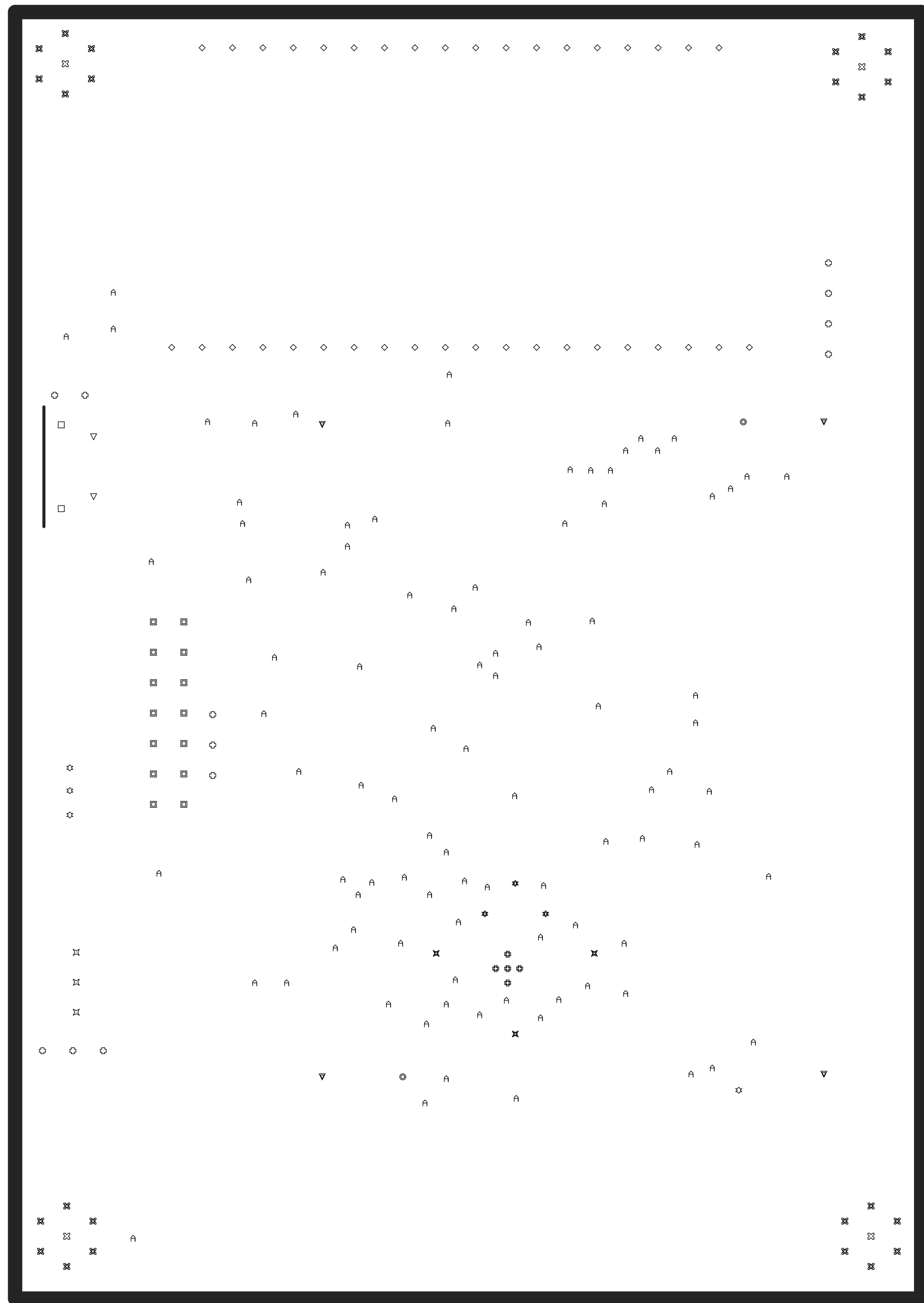
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/12/2016 10:20:57 AM		TEXAS INSTRUMENTS


















ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/12/2016 10:20:58 AM		TEXAS INSTRUMENTS



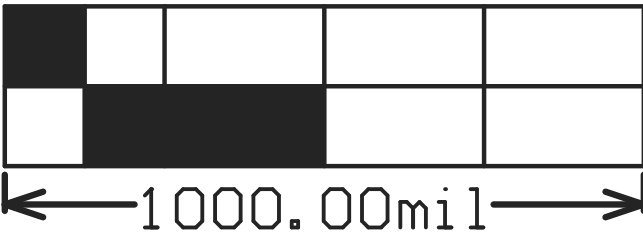
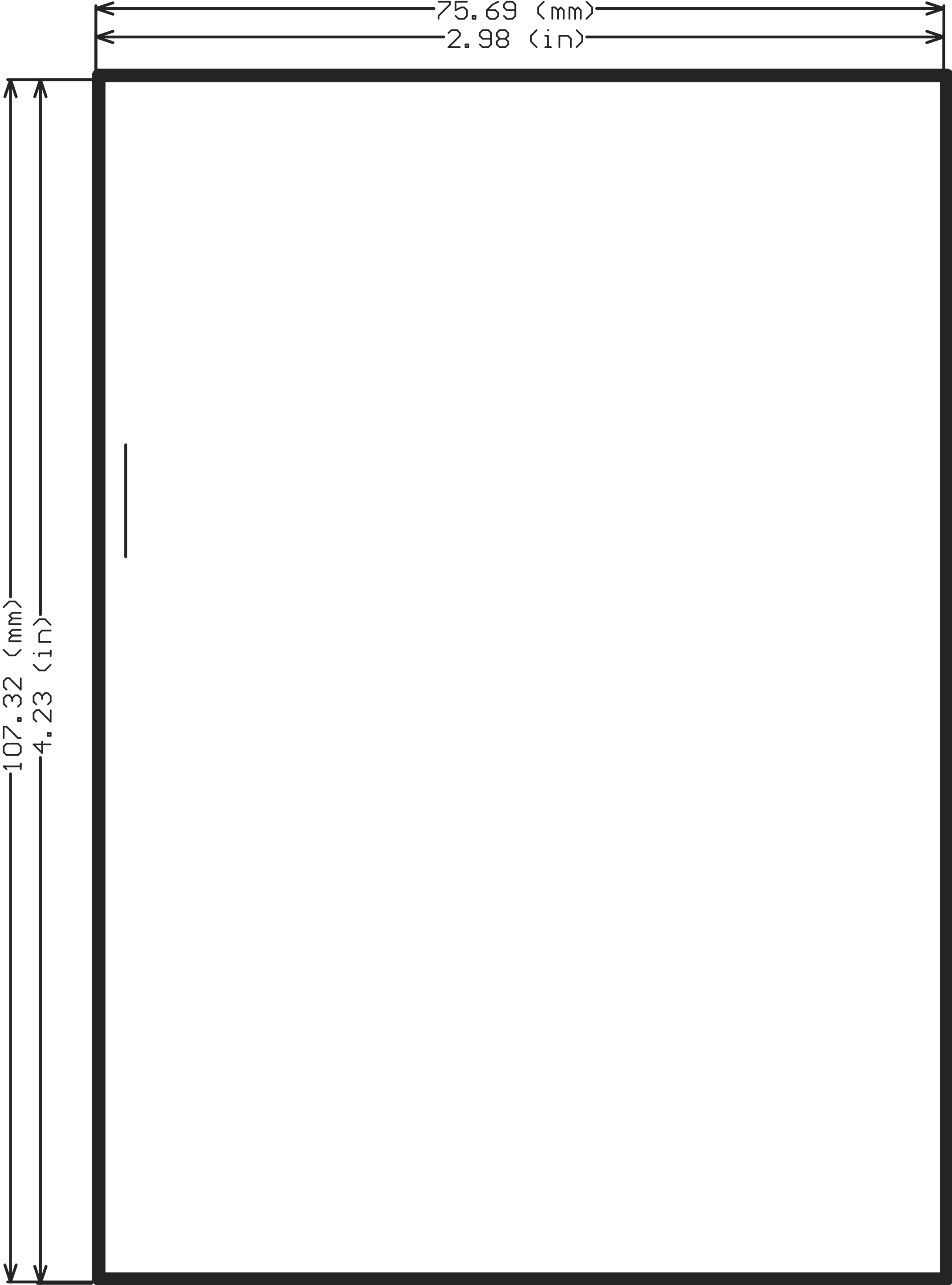
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/12/2016 10:20:59 AM		TEXAS INSTRUMENTS



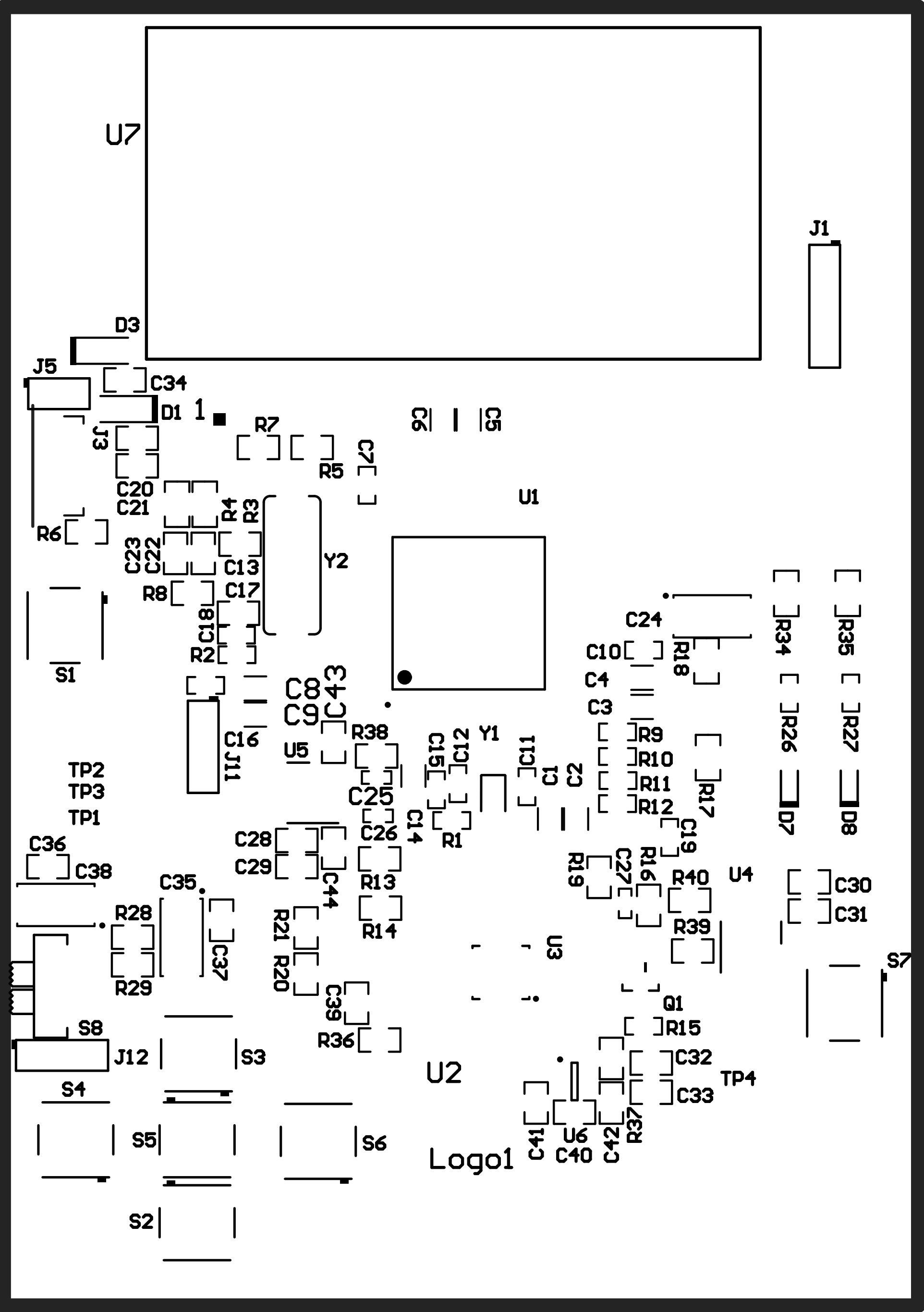
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

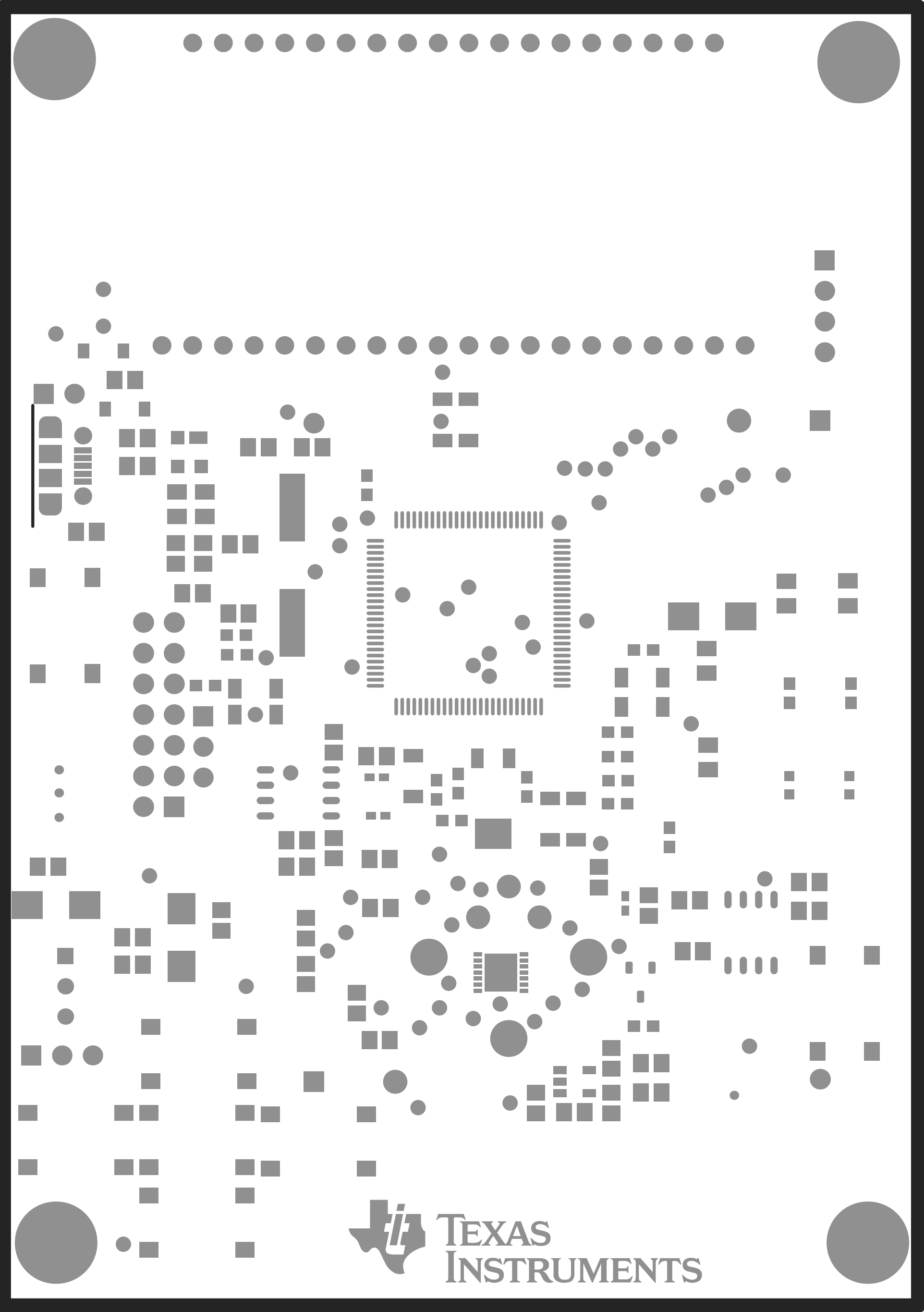
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing Mfr Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/12/2016 10:21:00 AM		TEXAS INSTRUMENTS



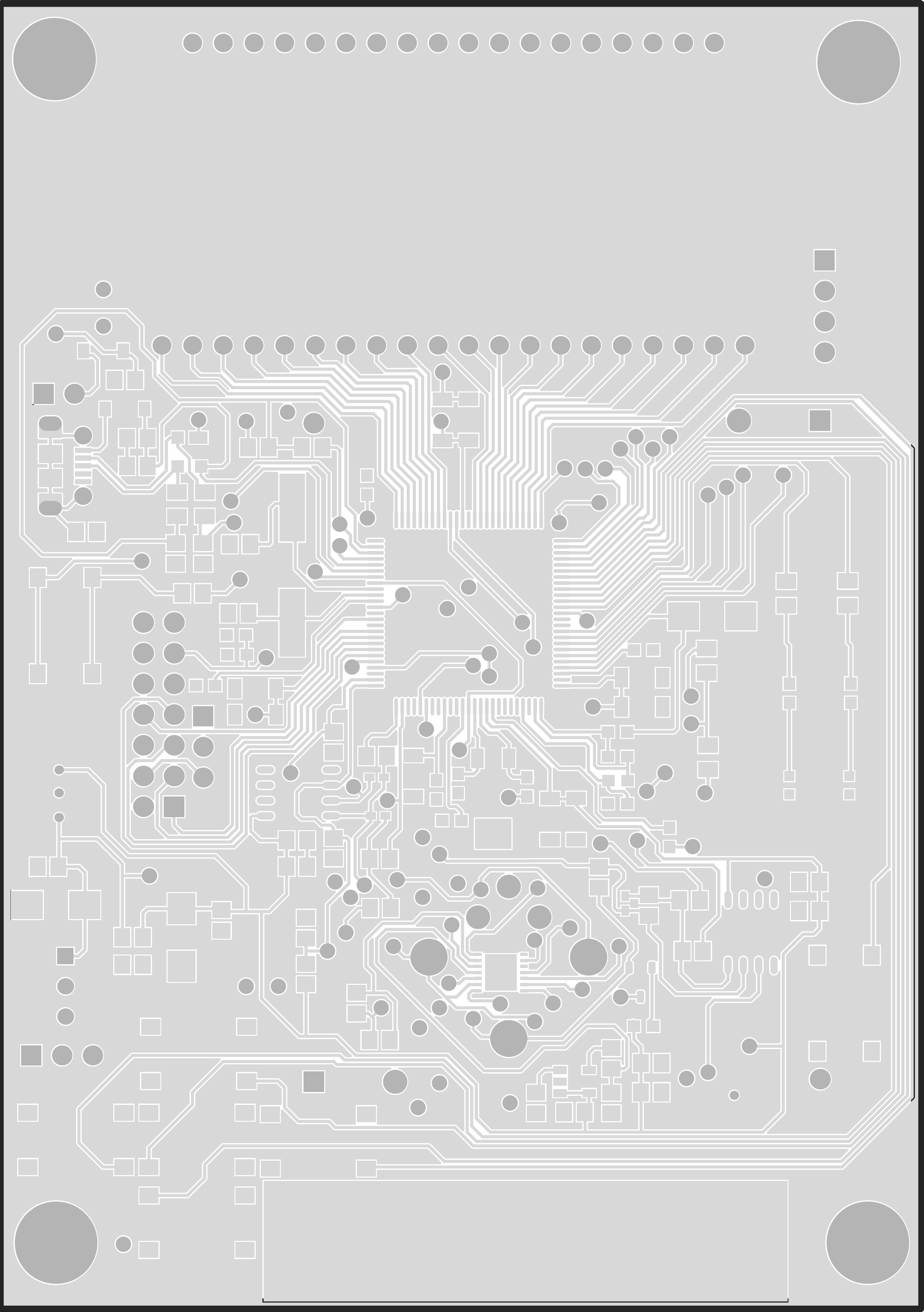
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/12/2016 10:21:02 AM		TEXAS INSTRUMENTS



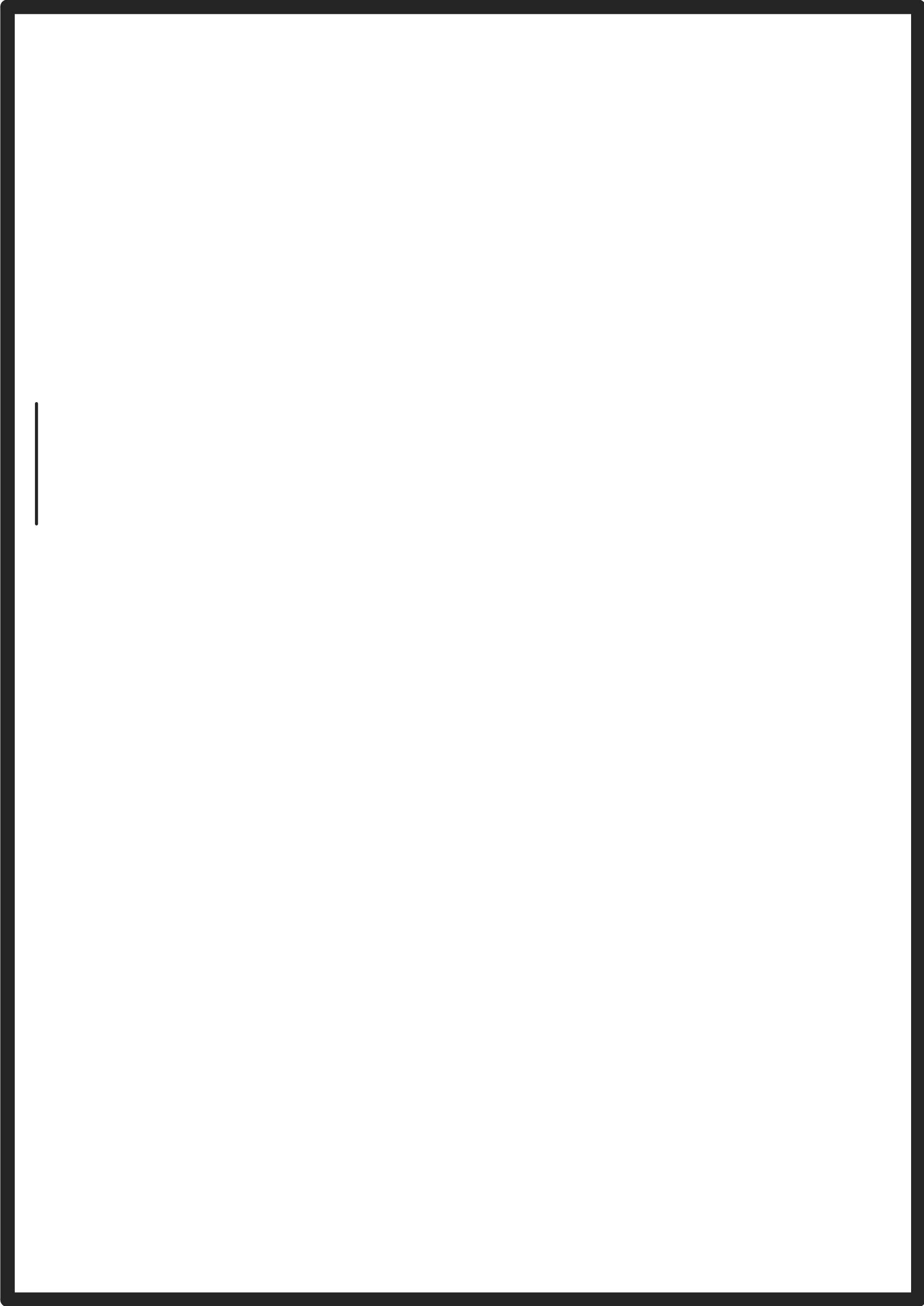
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/12/2016 10:21:04 AM		TEXAS INSTRUMENTS



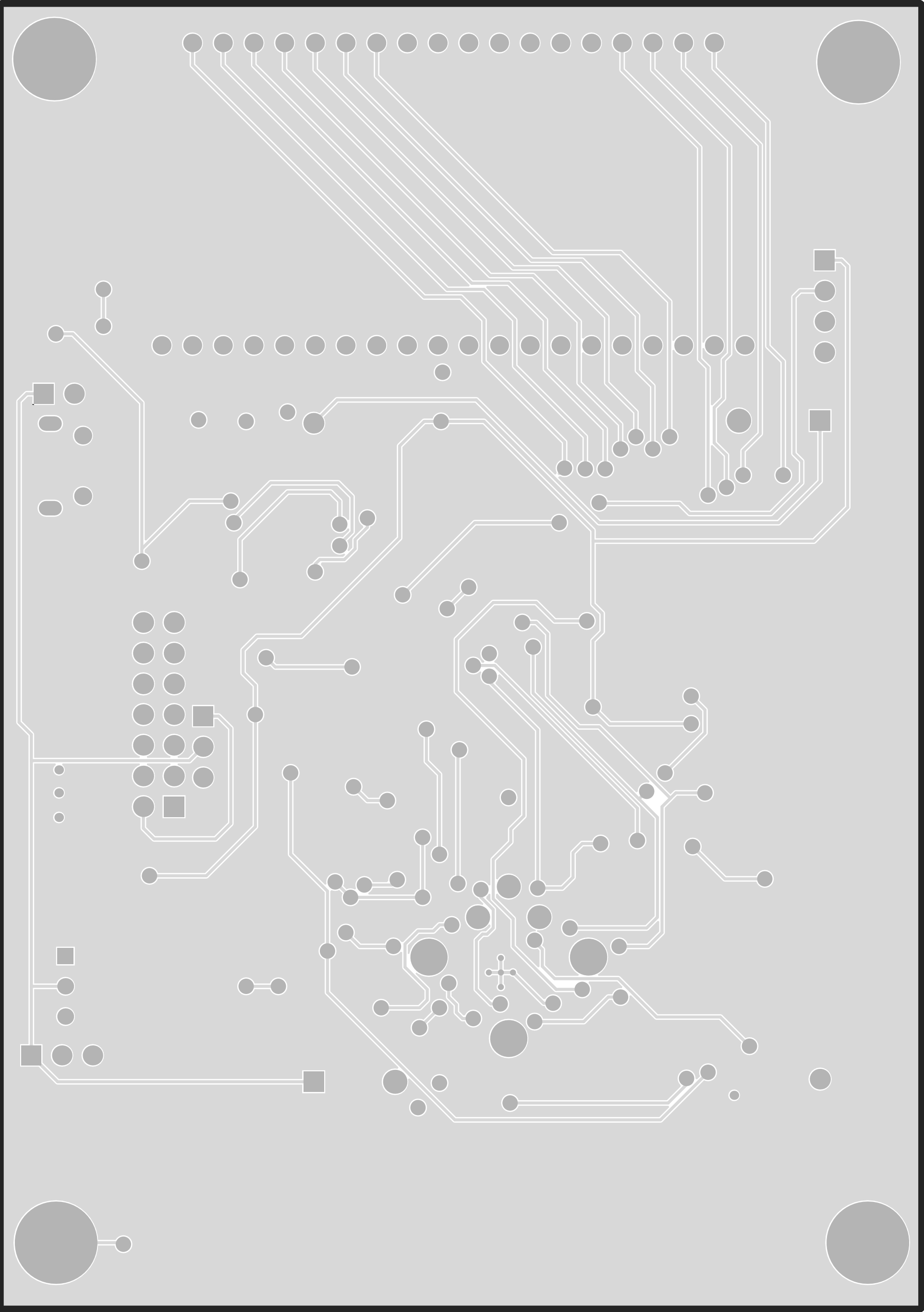
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/12/2016 10:21:06 AM		TEXAS INSTRUMENTS



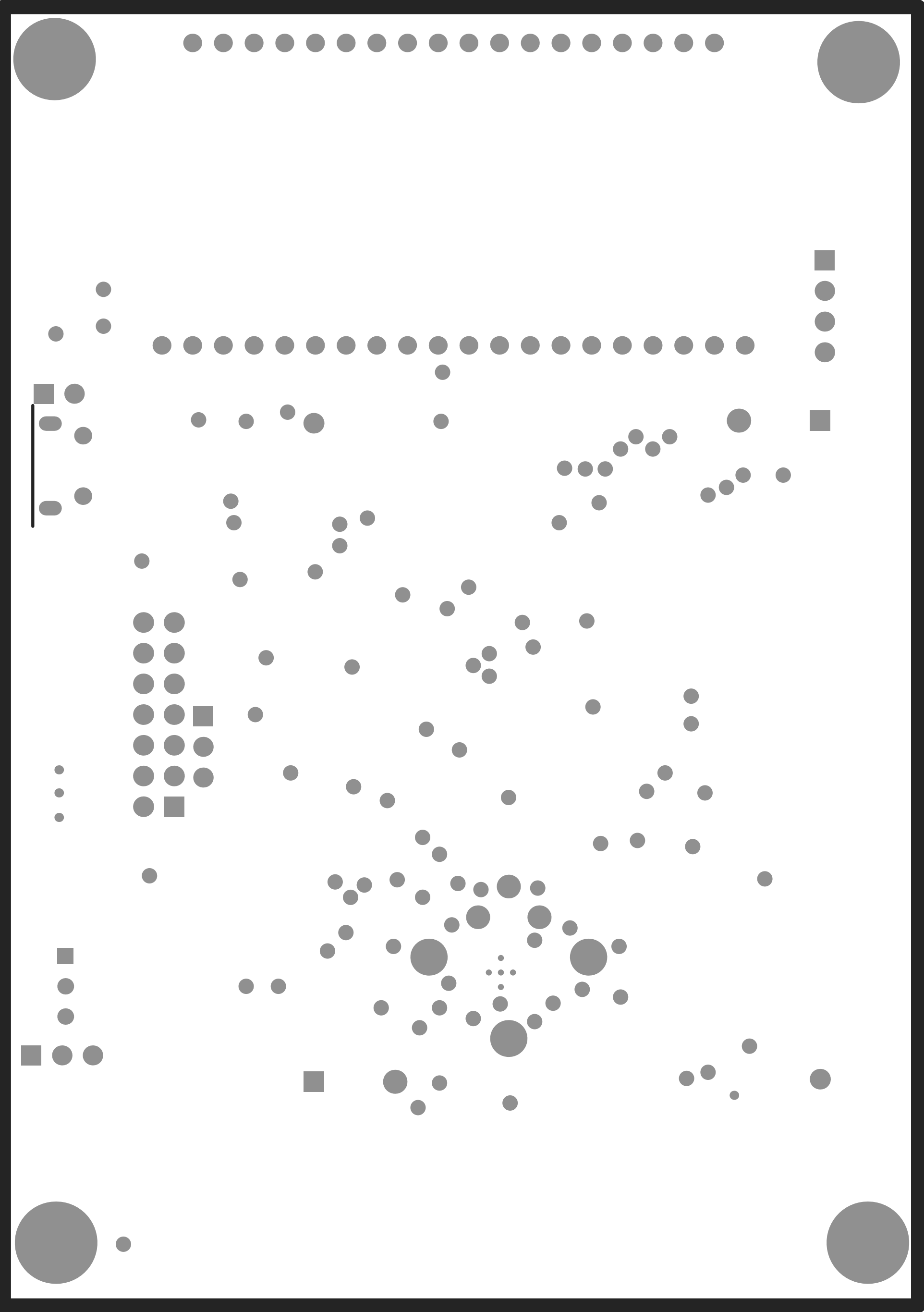
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/12/2016 10:21:07 AM		TEXAS INSTRUMENTS



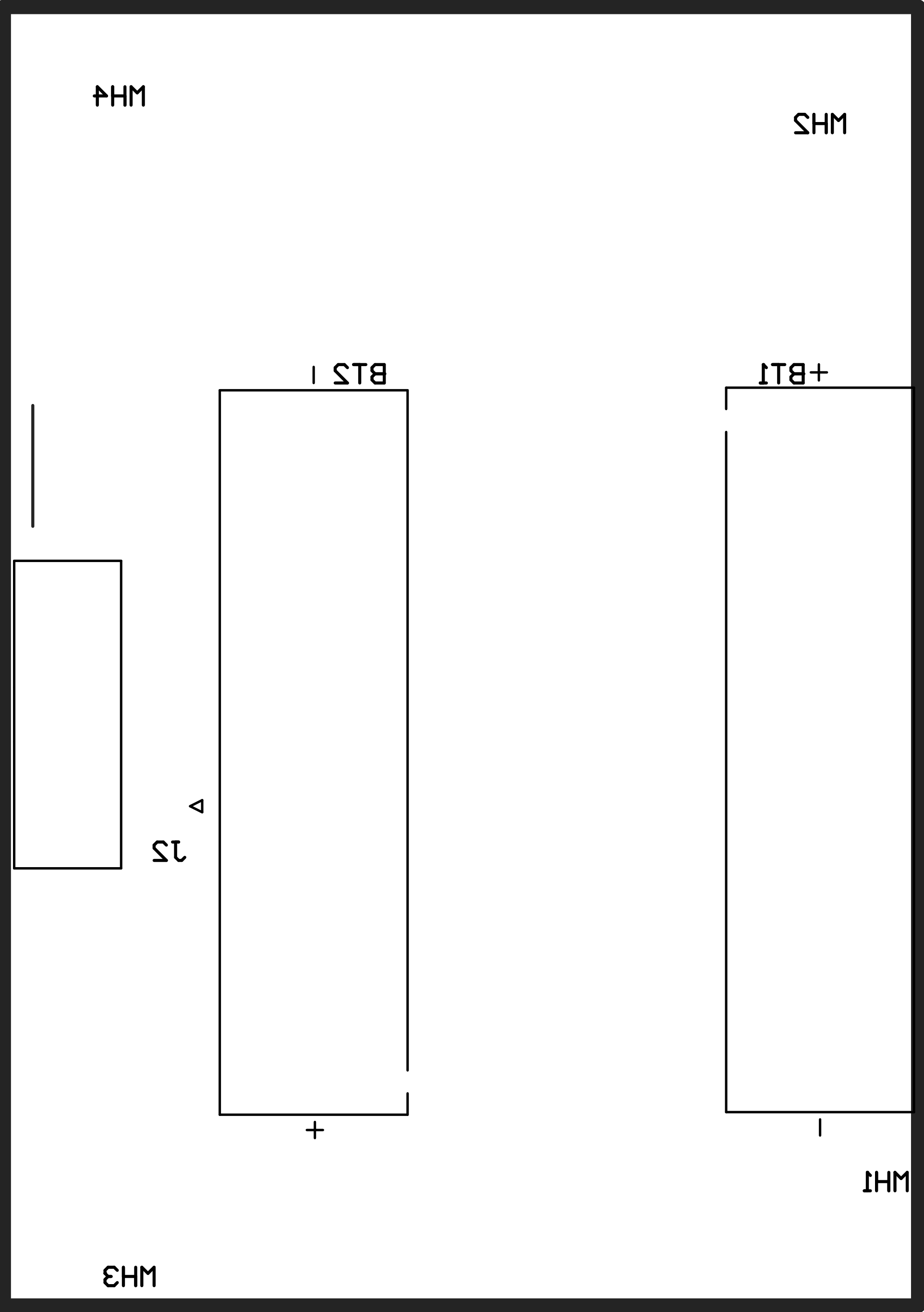
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/12/2016 10:21:08 AM		TEXAS INSTRUMENTS



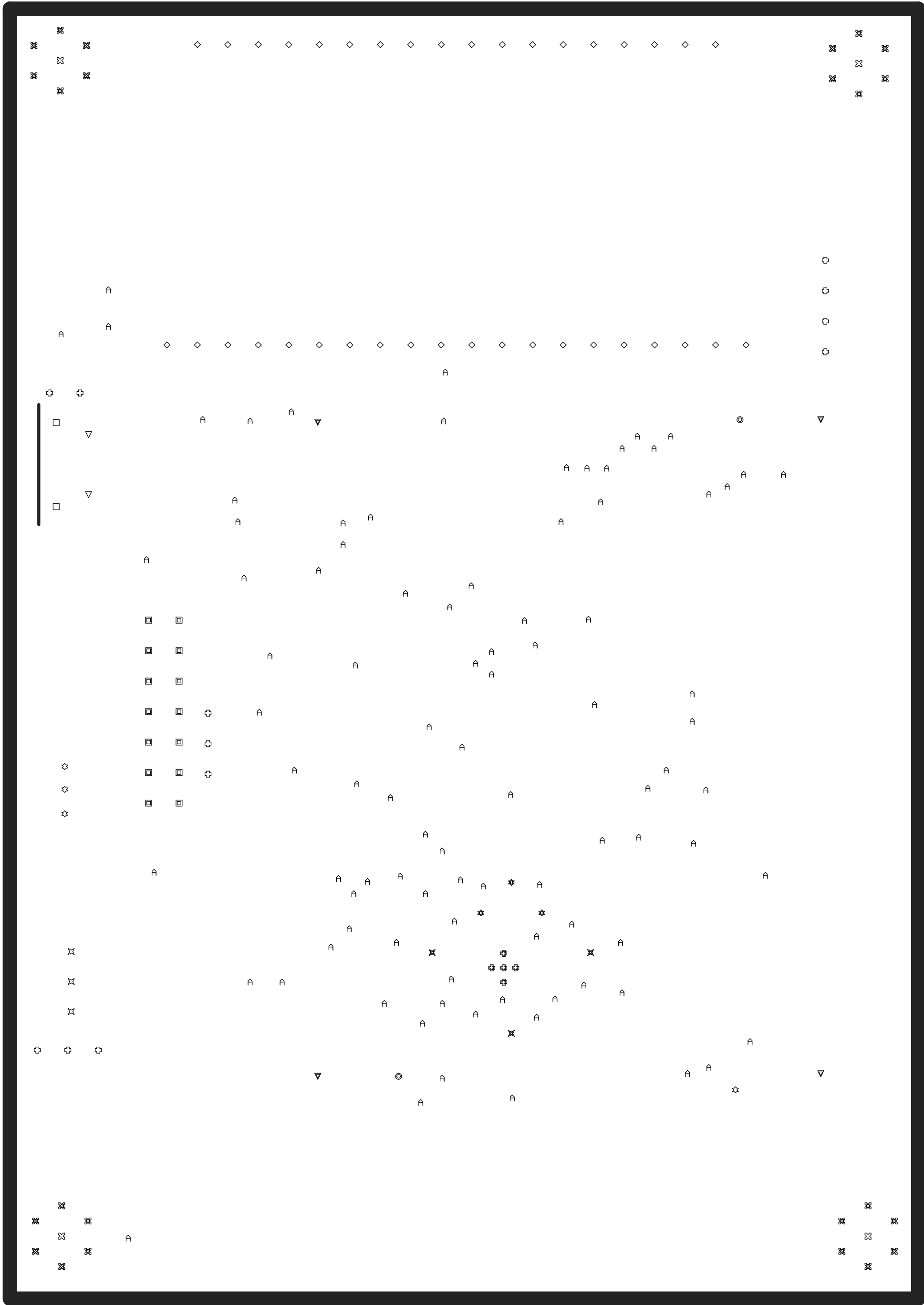
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/12/2016 10:21:09 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/12/2016 10:21:11 AM		TEXAS INSTRUMENTS



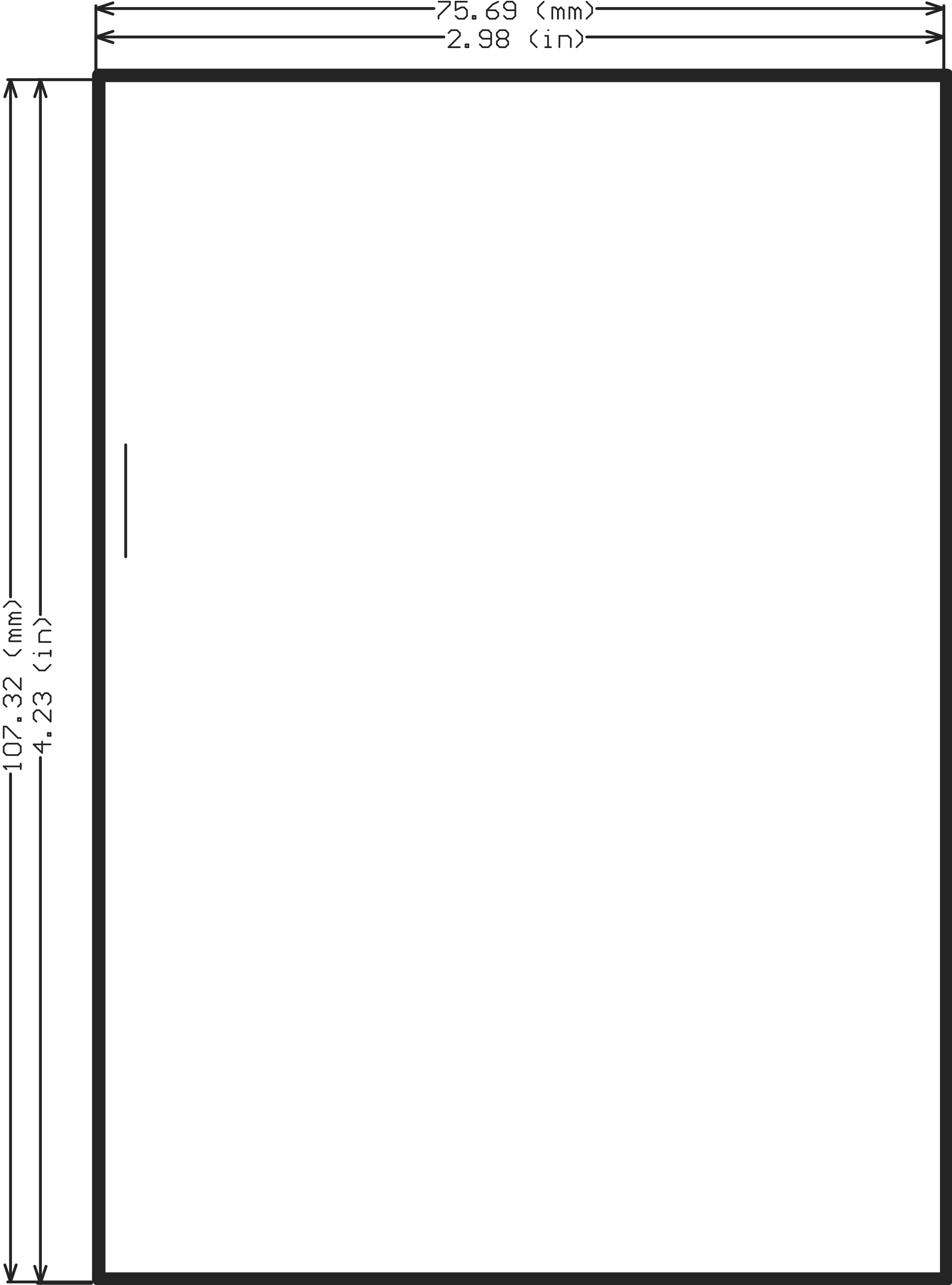
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/12/2016 10:21:12 AM		TEXAS INSTRUMENTS



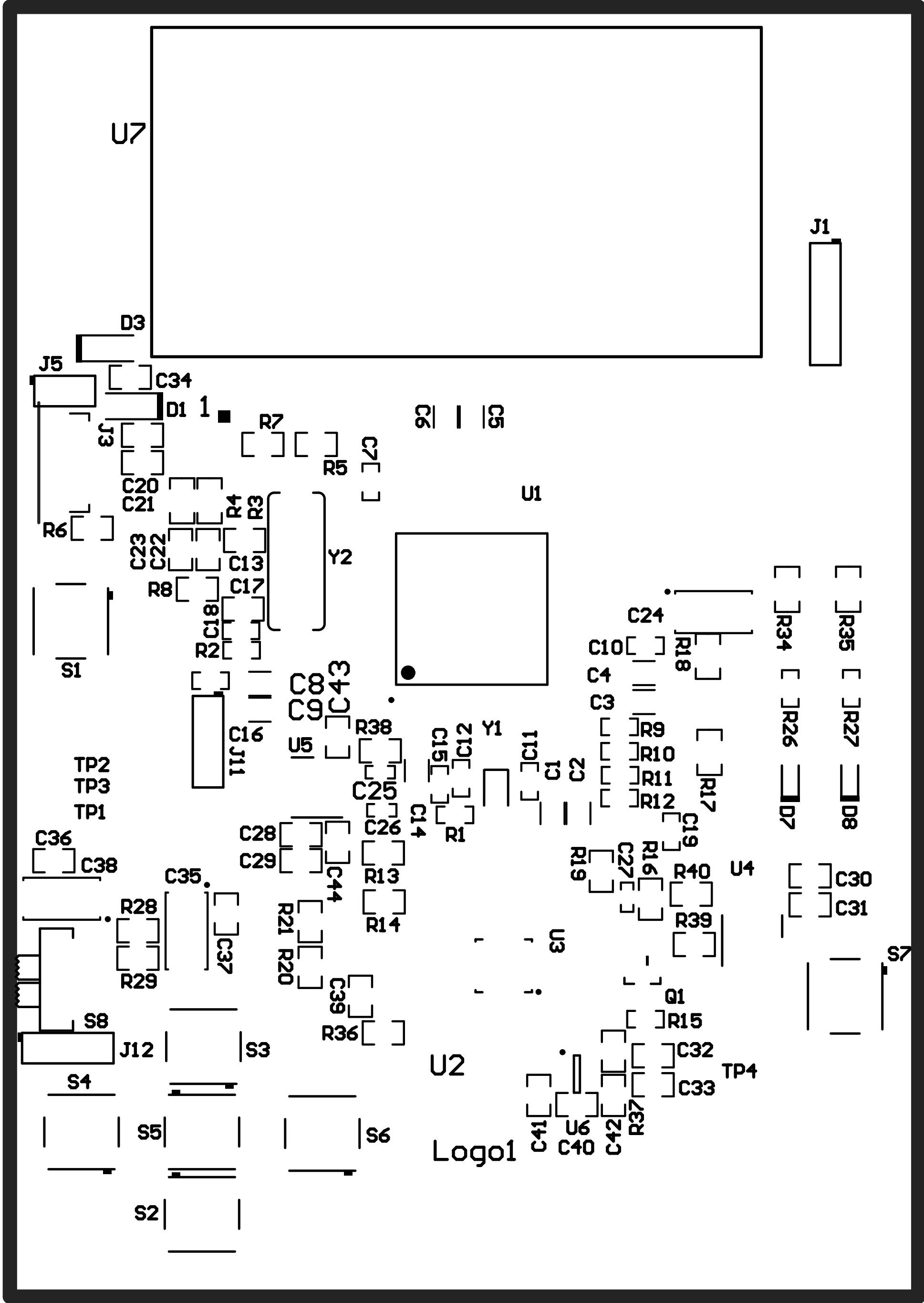
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⦿	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
▣	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
□	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

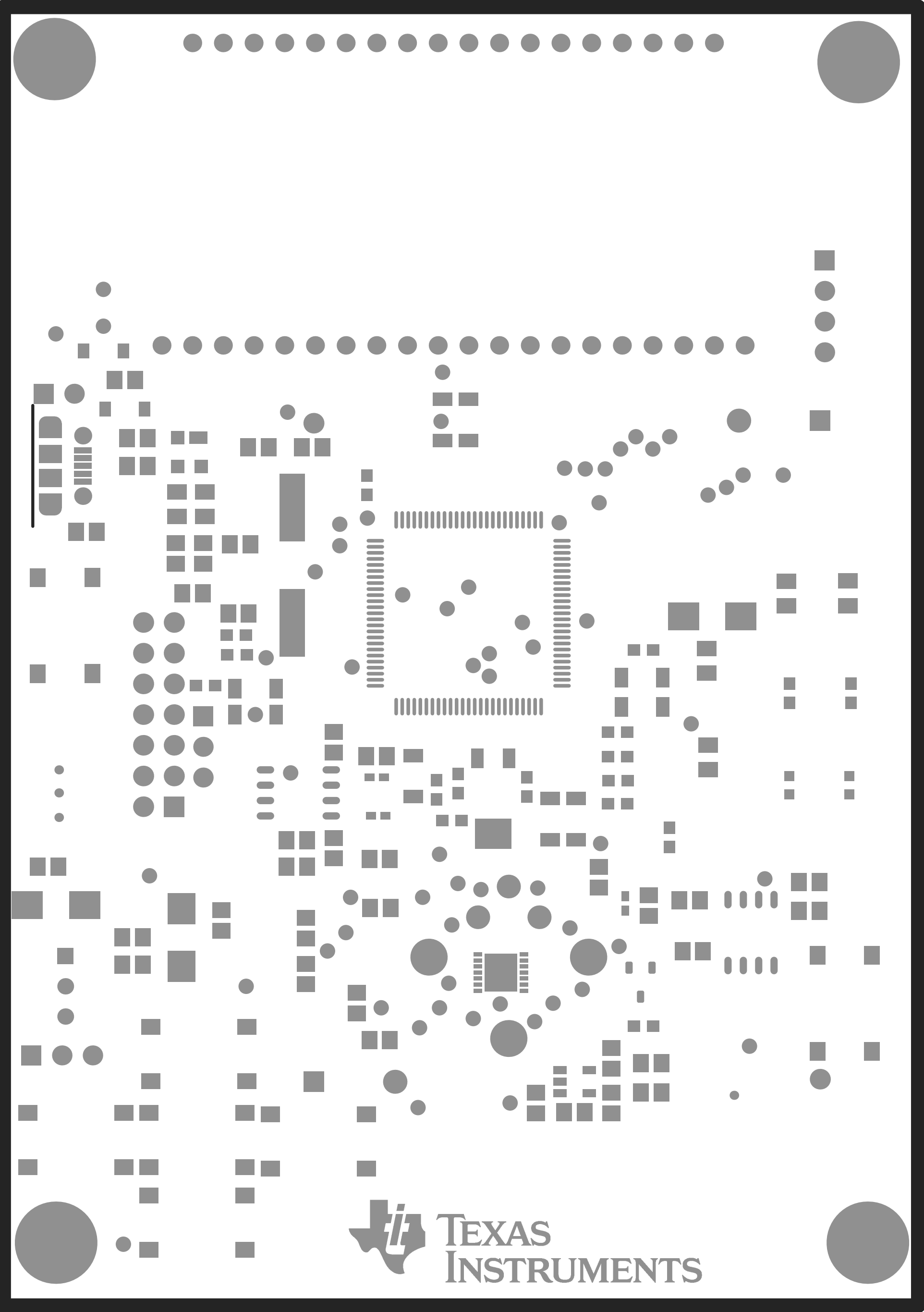
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SUN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/12/2016 10:21:13 AM		TEXAS INSTRUMENTS



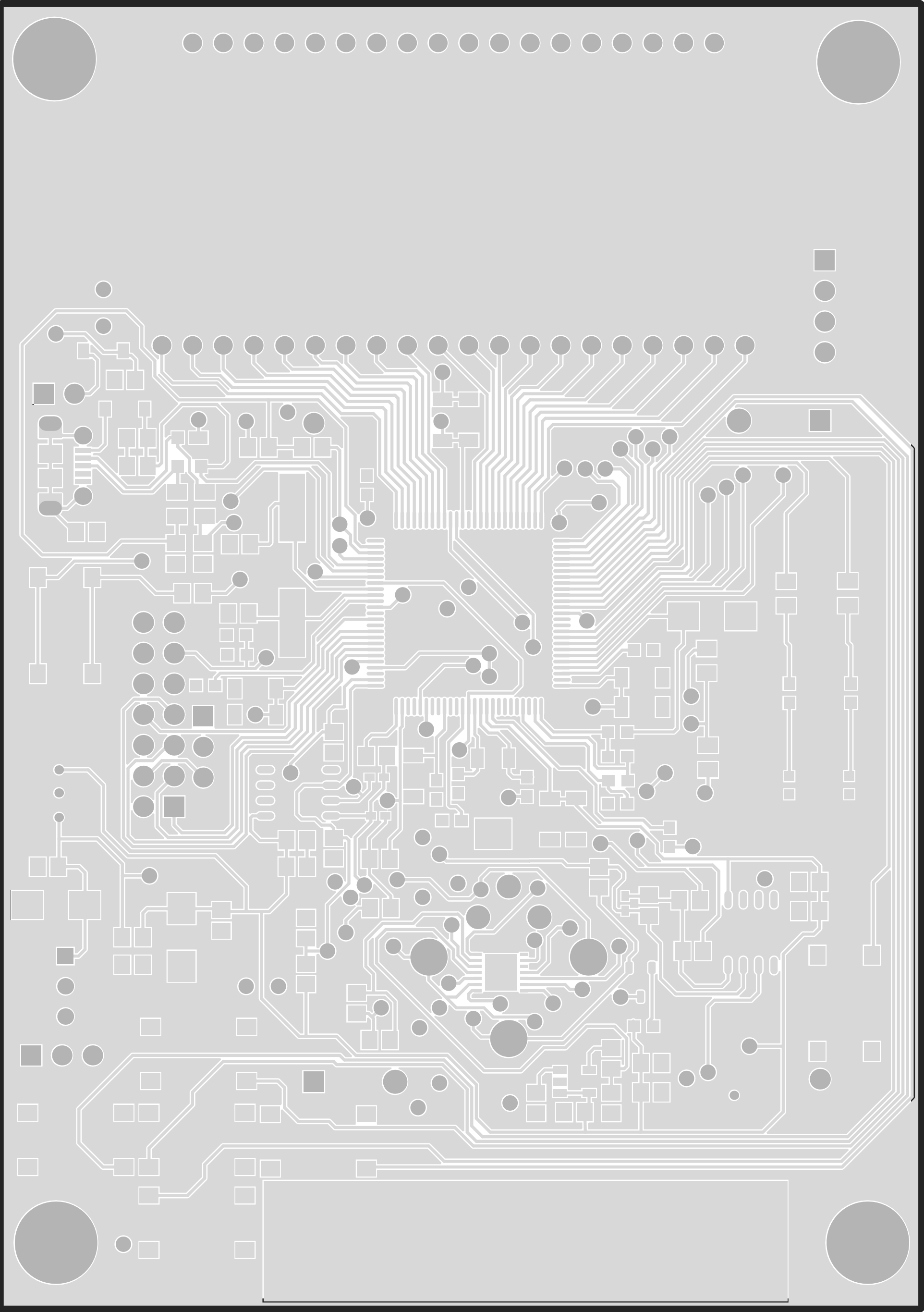
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/12/2016 10:21:15 AM		TEXAS INSTRUMENTS



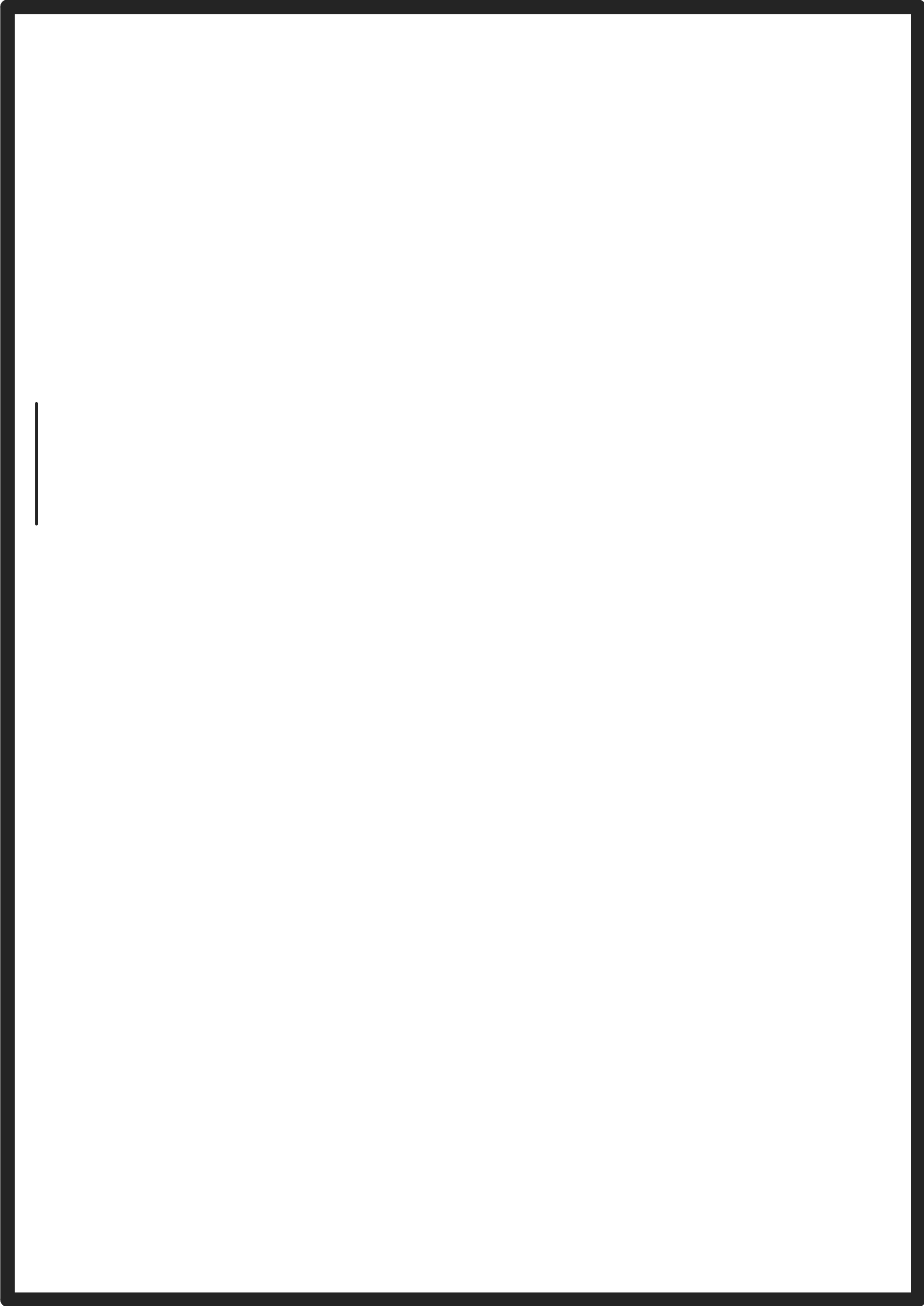
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/12/2016 10:21:17 AM		TEXAS INSTRUMENTS



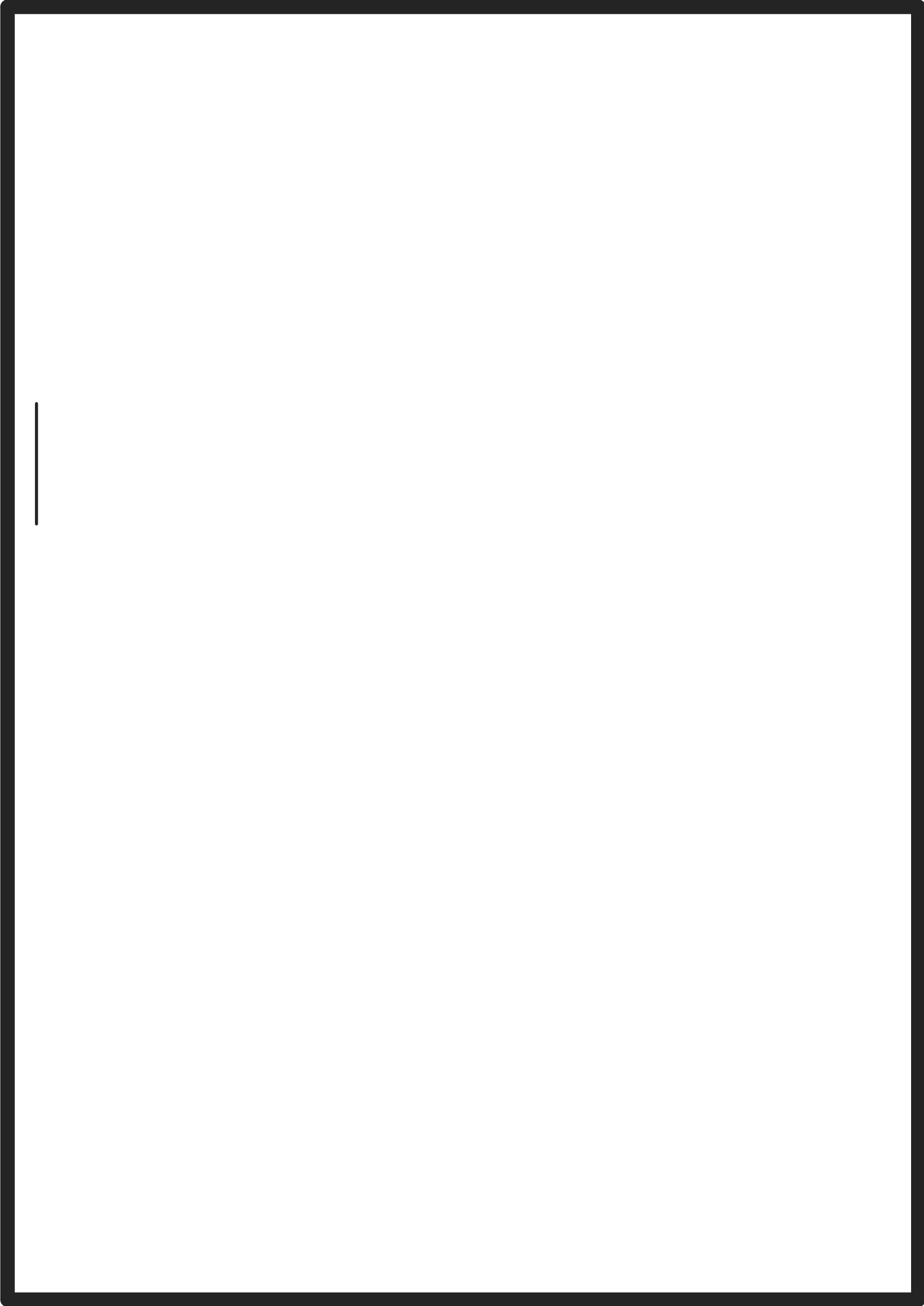
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/12/2016 10:21:19 AM		TEXAS INSTRUMENTS



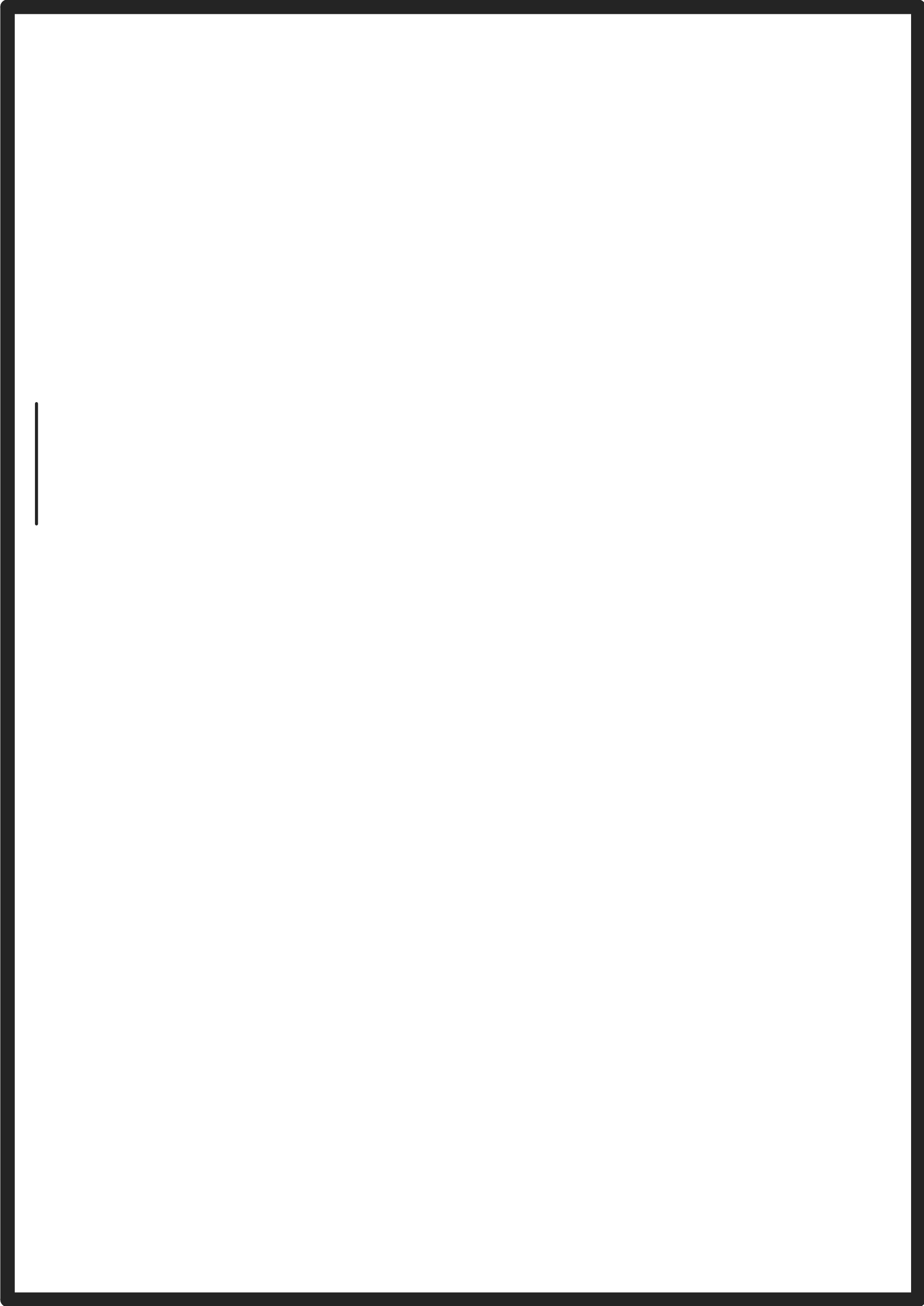
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/12/2016 10:21:20 AM		TEXAS INSTRUMENTS



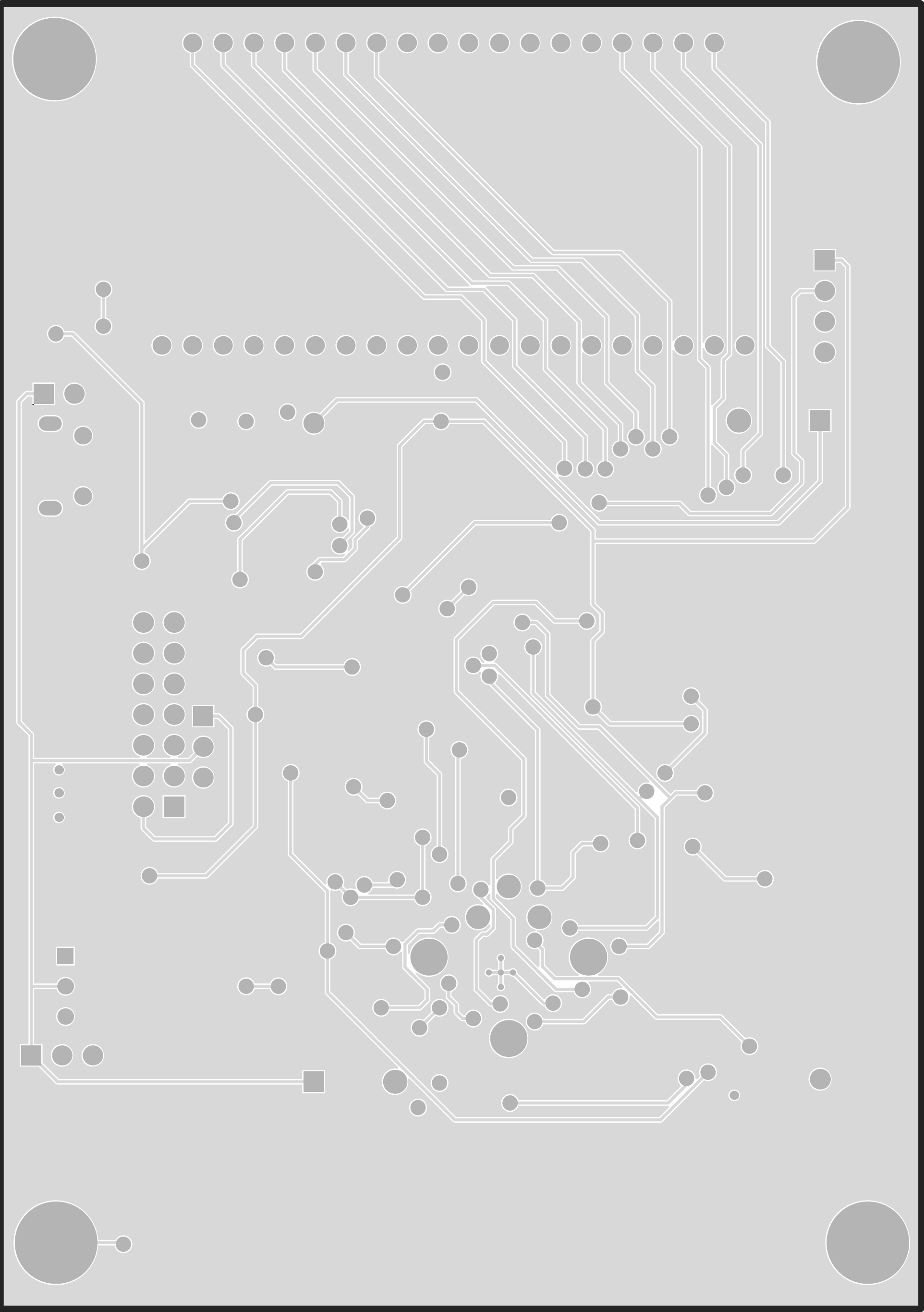
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/12/2016 10:21:22 AM		TEXAS INSTRUMENTS



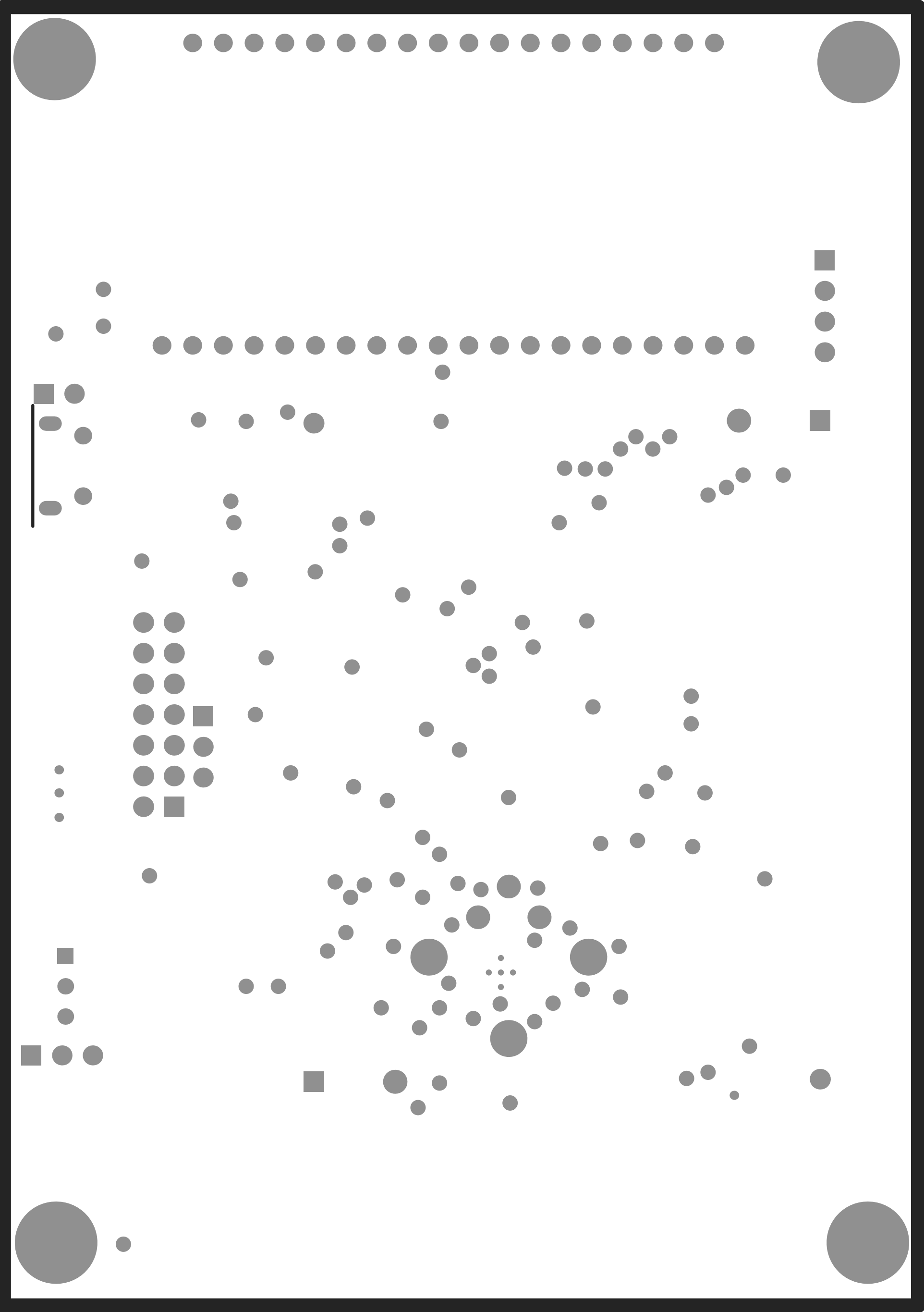
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 3	GENERATED : 5/12/2016 10:21:23 AM		TEXAS INSTRUMENTS



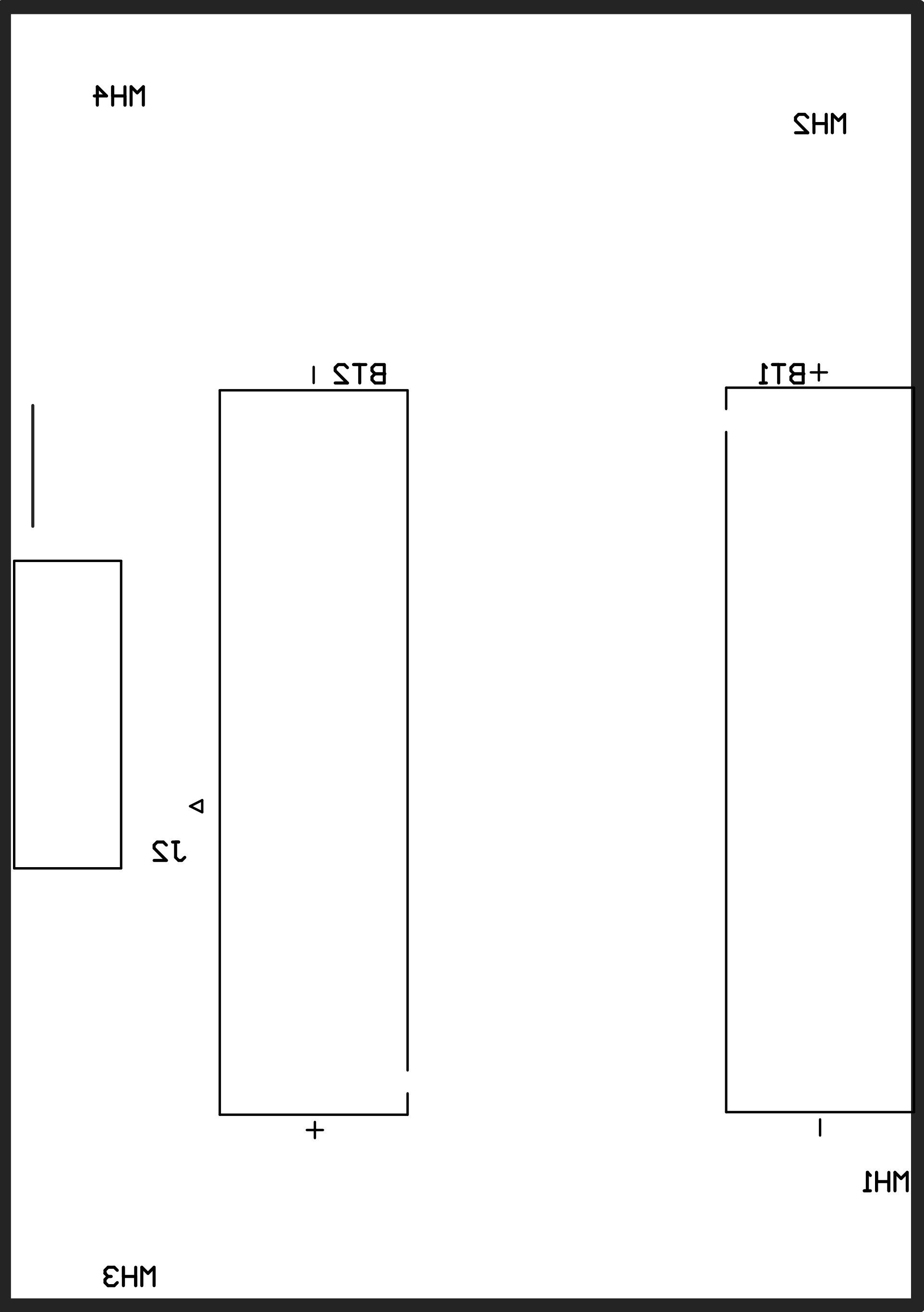
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 4	GENERATED : 5/12/2016 10:21:24 AM		TEXAS INSTRUMENTS



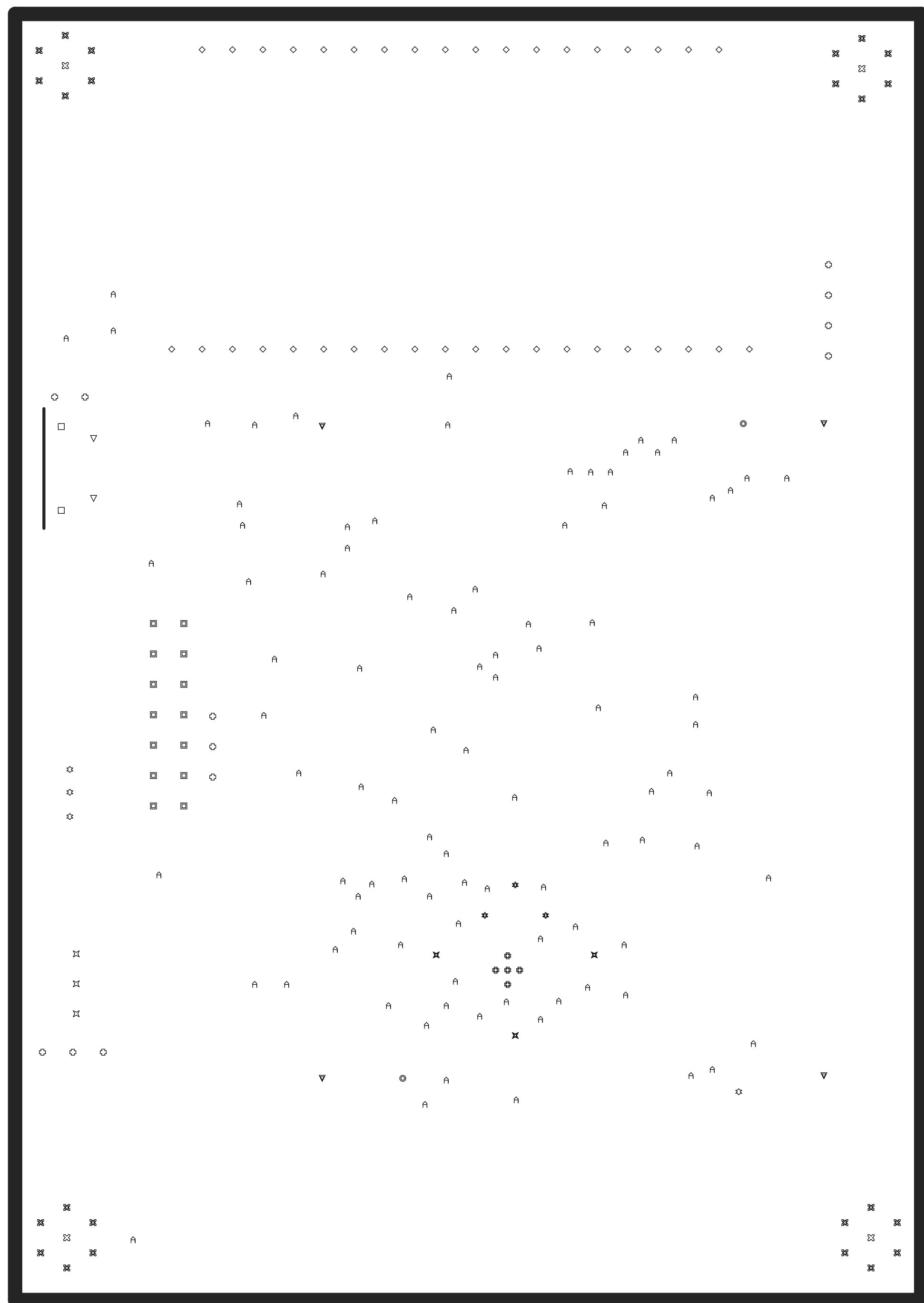
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/12/2016 10:21:25 AM		TEXAS INSTRUMENTS


















ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/12/2016 10:21:27 AM		TEXAS INSTRUMENTS



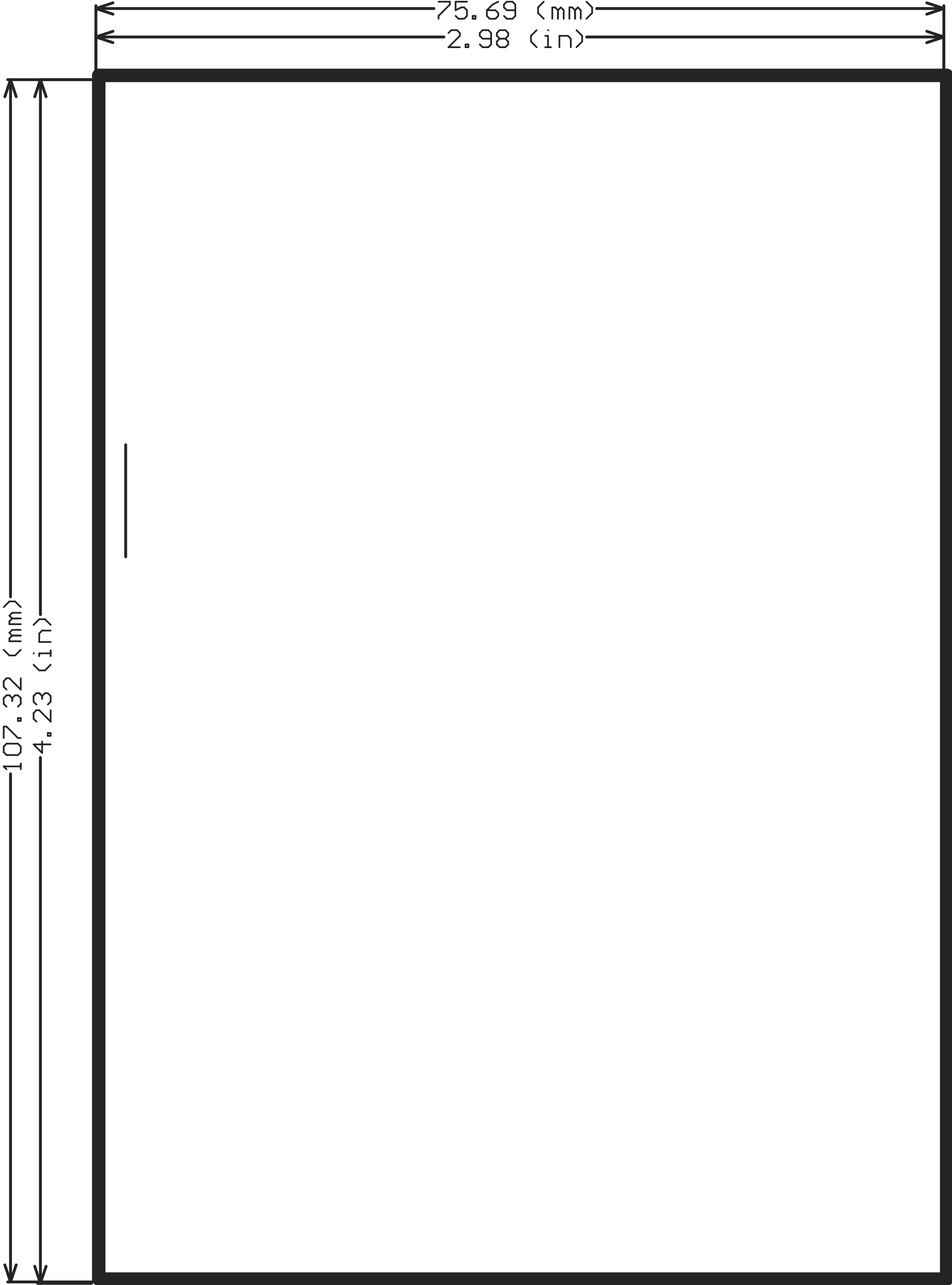
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/12/2016 10:21:28 AM		TEXAS INSTRUMENTS



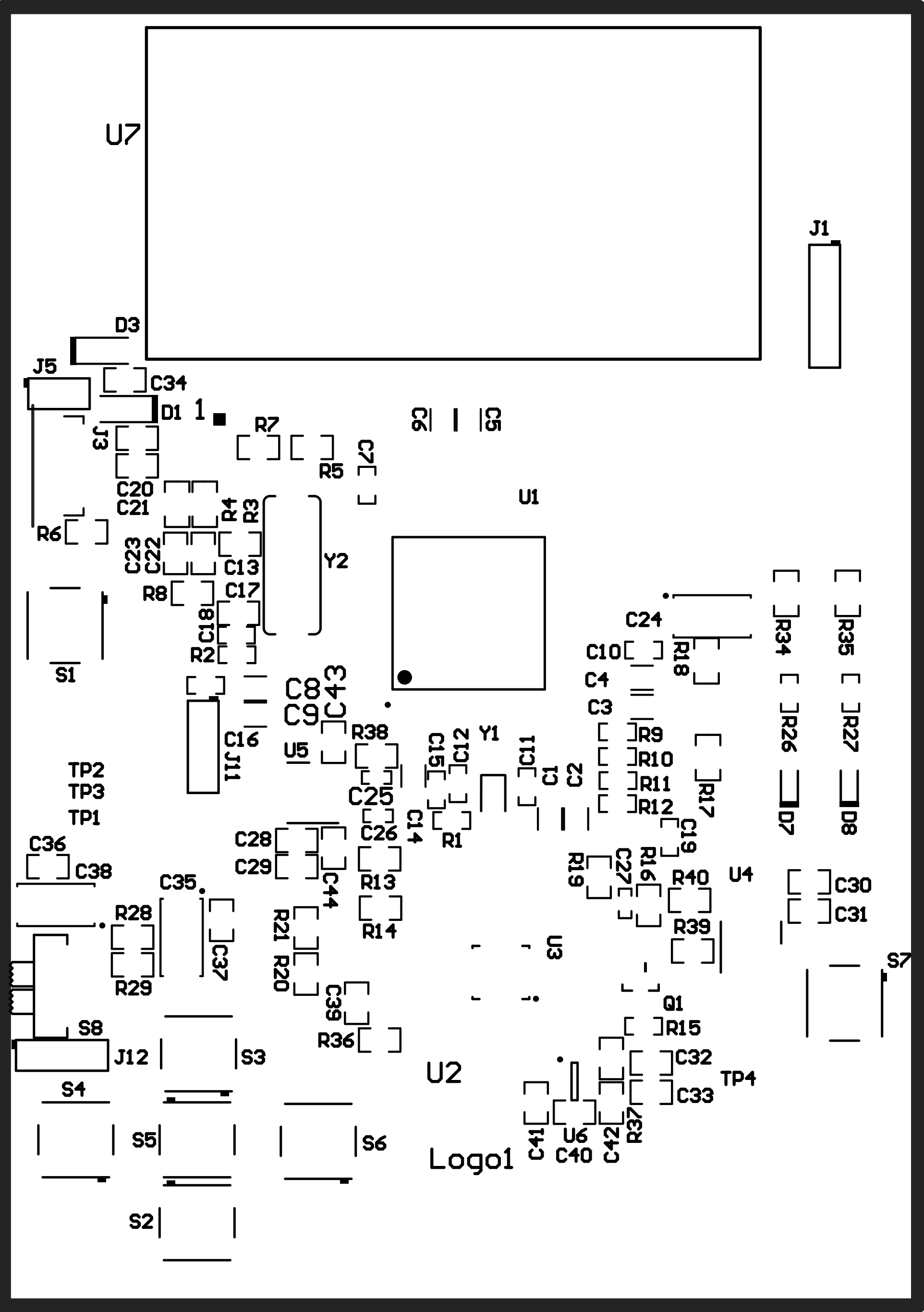
Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

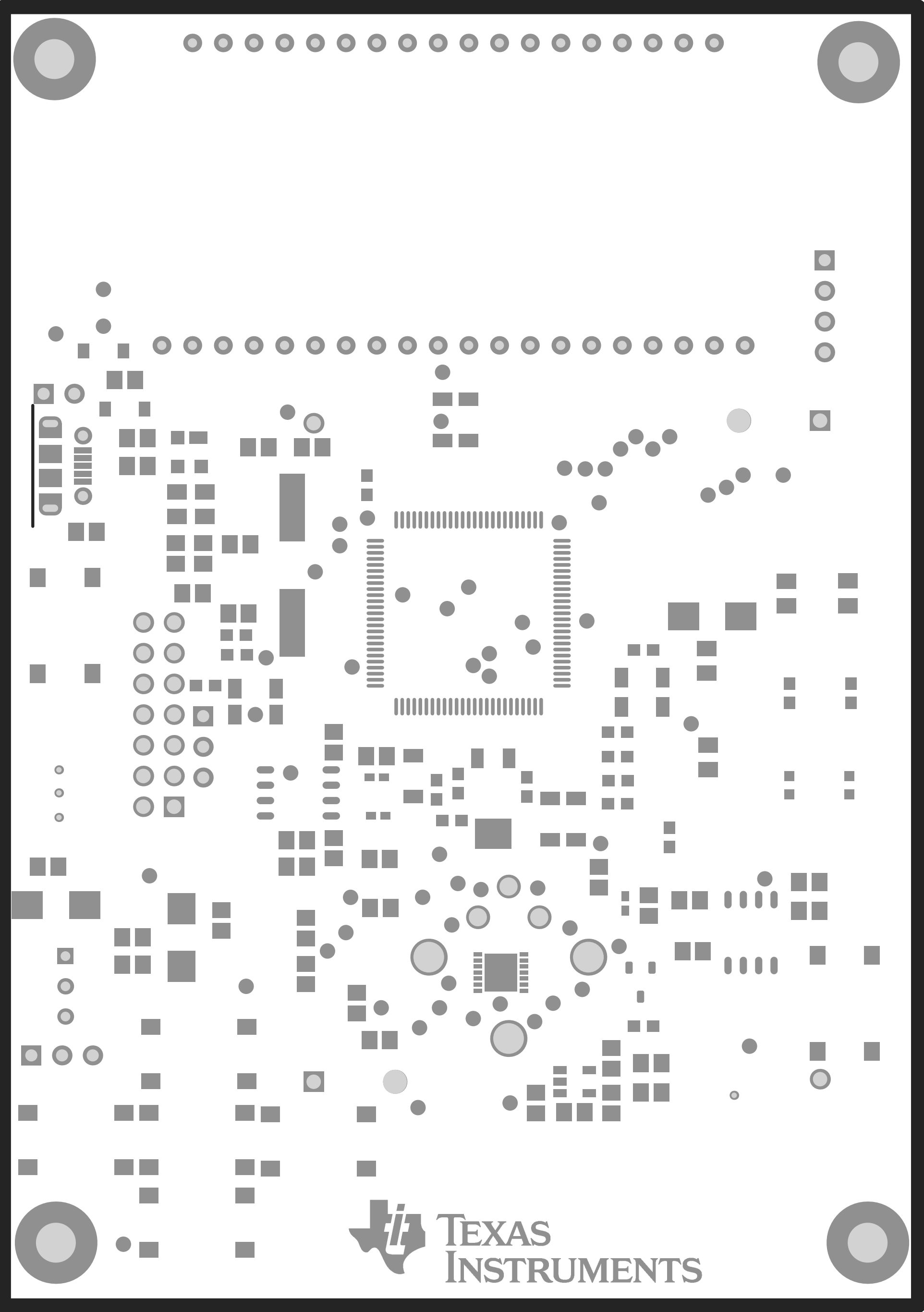
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/12/2016 10:21:30 AM		TEXAS INSTRUMENTS



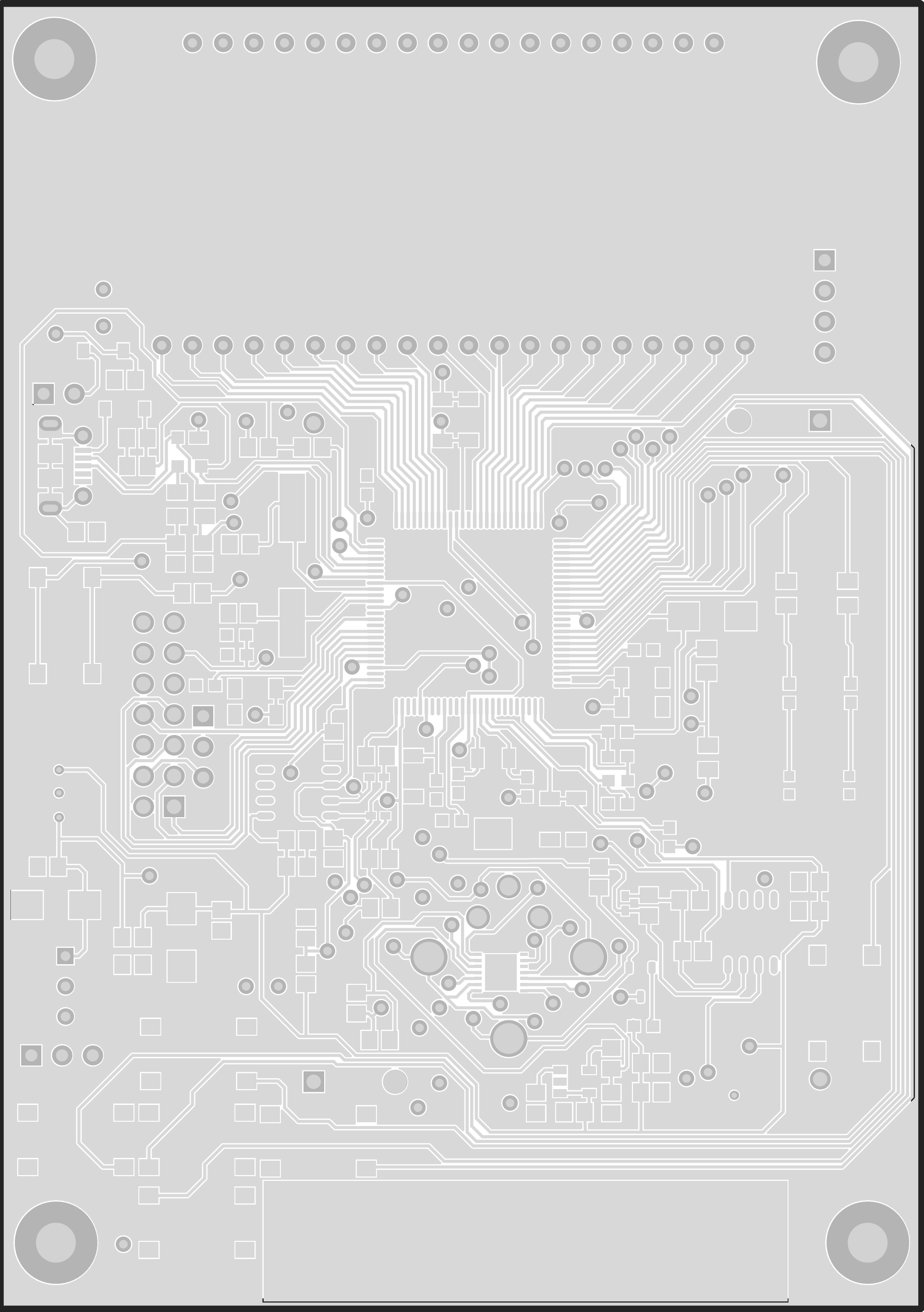
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/12/2016 10:21:32 AM		TEXAS INSTRUMENTS



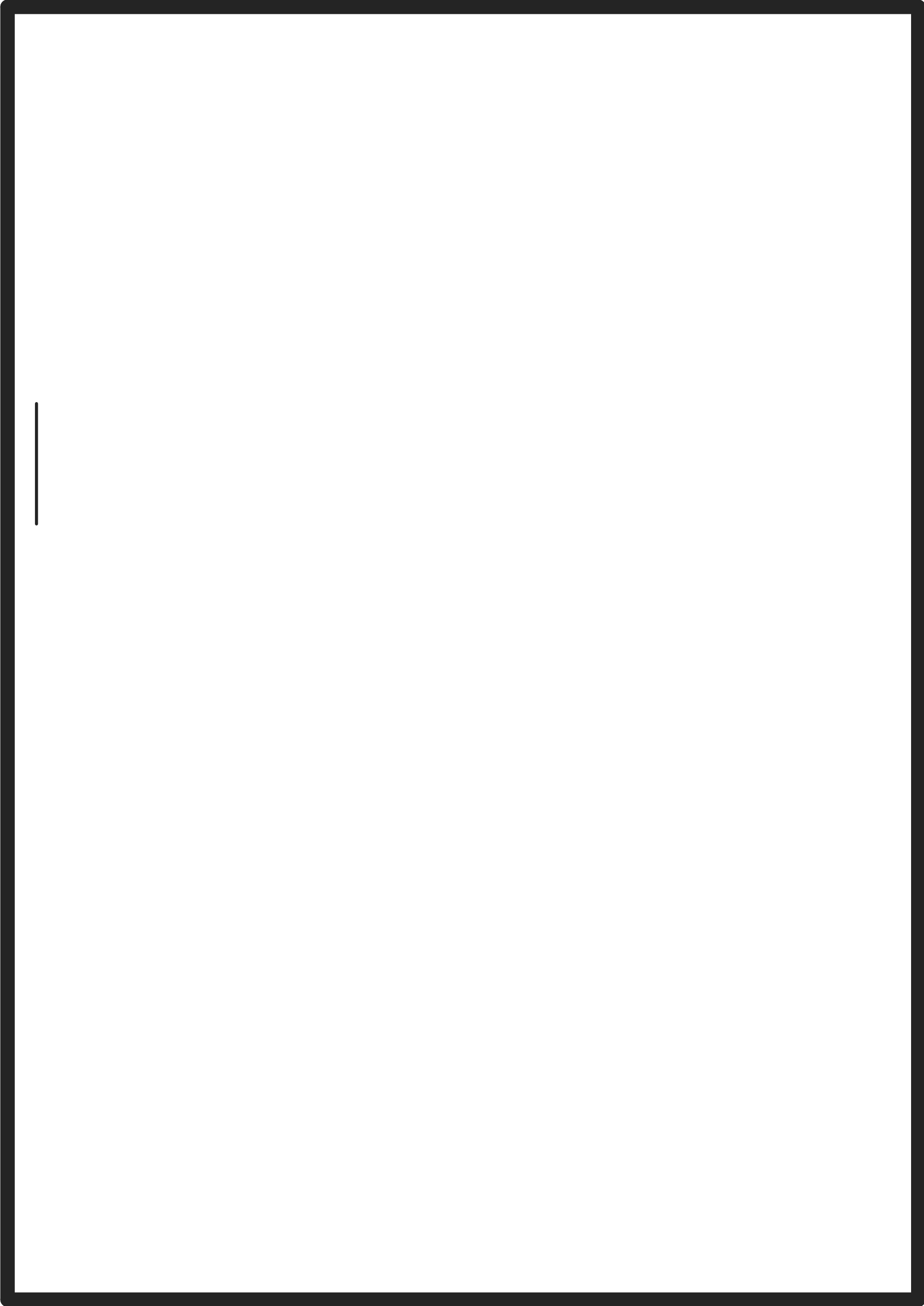
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Overlay	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Overlay	GENERATED : 5/12/2016 10:21:34 AM		TEXAS INSTRUMENTS



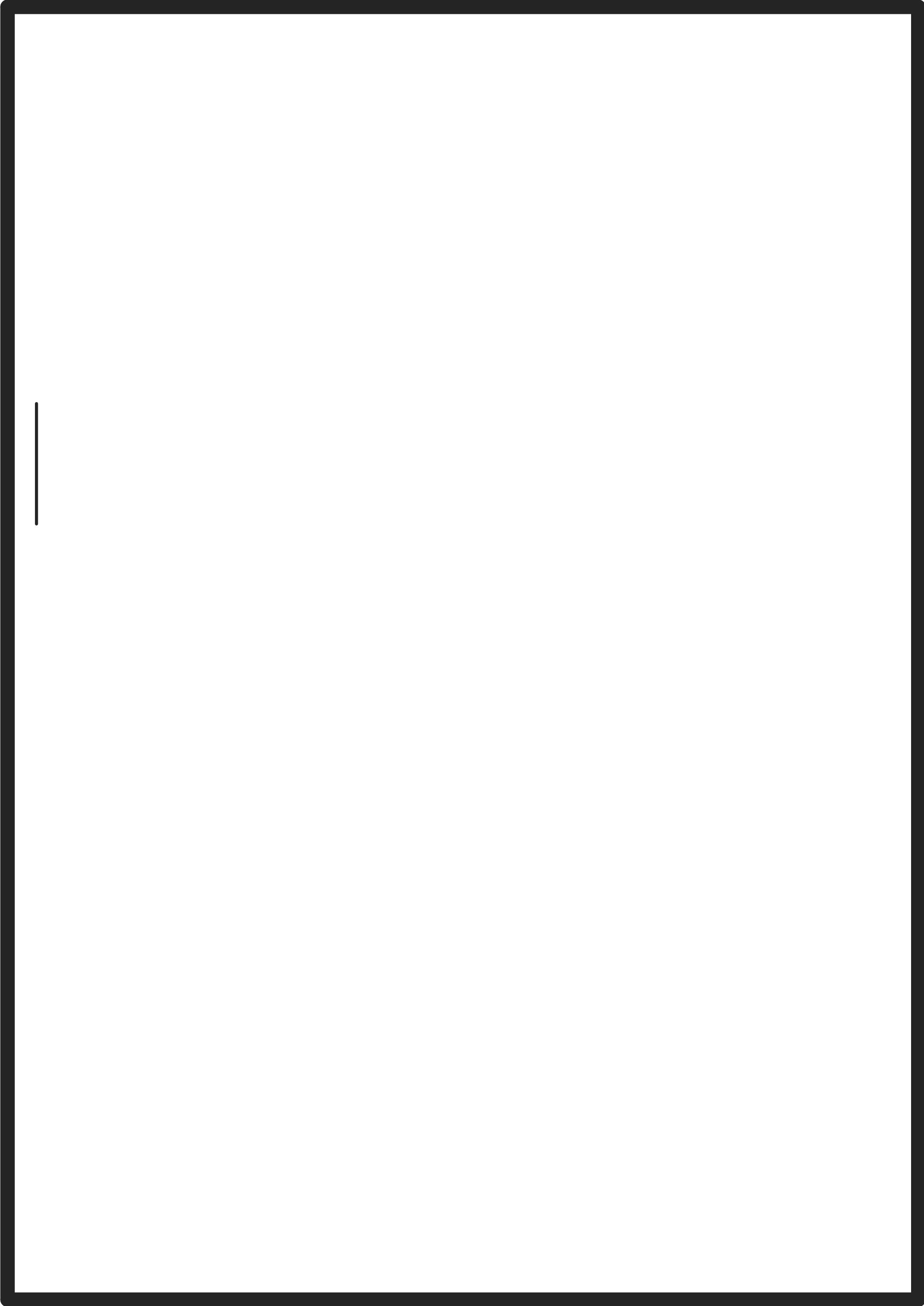
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Top Solder Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Solder Mask	GENERATED : 5/12/2016 10:21:36 AM		TEXAS INSTRUMENTS



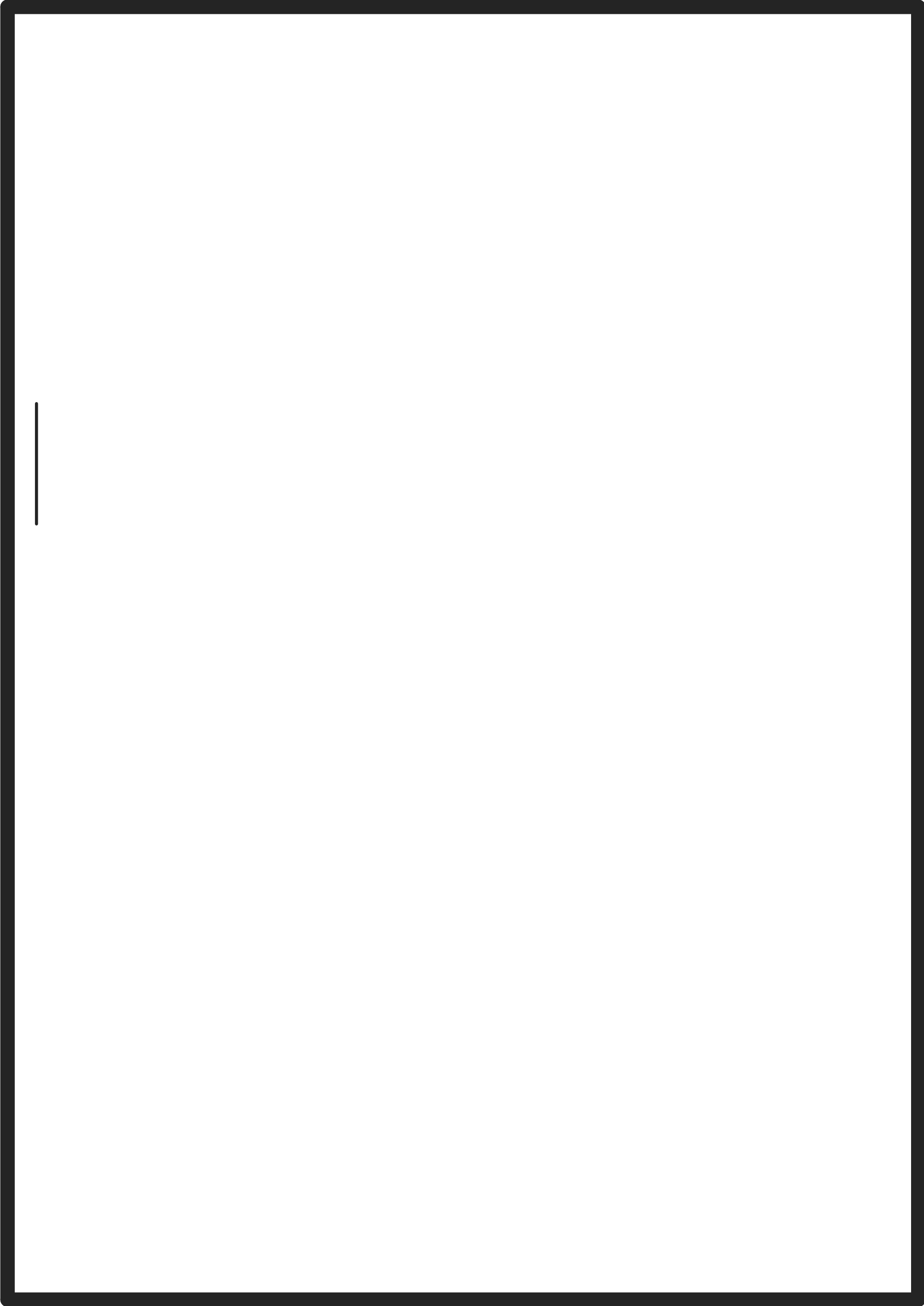
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Top Layer	GENERATED : 5/12/2016 10:21:38 AM		TEXAS INSTRUMENTS



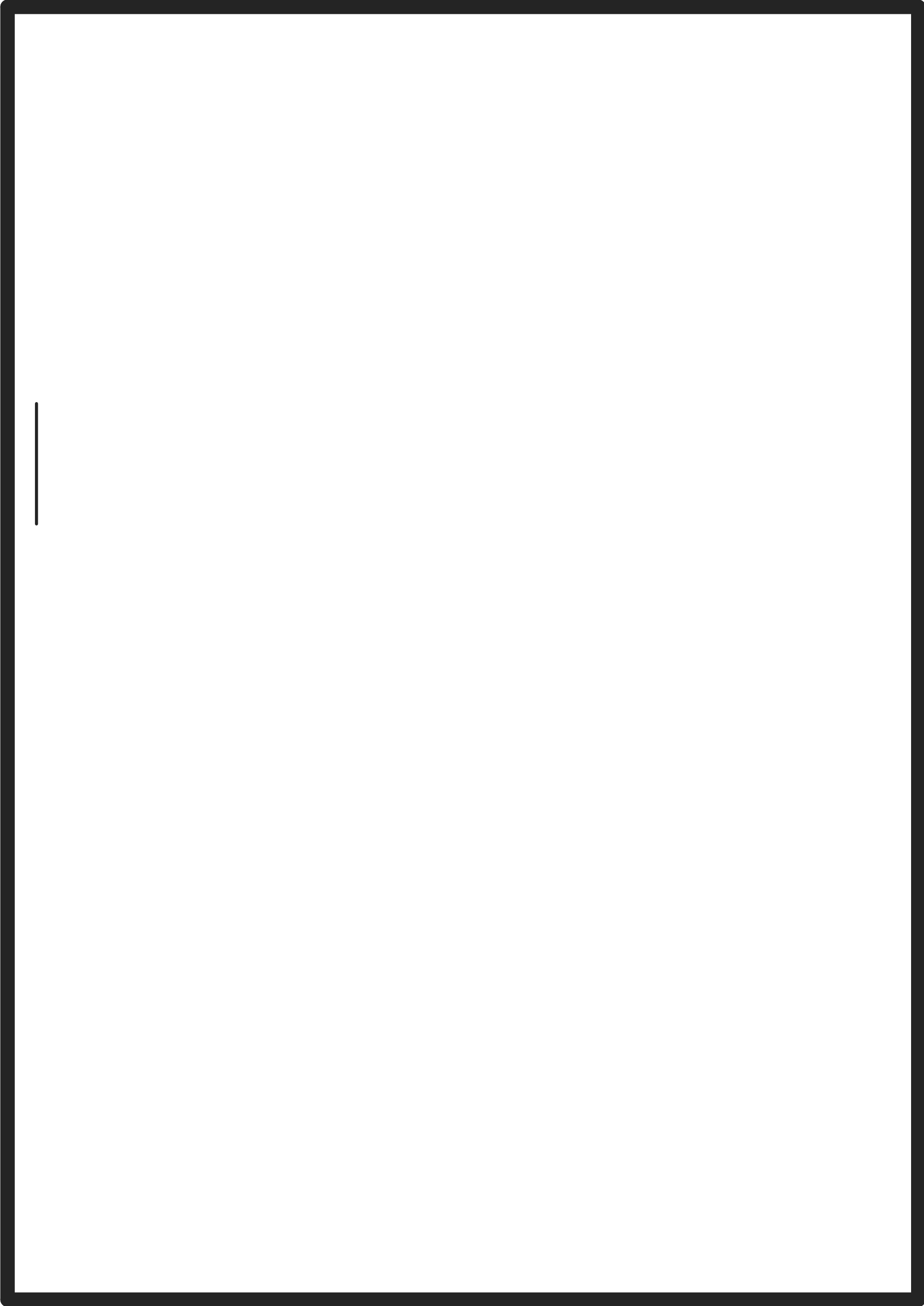
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 1	GENERATED : 5/12/2016 10:21:40 AM		TEXAS INSTRUMENTS



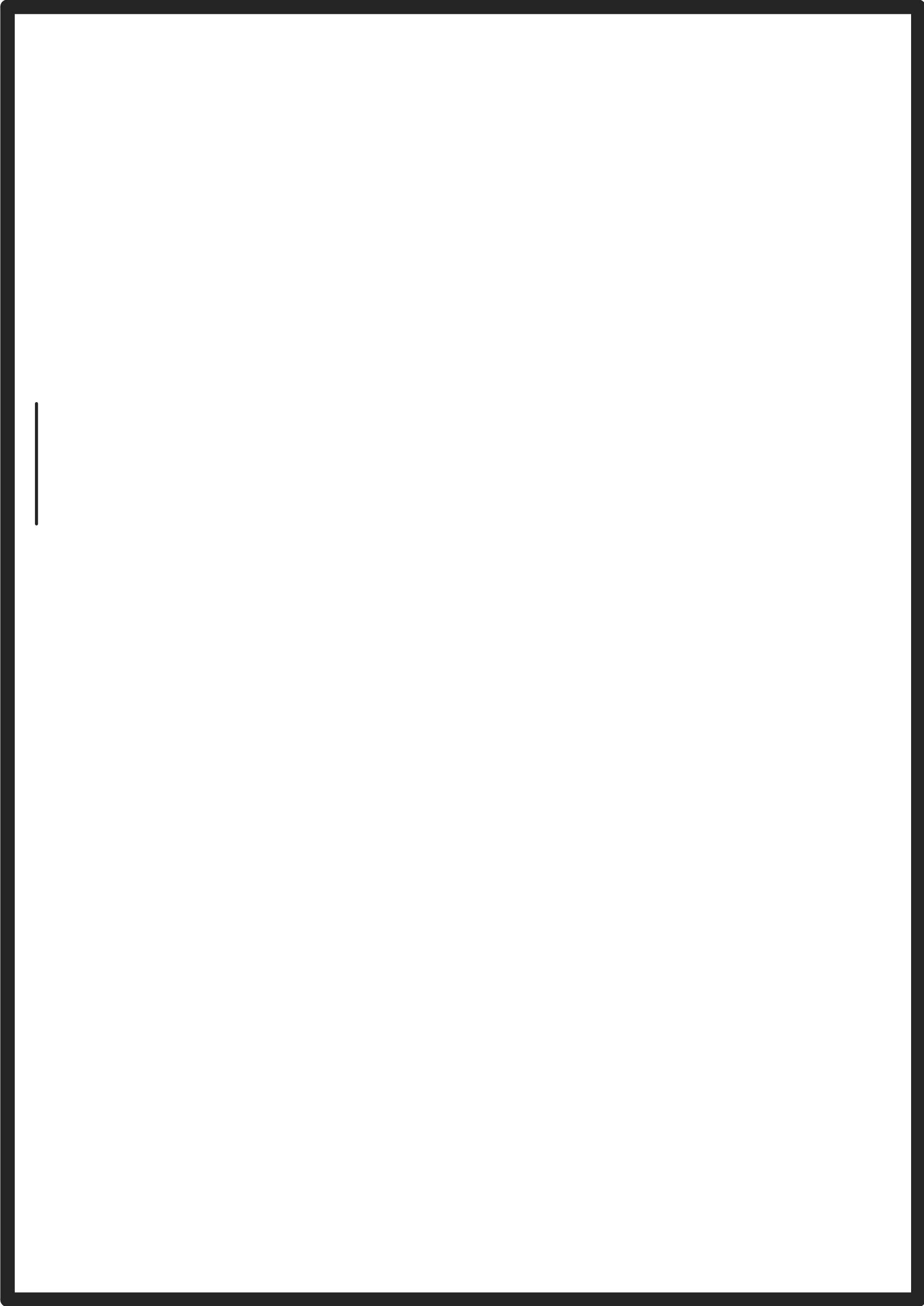
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 3	GENERATED : 5/12/2016 10:21:41 AM		TEXAS INSTRUMENTS



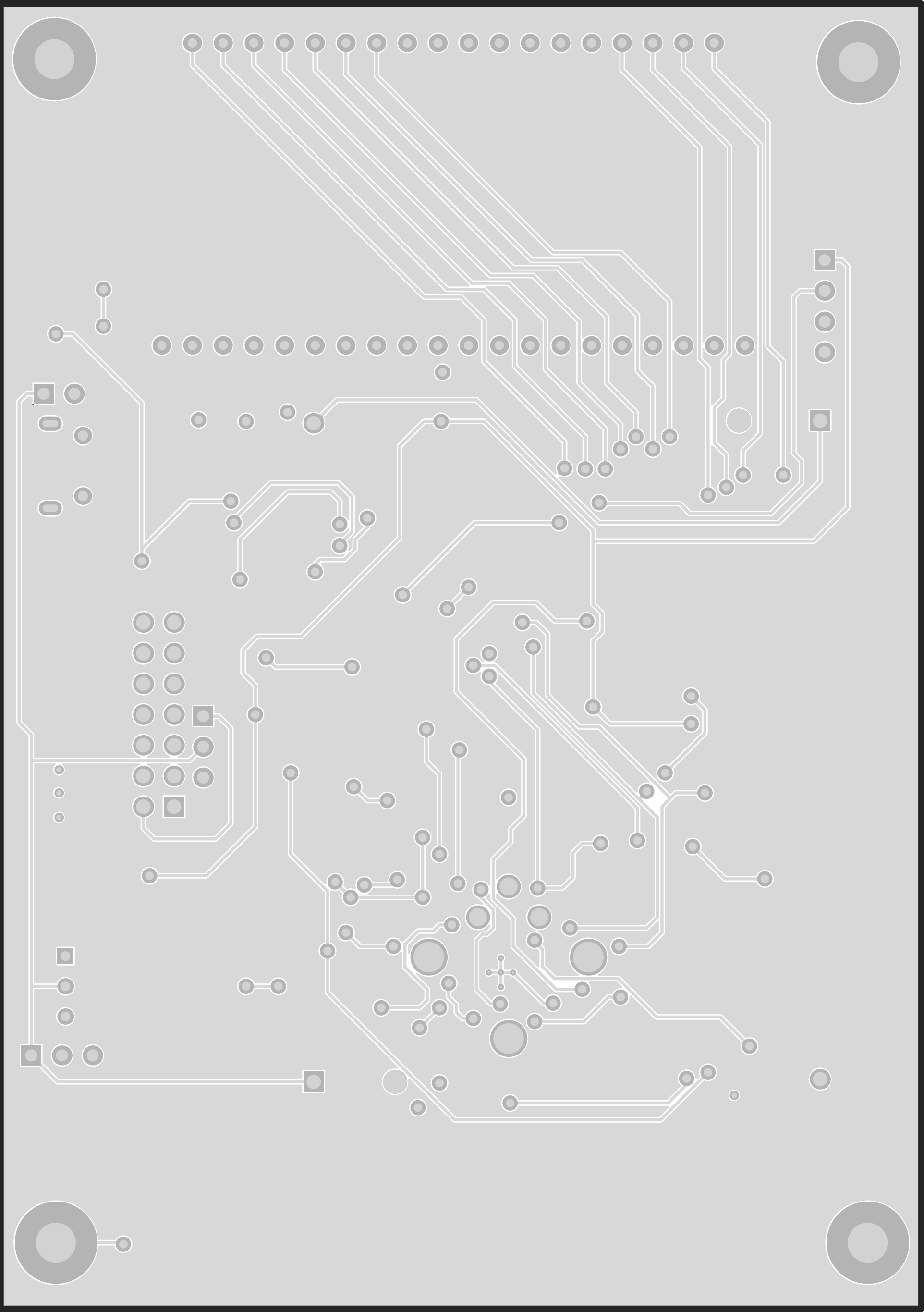
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 4	GENERATED : 5/12/2016 10:21:43 AM		TEXAS INSTRUMENTS



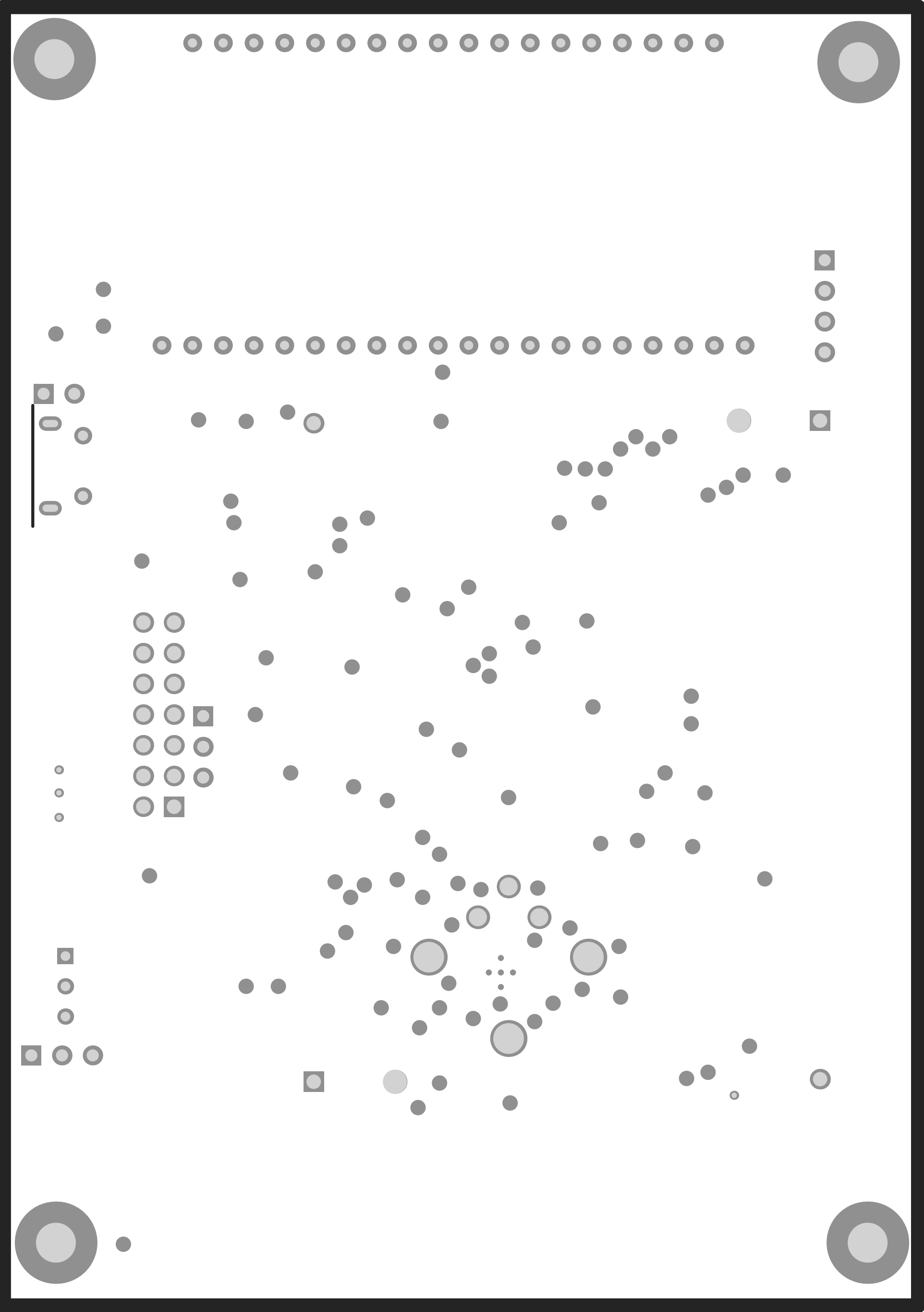
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 5	GENERATED : 5/12/2016 10:21:44 AM		TEXAS INSTRUMENTS



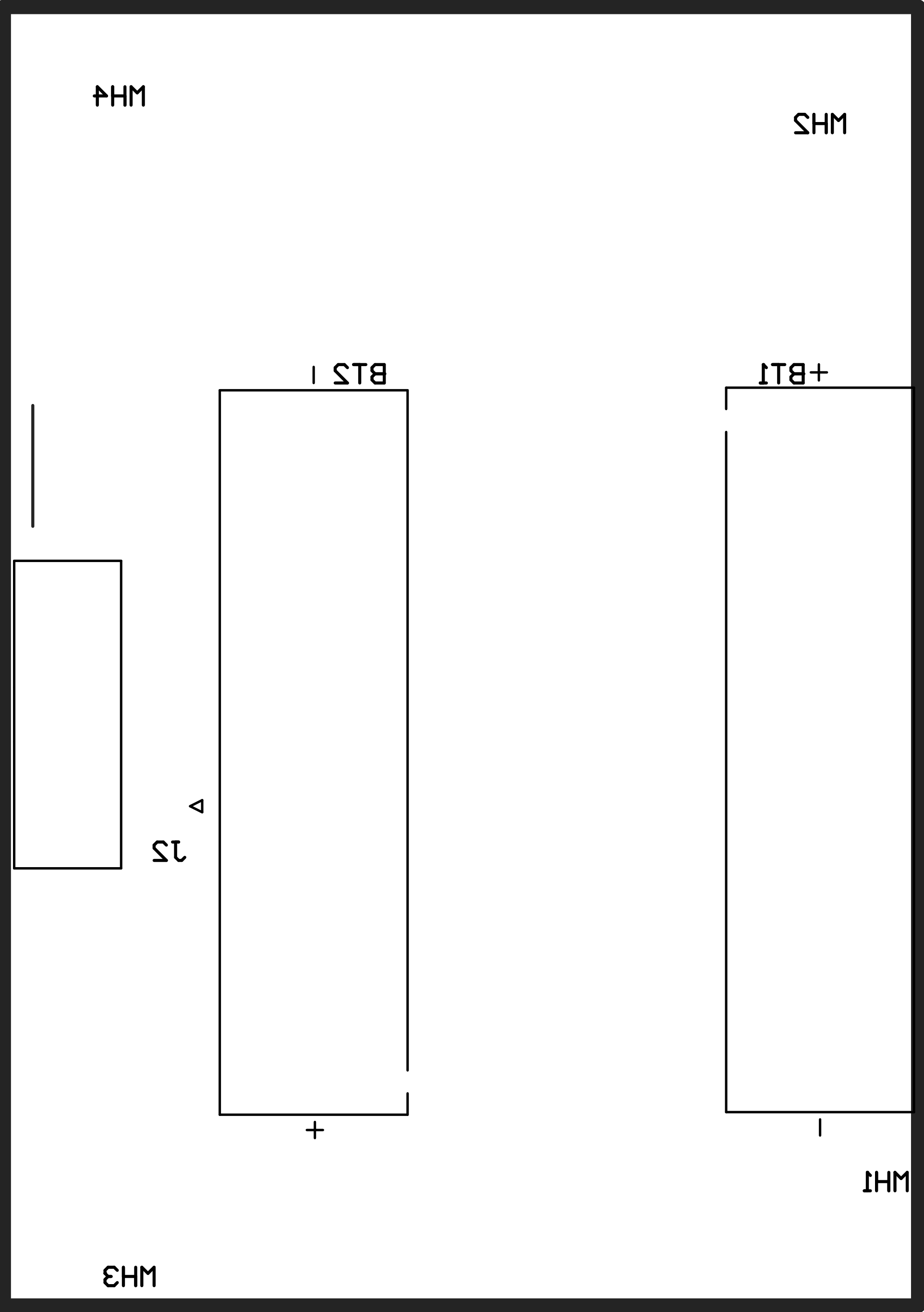
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M1 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Signal Layer 6	GENERATED : 5/12/2016 10:21:46 AM		TEXAS INSTRUMENTS



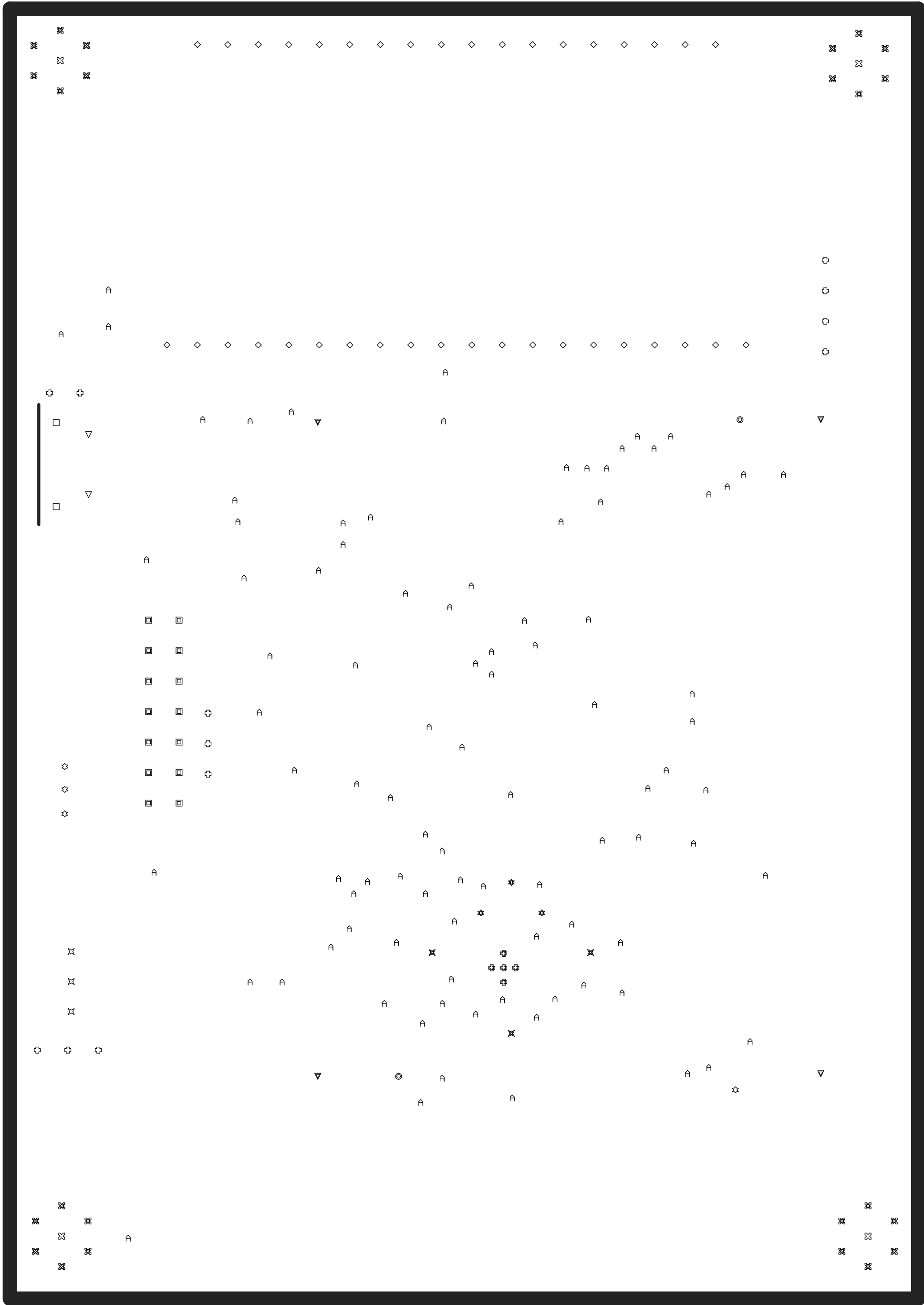
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = 11 Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Layer	GENERATED : 5/12/2016 10:21:48 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bottom Solder Mask	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Solder Mask	GENERATED : 5/12/2016 10:21:49 AM		TEXAS INSTRUMENTS



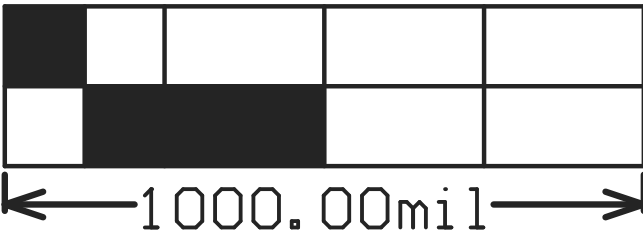
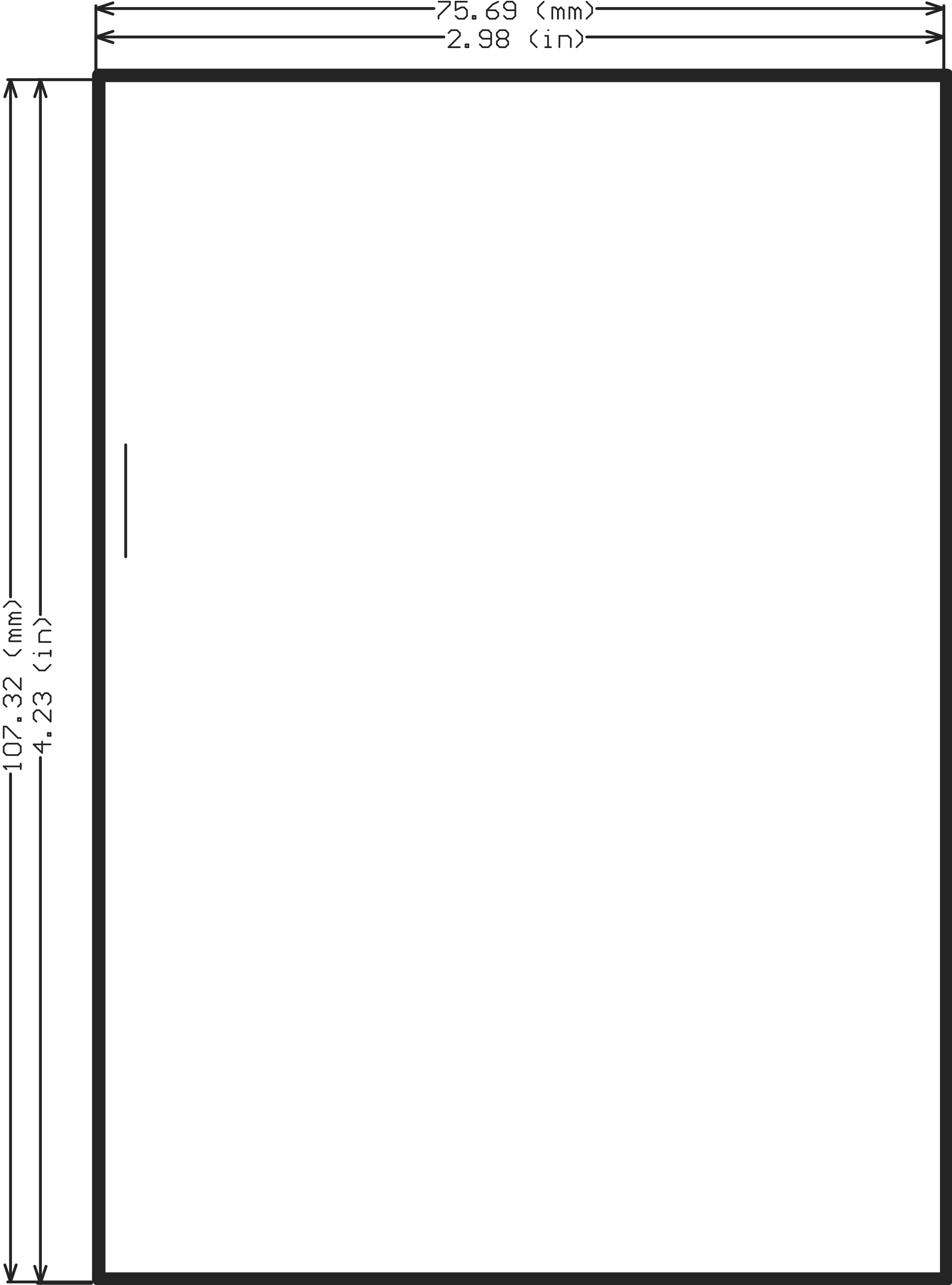
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Bot Board Outline	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Bottom Overlay	GENERATED : 5/12/2016 10:21:51 AM		TEXAS INSTRUMENTS



Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
⦿	2	77.95mil (1.980mm)	NPTH	Round	Top Layer - Bottom Layer	
⊕	5	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	4	17.72mil (0.450mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	24	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	
⌒	94	28.00mil (0.711mm)	PTH	Round	Top Layer - Bottom Layer	
◇	38	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	
▽	2	33.46mil (0.850mm)	PTH	Round	Top Layer - Bottom Layer	
⊕	12	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer	
▽	4	46.85mil (1.190mm)	PTH	Round	Top Layer - Bottom Layer	
◻	14	47.24mil (1.200mm)	PTH	Round	Top Layer - Bottom Layer	
☆	3	59.06mil (1.500mm)	PTH	Round	Top Layer - Bottom Layer	
✕	3	100.00mil (2.540mm)	PTH	Round	Top Layer - Bottom Layer	
⊗	4	129.92mil (3.300mm)	PTH	Round	Top Layer - Bottom Layer	
◻	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	214 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = Micro Drawing	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Drill Drawing	GENERATED : 5/12/2016 10:21:52 AM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:	REV: 1.0	SVN REV: Not In VersionControl
LAYER NAME = M2 Board Dimensions	TID #: TIDM-1CHP-DTECT-CO		
PLOT NAME = Board Dimensions	GENERATED : 5/12/2016 10:21:54 AM		TEXAS INSTRUMENTS