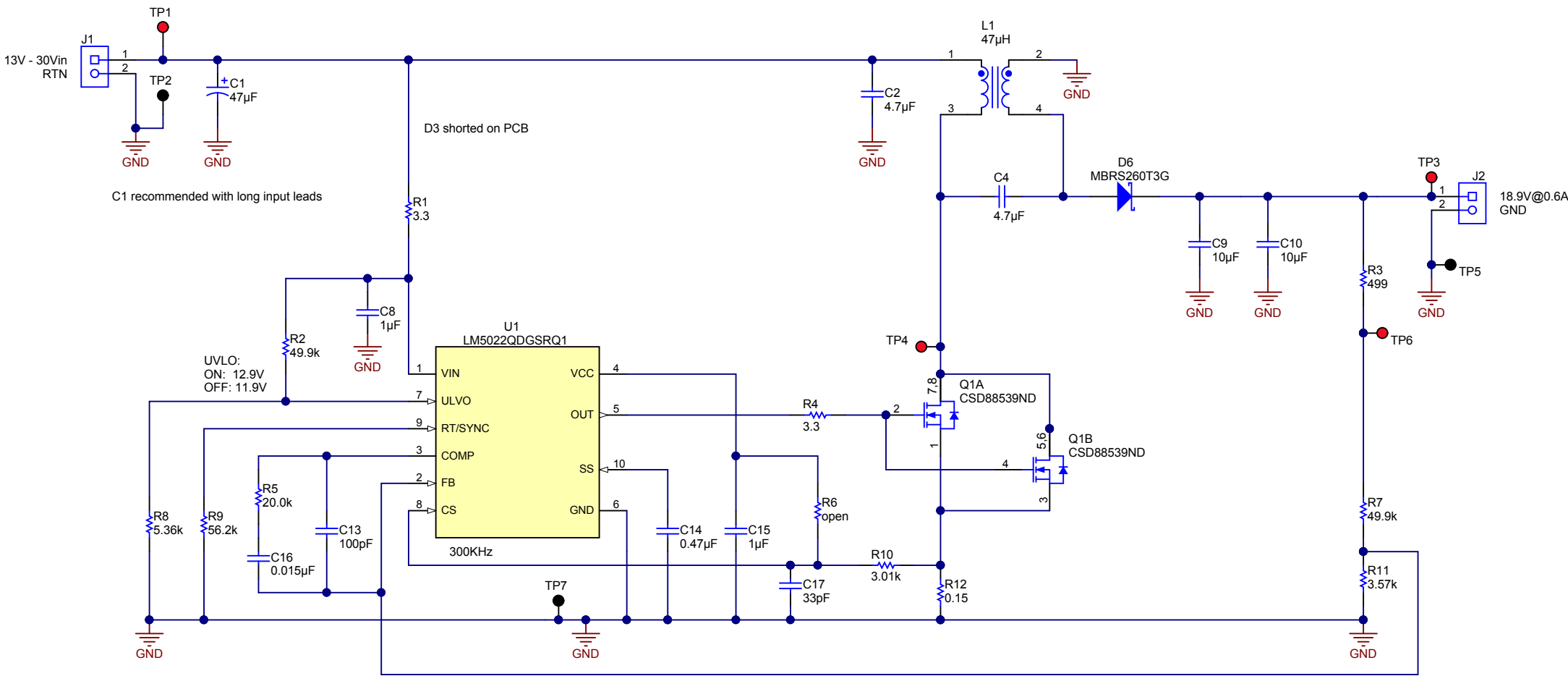


NOTES:
1) Ref Des' match PMP20128 REVA PCB.
2) Ref Des' 100 or larger are add-on parts to the PCB.
3) Modifications required on Q1.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 4/22/2016
TID #:	Project Title: LM5022 SEPIC	
Number: PMP20197	Rev: B	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 1
Drawn By:	File: PMP20197 REVB.SchDoc	Size: B
Engineer: J Betten	Contact: http://www.ti.com/support	