

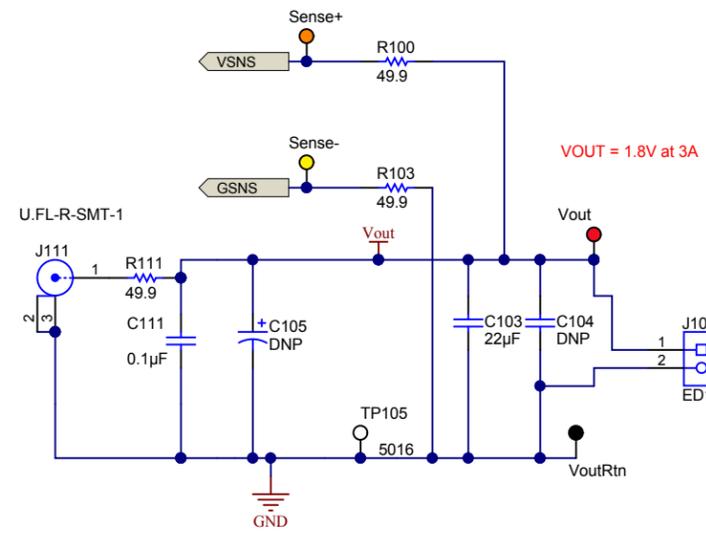
Tested, see Test Report

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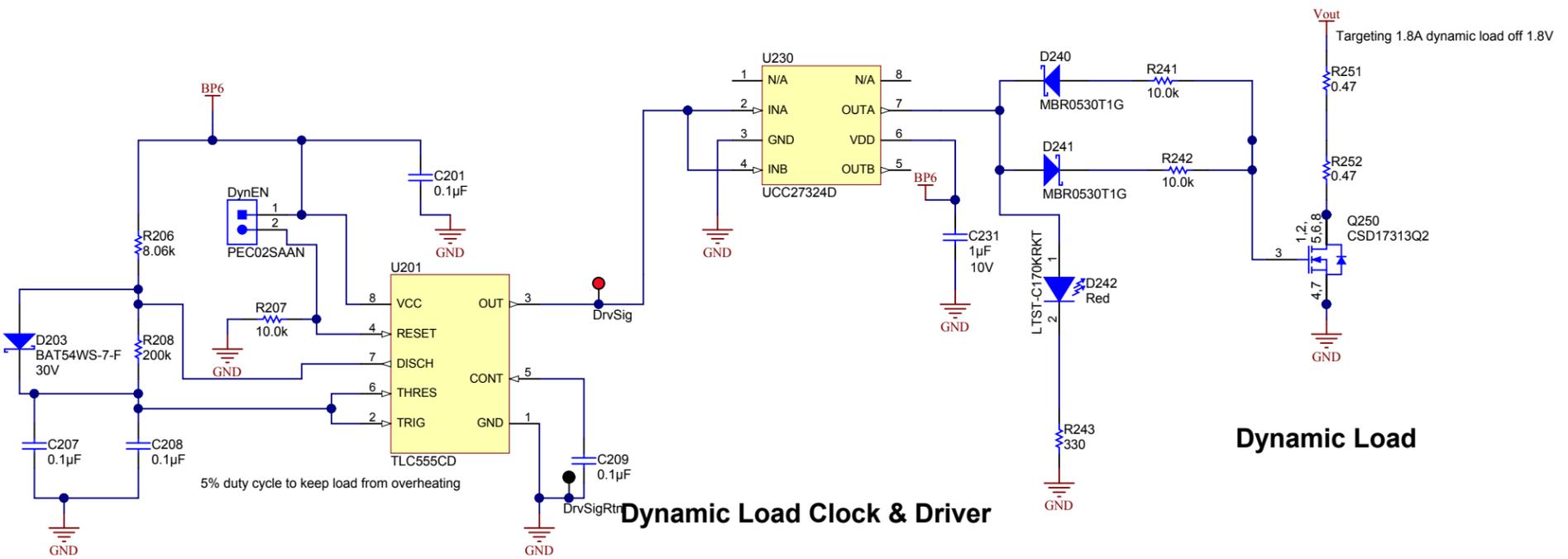
Orderable: N/A	Designed for: Public Release	Mod. Date: 7/2/2015
TID #: PMP11140	Project Title: 12Vin 1.8V 3A with PMBus	
Number: PMP11140	Rev: A	Sheet: 2 of 3
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 3
Drawn By:	File: PMP11140_TPS40400_CSD85301Q2.SchDoc	Size: B
Engineer: Josh Mandelcom	Contact: http://www.ti.com/support	



Output Power & Ripple Sense Interface

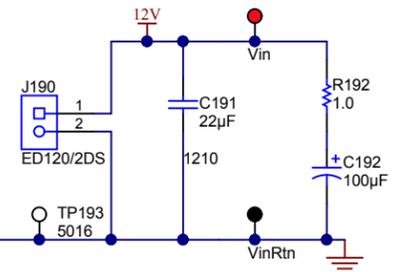


Dynamic Load Clock & Driver

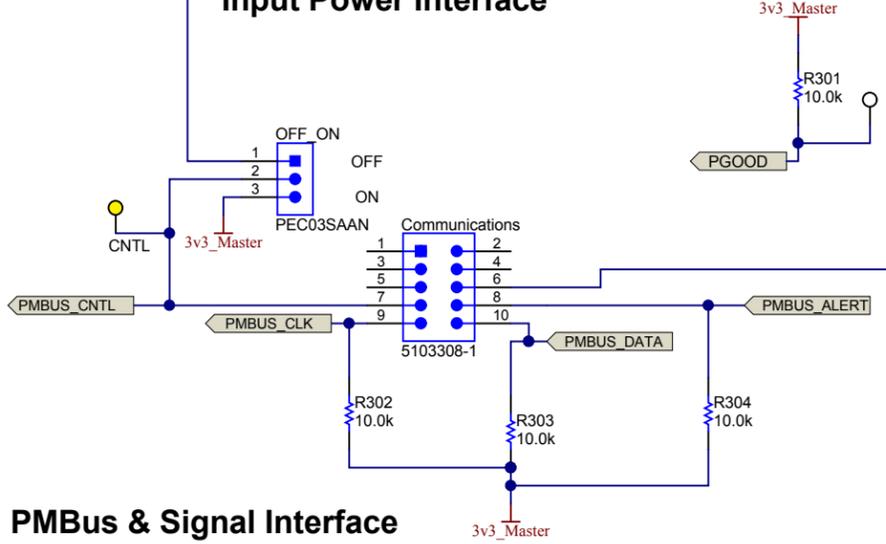


Dynamic Load

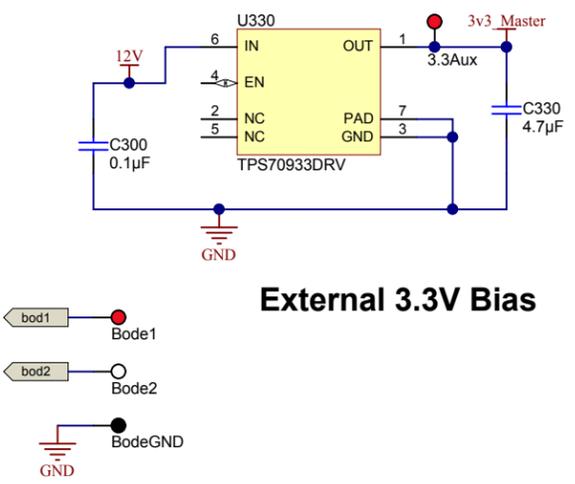
Input Power Interface



PMBus & Signal Interface



External 3.3V Bias



- ZZ2 **Assembly Note**
These assemblies are ESD sensitive, ESD precautions shall be observed.
- ZZ3 **Assembly Note**
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- ZZ4 **Assembly Note**
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

- H9 SJ-5303 (CLEAR)
- H10 SJ-5303 (CLEAR)
- H11 SJ-5303 (CLEAR)
- H12 SJ-5303 (CLEAR)

- DNP FID1
- DNP FID2
- DNP FID3

PCB Number: PMP11140
PCB Rev: A

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SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 3 of 3
Drawn By:	File: PMP11140_Hardware&TestInterface.SchDoc	Size: B
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