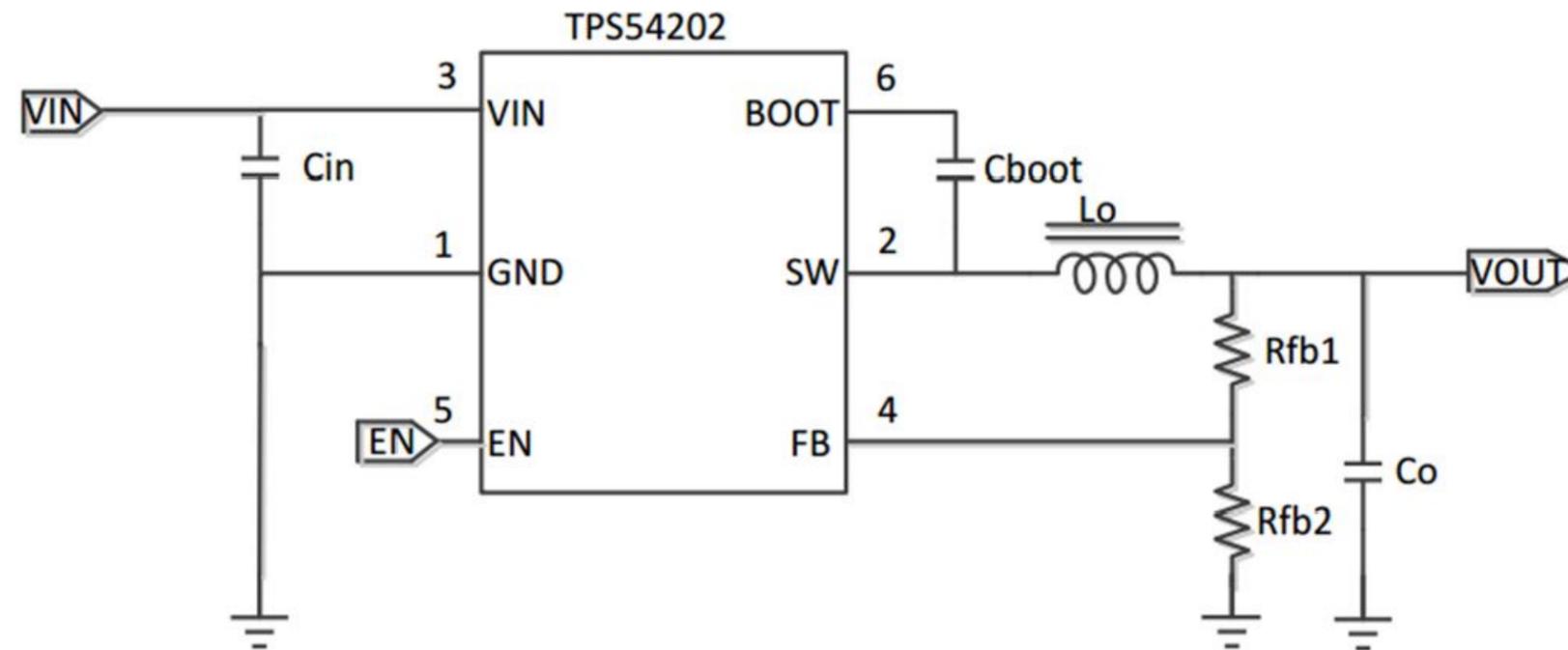


Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



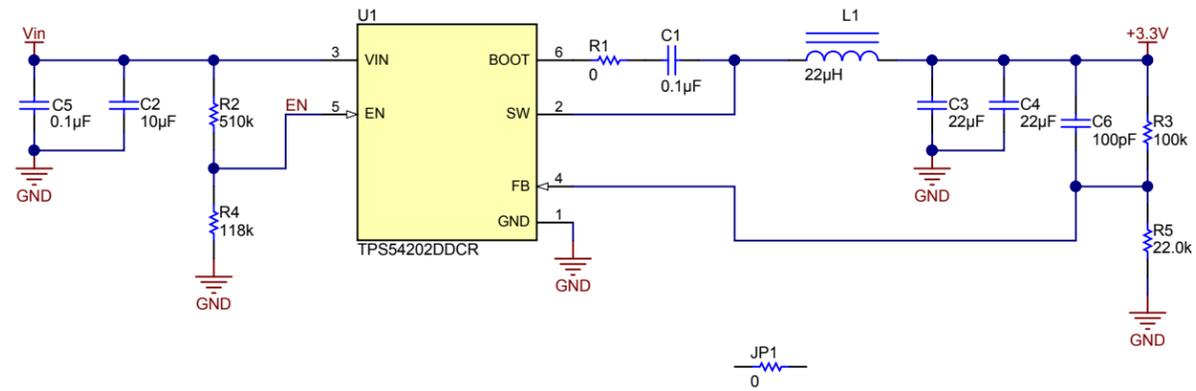
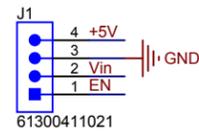
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TID #: TIDA-00949	Project Title: 3.3V 1A DC DC module with single layer and TO-24		
Number: TIDA-00949	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3	
Drawn By:	File: CoverSheet.SchDoc	Size: B	
Engineer: Jason Tao	Contact: http://www.ti.com/support		

NOTES:

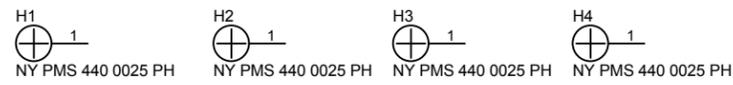
INPUT VOLTAGE: 7-V to 20-VDC, 12-VDC IS RECOMMENDED
 PEAK INPUT CURRENT: 1A

OUTPUT VOLTAGE: 3.3VDC Nominal
 OUTPUT CURRENT: 1A Nominal
 OCP THRESHOLD: 2A
 OTP THRESHOLD: 160 degree



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Number: TIDA-00949	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3	
Drawn By:	File: TIDA-00949 RevA01.SchDoc	Size: B	
Engineer: Jason Tao	Contact: http://www.ti.com/support		

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PCB Number: TIDA-00949
PCB Rev: E1

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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TID #: TIDA-00949	Project Title: 3.3V 1A DC DC module with single layer and TO-24		
Number: TIDA-00949	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 3 of 3	
Drawn By: Jason Tao	File: TID_Hardware.SchDoc	Size: B	
Engineer: Jason Tao	Contact: http://www.ti.com/support		

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