COP820CJ,COP840CJ,COP880C,COP884BC, COP888CF,COP888CL,COP888EK,COP888FH, COP888GW,COP8ACC5,COP8AME9,COP8CBE9, COP8CBR9,COP8CCE9,COP8CCR9,COP8CDR9, COP8SAA7,COP8SAC7,COP8SBR9,COP8SCR9, COP8SDR9,COP8SGE5,COP8SGE7,COP8SGG5, COP8SGH5,COP8SGK5,COP8SGR5,COP8SGR7, COP912C

AN-952 Low Cost A/D Conversion Using COP800



Literature Number: SNOA328

Low Cost A/D Conversion Using COP800

INTRODUCTION

Many microcontroller applications require a low cost analog to digital conversion. In most cases the controller applications do not need high accuracy and short conversion time. This appnote describes a simple method for performing analog to digital conversion by reducing external elements and costs.

PRINCIPLE OF A/D CONVERSION

The principle of the single slope conversion technique is to measure the time it takes for the RC network to charge up to the threshold level on the port pin, by using Timer T1 in the input capture mode. The cycle count obtained in Timer T1 can be converted into voltage, either by direct calculation or by using a suitable approximation.

Figure 1 shows the block diagram for the simple A/D conversion which measures the temperature.

BASIC CIRCUIT IMPLEMENTATION

Usually most applications use a comparator to measure the time it takes for a RC network to charge up to the voltage level on the comparator input. To reduce cost, it is possible to switch both inputs as shown in *Figure 2*.

Port G3 is the Timer T1 input. Ports G2/G1 are general purpose I/O pins that can be configurated using the I/O configurations (push-pull output/tristate). All Port G pins are Schmitt Trigger inputs. R_{LIM} is required to reduce the discharge current.

GENERAL IMPLEMENTATION

The temperature is measured with a NTC which is linearized with a parallel resistor. Using a parallel resistor, a linearization in the range of 100 Kelvin can be reached. The value of the resistor can be calculated as follow:

$$R_P = R_{tm} * (B - 2T_m)/(B + 2T_m)$$

- Rtm Value of the NTC at a medium temperature
- T_m Medium Temperature
- B NTC-material constant

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The linearization reduces the code, improves the accuracy and the tolerance of the NTC-R network (e.g. NTC = 100 k $\Omega \pm 10\%$, R = 12 k $\Omega \pm 1\%$, NTC//R $\pm 2\%$). Using that method the useful range does not cover the whole operating temperature range of the NTC.

GENERAL ACCURACY CONSIDERATIONS

Using a single slope A/D conversion the accuracy is dependent on the following parameters:

- Stability of the Clock frequency
- Time constant of the RC network
- Accuracy of the Schmitt Trigger level
- Non-linearity of the RC-network

Figure 3. The maximum failure that appears when a sawtooth is generated without using a current source. In the current application the maximum failure would be more than 15% without using methods for reducing the non-linearities of RC-network/NTC-network.

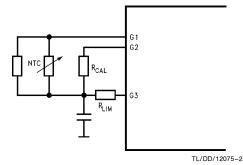
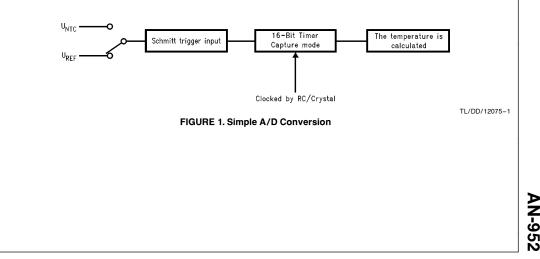
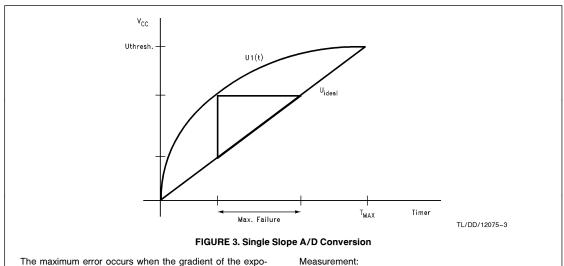


FIGURE 2. Basic Circuit Implementation



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Calculation:

triangle

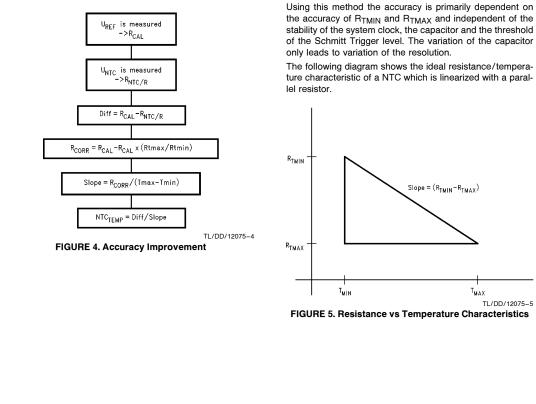
- Calculate the slope

- Calculate the actual temperature

The maximum error occurs when the gradient of the exponential function (RC) equals the gradient of the straight line (counter).

To reduce the error that is caused by the non-linearity of the RC-network a offset should be added to the calculated value. The offset reduce the failure to the middle.

Further, the accuracy can be improved by using a relative measurement method. The following diagram shows the method.

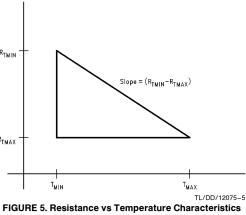


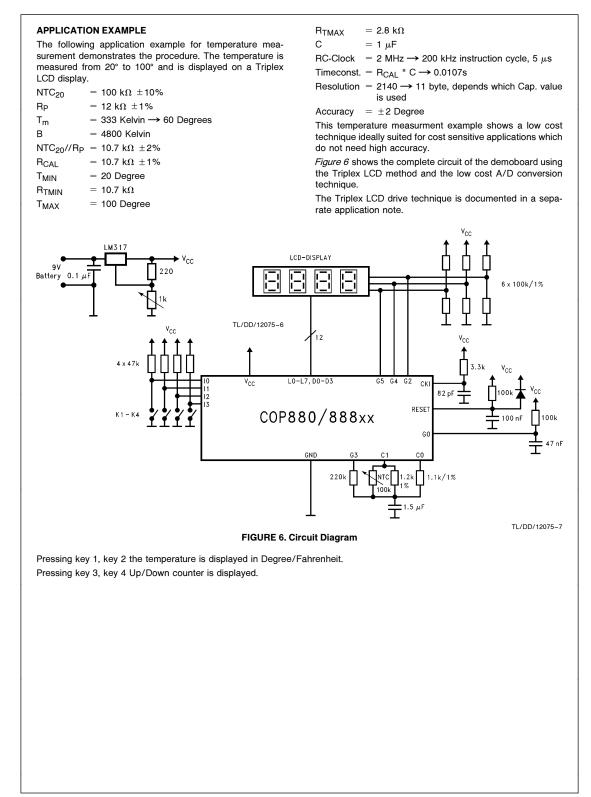
stability of the system clock, the capacitor and the threshold of the Schmitt Trigger level. The variation of the capacitor only leads to variation of the resolution. The following diagram shows the ideal resistance/temperature characteristic of a NTC which is linearized with a paral-

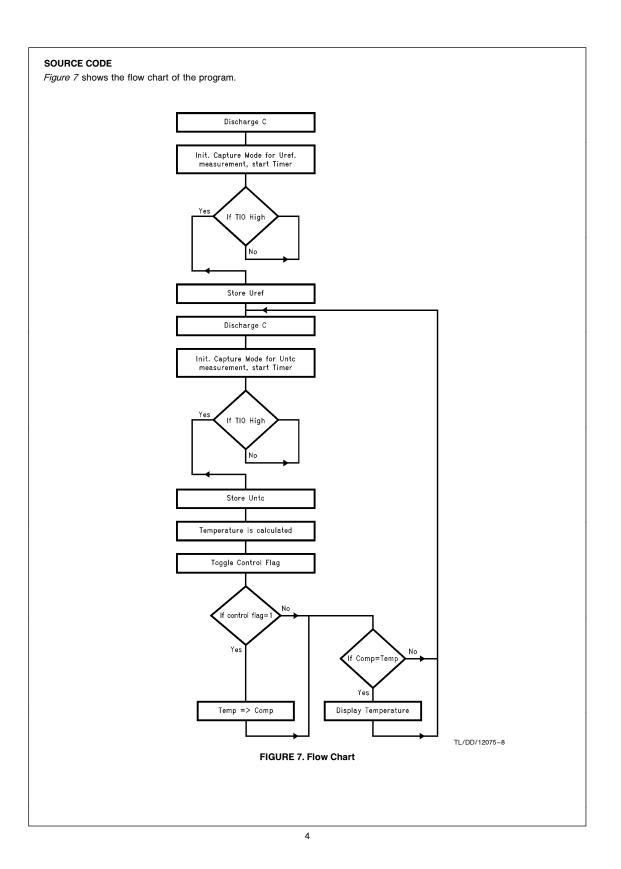
- Timer Capture mode: R_{CAL} * C is measured

- Timer Capture mode: R_{NTC//R} * C is measured

— Build the vertical-component ($R_{TMIN} - R_{TMAX}$) of the







$ROM = 450 B_{2}$	te; yte; Optimization	on is possible about 50 byte if the B - pointer consistent is used!	
•*************************************	****	****A/D-CONVERSION************************************	
, .******	*****	***VAR. DECLARIATION****************************	
, .SECT REGP/		VAR. DECEARIA NON	
COUNTI:	.DSB 1		
COUNT2:	.DSB 1		
;			
SECT BASEF	AGE,BASE		
ZL:	.DSB 1	;TEMPORARY	
YL:	.DSB 1	;TEMPORARY	
;			
SECT RAMP			
CALIBLO: CALIBHI:	.DSB 1 .DSB 1	;CALIBRATION-VALUE	
NTCLO:	.DSB 1	;NTC-VALUE	
NTCHI:	.DSB 1		
TEMP:	.DSB 2	;TEMPVALUE	
	.DSB 2		
COMPL:	.DSB 1		
COMPH:	.DSB 1		
CONTROL:	.DSB 1	;STATUS REGISTER	
		******START MAIN PROGRAM************************************	
MAIN: LD	SP,#06F	;INIT SPACKPOINTER	
JSR ISP	DISCH CALB	;DISCHARGE C (A/D-CONVERSION) ;INIT CAPTURE MODE FOR UREF. MEASURMENT	
POLL: IFBIT		POLL - MODE (TIO - PORT)	
	CAL	(10 - 10 KI)	
	POLL		
CAL: LD			
	CAPTH	;STOP TIMER, STORE CAPTURE VALUE	
	CALCR	SLOPE IS CALCULATED	
NEW: JSR	DISCH	;DISCHARGE C (A/D-CONVERSION)	
JSR	NTC	INIT CAPTURE MODEFOR UNTC MEASURMENT	
POLL1: IFBIT	3,PORTGP	;POLL-MODE	
ЛР С			
JP P			
CAL1: LD			
	CAPTH	STOP TIMER, STORE CAPTURE VALUE	
	CALCN	TEMPERATURE IS CALCULATED	
	DISCH	;DISCHARGE C (A/D-CONVERSION)	
JSK I	DCHECK	;REDUCE THE DISPLAY FLICKERING	
IMD			
JMP ENDSECT	******	*******************	
ENDSECT	*****		TL/DD/12075
ENDSECT			
.ENDSECT			
ENDSECT			

SECT CODELROM ;THIS ROUTINE IS REQUIRED TO REDUCE THE NOICE ON THE LINE AND THE ; DISPLAY FLICKERING. .SECT CODE1,ROM DCHECK: ;COMPARE TWO VALUES, IF EQUAL THEN LD A,CONTROL ;DISPLAY IT, OTHERWISE THE OLD VALUE XOR A,#080 ;IS DISPLAYED X A,CONTROL IFBIT 7, CONTROL JSR SAVE ;TEMP. SAVE JSR COMP :COMPARE RET ****************** ; HANDLER FOR CAPTURE MODE CAPTH: RBIT TPND,PSW RESET TIMER PENDING RBIT TRUN, PSW ;STOP TIMER LD A,#0FF SC SUBC A, TAULO STORE THE CAPTURED VALUE X A,[B+] LD A,#0FF SUBC A, TAUHI STORE THE CAPTURED VALUE X A,[B+] RET ; CALIBRATION SUBROUTINE, UREF IS MEASURED CALB: **RBIT 3, PORTGD RBIT 3.PORTGC :TRISTATE TIO** LD PORTCD,#00 LD PORTCC,#00 ;TRISTATE PORT C ;INIT CAPTURE MODE, HIGH SENSITIVE (MACRO) T1CAP HIGH LD B,#CALIBLO SBIT 0, PORTCD ;CONFIGURE C0 TO OUTPUT HIGH SBIT 0.PORTCC :CHARGE CAP. SBIT TRUN, CNTRL ;START TIMER CAPTURE MODE RET ****************** ; NTC SUBROUTINE, UNTC IS MEASURED NTC: **RBIT 3, PORTGD RBIT 3, PORTGC** ;TRISTAT TIO LD PORTCD,#00 LD PORTCC,#00 :TRISTATE PORT C ;INIT CAPTURE MODE, HIGH SENSITIVE (MACRO) TICAP HIGH LD B,#NTCLO SBIT 1, PORTCD ;CONFIGURE C1 TO OUTPUT HIGH SBIT 1, PORTCC ;CHARGE CAP. SBIT TRUN, CNTRL ;START TIMER CAPTURE MODE RET

TL/DD/12075-10

******************* ;DISCHARGE - ROUTINE DISCH: LD PORTCD,#000 LD PORTCC,#000 RBIT TIO, PORTGD ; DISCHARGE CAP. SBIT TIO, PORTGC LD COUNT1,#H(500) ;DISCHARGE TIME LD COUNT2,#L(500) ;DELAY ROUTINE FOR DISCHARGE TIME JSR C1 RET *********************** THIS SUBROUTINE CALCULATES THE SLOPE THE FOLLOWING CALCULATIONS ARE DONE ;KORR=CALIB/11KOHM (RCALIB.=11KOHM) ;KORR=KORR*2,8KOHM (T=100 DEGREE, RNTC=2,8KOHM) ;CALIB=CALIB-KORR ;DIV=CALIB\80 (TEMPRANGE=80 DEGREE, 100-20), SLOPE IS CALCULATED CALCR: ;KORR=CALIB/11KOHM LD ZL,#L(110) LD ZL+1,#H(110) LD A, CALIBLO X A,YL LD A,CALIBHI X A,YL+1 ;SUBROUTINE BINARY DIVIDE 16 BIT BY 16 BIT JSR DIVBIN16 LD A.YL X A,KORRL ;KORR=KORR*28 LD A,KORRL X A,ZL LD A,#28 X A,YL JSR MULBIN8 SUBROUTINE MULTIPLY TWO 8 BIT VALUES LD A,YL X A,KORRL LD A,YL+1 X A,KORRL+1 ************* ;KORR=CALIB-KORR LD B,#CALIBLO LD A,[B+] SC SUBC A,KORRL X A,KORRL LD A,[B] TL/DD/12075-11

SUBC A,KORRL+1		
X A.KORRL+1		
;*************************************	*********	
LD ZL,#L(80)		
LD ZL+1,#H(80)		
LD A,KORRL		
X A,YL		
LD A,KORRL+1		
X A,YL+1		
JSR DIVBIN16 LD A.YL	SUBROUTINE BINARY DIVIDE 16 BIT BY 16 BIT	
X A,DIV		
RET		
,	*******	
	ULATES THE TEMPERATURE	
;THE FOLLOWING CALCUI ;TEMP=CALIB-NTC	LATIONS ARE DONE	
;TEMP=TEMP/DIV		
;ADD OFFSET 20 DEGREE		
;CONVERSION FROM HEX		
,	******************	
;TEMP=CALIB-NTC		
CALCN: LD B,#CALIBLO LD A,[B+]		
SC		
SUBC A,NTCLO		
X A,TEMP		
LD A,[B]		
SUBC A,NTCHI		
IFNC JMP ERR		
X A,TEMP+1		

;TEMP=TEMP/DIV		
LD A,TEMP		
X A,YL		
LD A,TEMP+1 X A,YL+1		
LD A, DIV		
X A,ZL		
CLRA		
X A,ZL+1		
JSR DIVBIN16	SUBROUTINE BINARY DIVIDE 16 BIT BY 16 BIT;	
LD A,YL ADD A,#20	ADD TEMPERATURE OFFSET	
IFGT A,#56	;IF TEMPERATURE IS HIGER THAN 56 DEGREE THEN	
JSR CORR	;ADD CORRECTION. OFFSET	
***************************************	*******************	
		TL/DD/12075-12

,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	TO BCD X A.Z	CONVERSIC	JN	
	LD A,			
	IFGT A	, #100	;IF TEMPERATURE IS MORE THAN 100 DEGREE THEN	
JP ERR JSR BINBCD			;ERROR	
			;SUBROUTINE BINARY TO BCD CONVERSION;	
	LD A,BCDLO X A,TEMP LD A,BCDLO+1 X A,TEMP+1			
	RET			
ERR:	LD A,#00E X A,TEMP CLR A		;ERROR MESSAGE IS DISPLAYED	
		EMP+1		
	RET			
·*****	******	*****	*************************	
COMP		A,COMPL	;IF THE LAST BOTH MEASURMENTS ARE EQUAL	
	SC	A 1777 M 40	;THEN DISPLAY	
	IFEO	A,TEMP		
	JP	DISPLAY		
	RET		OTHERWISE DISPLAY THE OLD VALUE	
DISPL.	AY:LD	A,TEMP		
	X	A,PB+2		
M1:	LD X	A,TEMP+1		
WIT:		A,PB+3 LCDDR	;UPDATE THE DISPLAY	
	JSR	DEL	;DELAY TIME	
	RET			
·*****	******		*************	
		A,TEMP	;TEMPORARY SAVE	
SAVE:		A COMPI		
SAVE:	Х	A,COMPL A TEMP+1		
SAVE:		A,COMPL A,TEMP+1 A,COMPH		
SAVE:	X LD	A,TEMP+1		
	X LD X RET	A,TEMP+1 A,COMPH	*****	
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	**********	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
SAVE: ;*****	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	******	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	**********	TL/DD/12076
	X LD X RET	A,TEMP+1 A,COMPH	************	TL/DD/12075-1
	X LD X RET	A,TEMP+1 A,COMPH	*****************	TL/DD/12075
	X LD X RET	A,TEMP+1 A,COMPH	*****	TL/DD/12075-

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