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<td>ADC32RFxxEVM</td>
<td>B</td>
<td>TIDA-01016</td>
<td>D</td>
<td>Thursday, October 15, 2015</td>
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Place close to LMK

Place close to LMK

Place near Vc pin of Y1
R20 and C31 form voltage divider set the center frequency voltage for PLL2 reference

Y1 CMOS version OUTPUT on PIN 4

+3.3V_CLK

Place close to LMK

+3.3V_CLK

+3.3V_CLK

Place close to LMK

+3.3V_CLK

Place close to LMK

+3.3V_CLK
Make sure this line does not hold LMK high before LMK can be programmed for SDO.
MECHANICAL PARTS

BARE BOARD, ADC32RFxx

STANDOFF, FEMALE, 4-40 X 1 3/16", AL

MT1

SCREW, 4-40 X 3/4", PHIL, SS

MT2

SCREW, 4-40 X 3/4", PHIL, SS
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