Input Connector

3.3V DVDD and 5.3V AVDD Generation

Reference Voltage Generation

CM Voltage Generation

*Internal Vref = 0.8V for both the channels (for calculation of feedback resistors)
Series capacitors are for DC blocking.
If the NFE has intrinsic DC blocking caps,
place the zero ohm resistors in parallel.

Bandpass Filter for I-Channel
Series capacitors are for DC blocking.
If the AFE has inbuilt DC blocking caps, open the external DC blocking cap and place the zero ohms resistors in parallel.
fHP = 20Hz
fLP = 20kHz
Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

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Connection to PHI Board for data capture

EEPROM for ADC Identification
1. **Variant/Label Table**

<table>
<thead>
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<th>Variant</th>
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</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>ChangeMe!</td>
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<tr>
<td>002</td>
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</tr>
</tbody>
</table>

2. **Assembly Notes**

- **ZZ1**: These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
- **ZZ2**: These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- **ZZ3**: These assemblies are ESD sensitive. ESD precautions shall be observed.

3. **PCB Label**

- Variant: TIDA-01351
- PCB Rev: E1

4. **PCB Number**: TIDA-01351

5. **PCB Rev**: E1

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