NOTE:
T1, T2, T3, T4 are TC1-1-43+ on ADC32RF80EVM
Value shown for ADC32RF45EVM
DNI = DO NOT INSTALL
Place close to LMK

Y1 CMOS version OUTPUT on PIN 4

Place near Vc pin of Y1

R30 and C31 form voltage divider

set the center frequency voltage for PLL2 reference

Place close to LMK

MATCHED LENGTH

MATCHED LENGTH

Place close to LMK

Shared Pad
Make sure this line does not hold LMK high before LMK can be programmed for SDO.
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