PCB Number: TIDA-01624
PCB Rev: B

VDD Decoupling Caps

Wireless MCU

Wake Up Switch

Temperature Sensing & I2C

VDDS/VCC Decoupling Caps

External Oscillators

JTAG

RF Signal Path

Internal DC/DC Buck Converter is enabled in the AON_SYSCTL: PWTCTL register, set the DCDC_ACTIVE bit
Internal Bias is enabled for the CC2640

SDA/SCL provided by GP10 of CC2640, use correct PORTID in the IOC/IOCFlags register
SCL → PORTID 14
SDA → PORTID 13

Brandon Fisher
Contact: http://www.ti.com/support

Sheet Title:

Designed for: Public Release

Sheet: of 6.98k

Mod. Date:

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Logo3
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