PMP5922 rev C is what TPS59610EVM-732 is based upon. Below are additional waveforms for each of the 5 switchers on the board. But first is shown a detailed thermal picture of the highest current switcher, the 1.8V at 5A.

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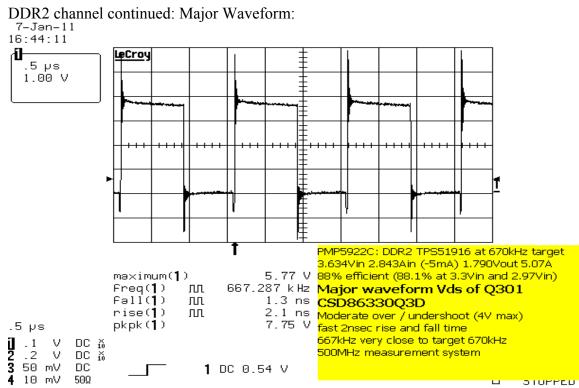
DDR2 channel:	TPS51916 & CSD86330Q3D	Pages 2-4
CPU waveforms :	TPS51610 & CSD86330Q3D	Page 5
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1.05V channel:	TPS53219 & CSD86330Q3D	Pages 7-8
1.2V channel :	TPS53311	Pages 9-10

DDR2 channel 1.8V 5A TPS51916 & CSD86330Q3D off 3.3V

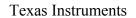
Full load switching Thermal image: 0.9V linear not loaded PMP5922C: TPS51916 DDR2 switcher only loaded 667kHz 3.634Vin 2.843Iin 1.790Vout at 5.07A Chokes MPT420-R47 x2 Chokes hottest at 61 & 58 degrees Celsius; dual switch CSD86330Q3D at 44 degrees C; ambient at 23-25 deg. C

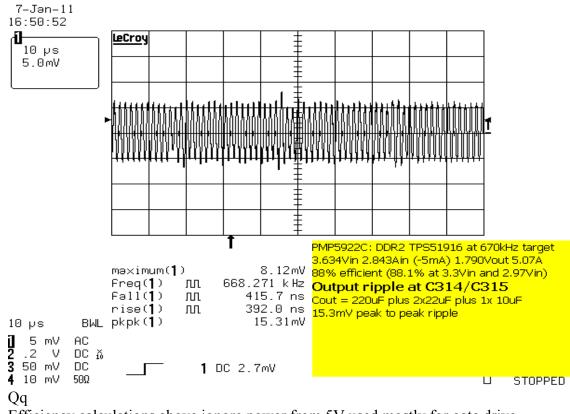


Texas Instruments

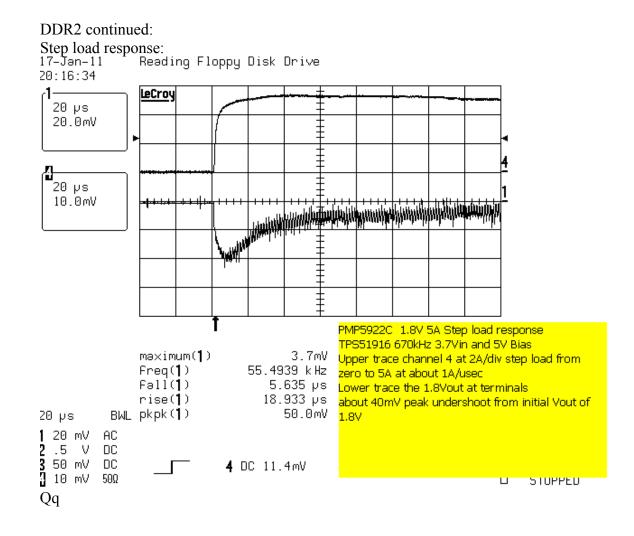


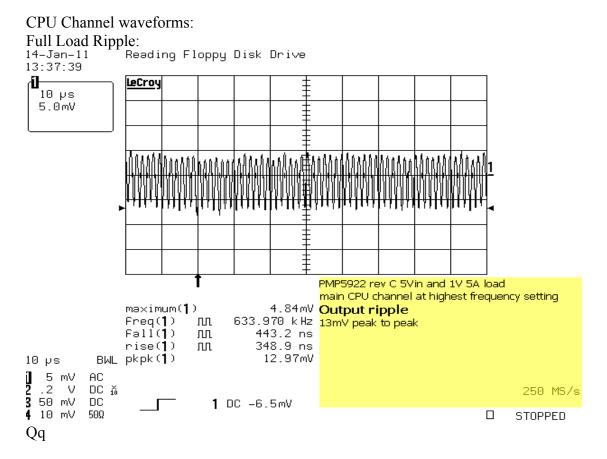
Output ripple:



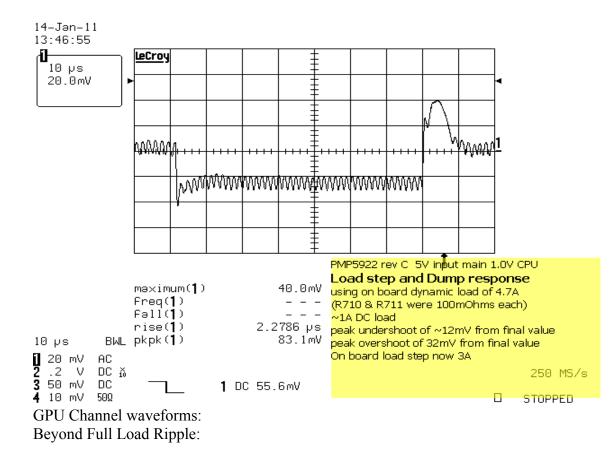


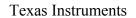
Efficiency calculations above ignore power from 5V used mostly for gate drive.

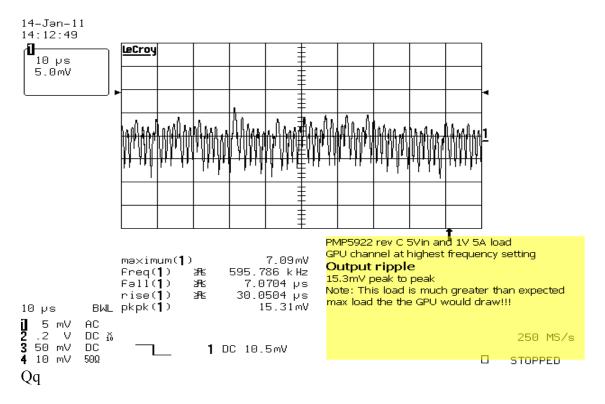




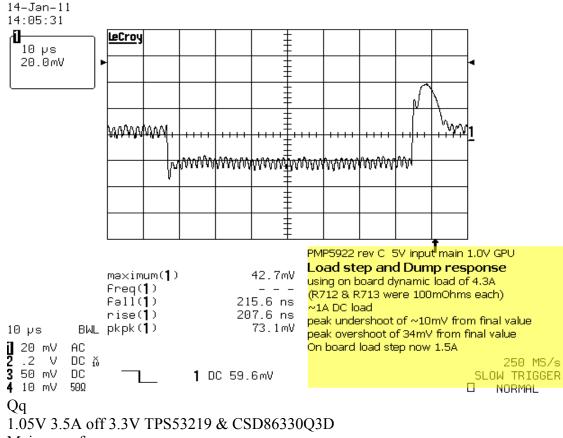
Load step & dump:



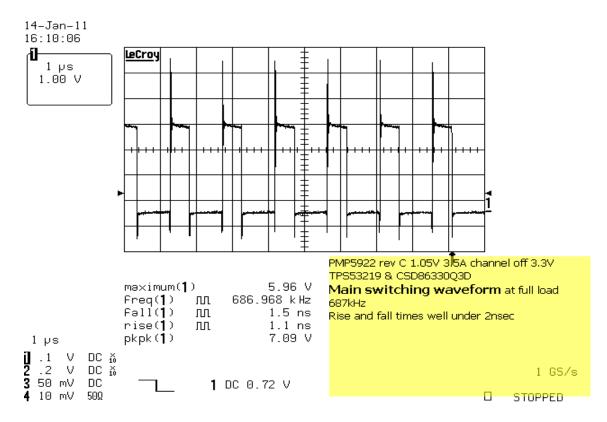




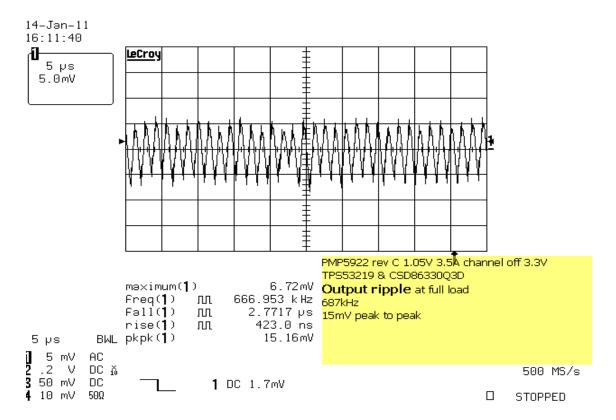
Dynamic load: Note: Step and dump well above max expected application load



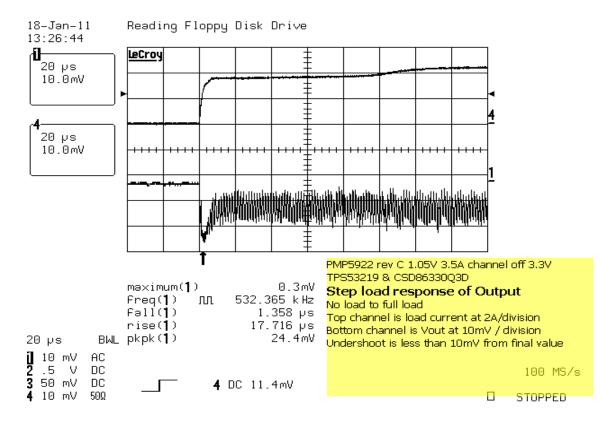
Main waveform:



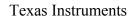
Output ripple:

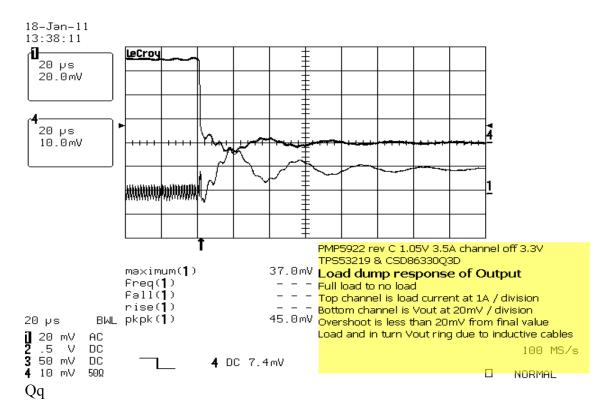


1.05V 3.5A off 3.3V TPS53219 & CSD86330Q3D continued: Step load response:



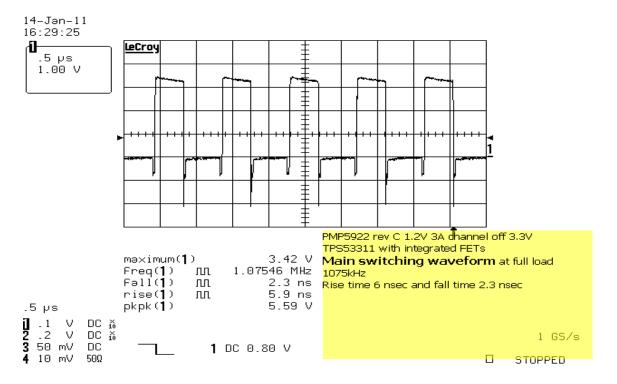
Load dump response:



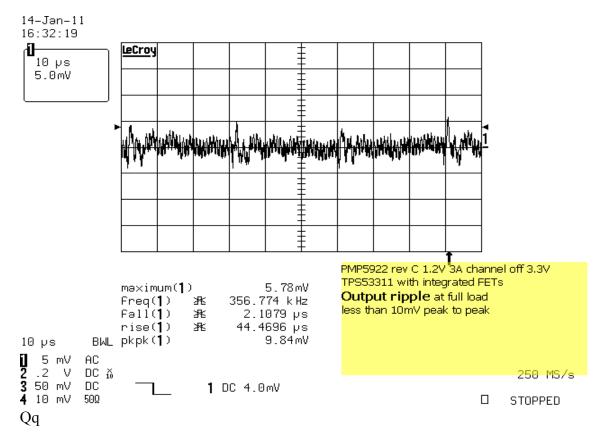


1.2V Channel: TPS53311 with integrated FETs Main waveform:

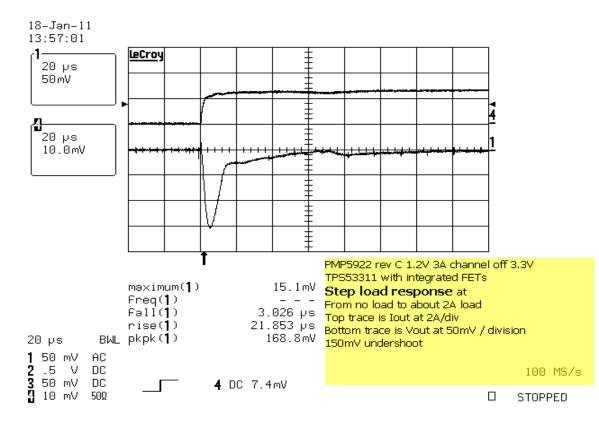




Output ripple:

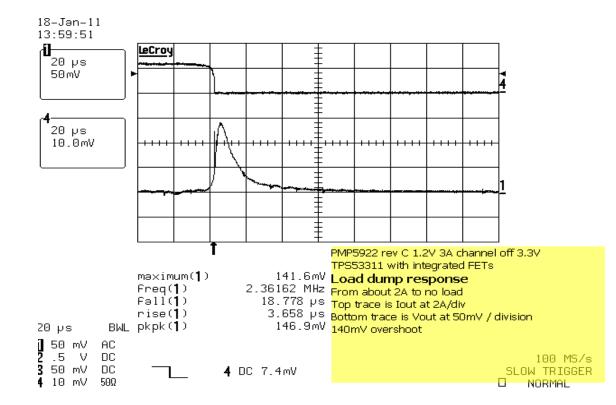


1.2V Channel: TPS53311 with integrated FETs continued: Step load response:



Load dump response:





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