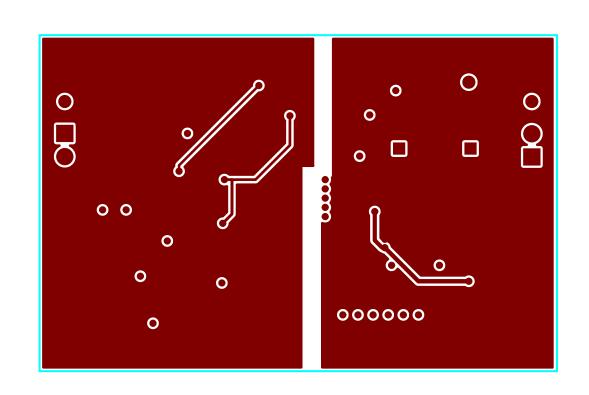
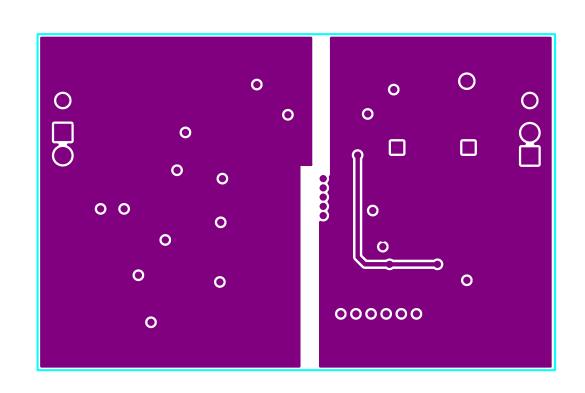


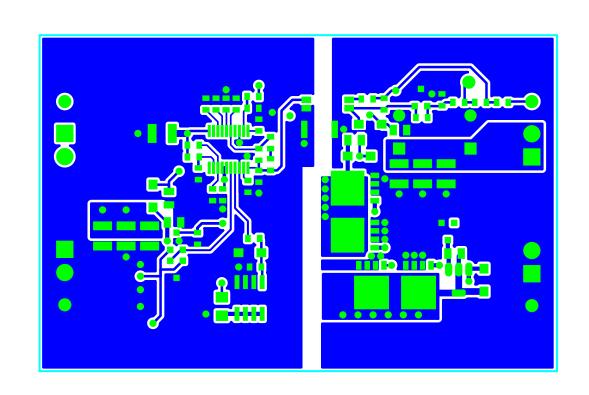
Γ	TEYASI	NSTRUMENTS	<u> </u>	Co	pper L	ayer Nar	ne	Silks	creen	SN	lask	PΜ	lask	Asse	mbly	Drill Drawing
L	ILAAJI	INSTINUINENT		Top	Inte	ernal	Bot	Top	Bot	Тор	Bot	Top	Bot	Тор	Bot	51111 51aming
E	Roard No. PMP67		Rev.	L1												
Da	te: {Start Date}	Filename: PMP6753 REVA	Engineer: Brian K		PCB	Dsgnr: Bri	ian K.	Modi	fied Date: {	(Modification	Date}				Software F	PADs v9.2



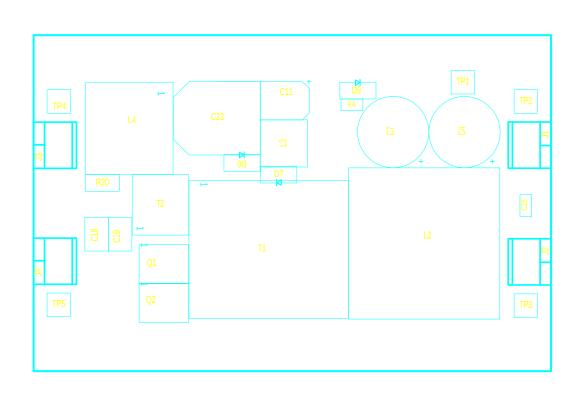
	TEYASI	NSTRUME	JTS		Co	opper L	ayer Nan	ne	Silks	creen	SN	lask	PΝ	1ask	Asse	embly	Drill Drawing
L	ILAASI	NOTIVONE	V13		Top	Int	ernal	Bot	Тор	Bot	Top	Bot	Top	Bot	Top	Bot	51111 51GITING
Во	ard No. PMP67	753	Rev.			L2											
Date	: {Start Date}	Filename: PMP6753 REV	4	Engineer: Brian K.		PCI	Bri Bri	an K.	Modif	ied Date:	(Modification	Date}				Software	PADs v9.2



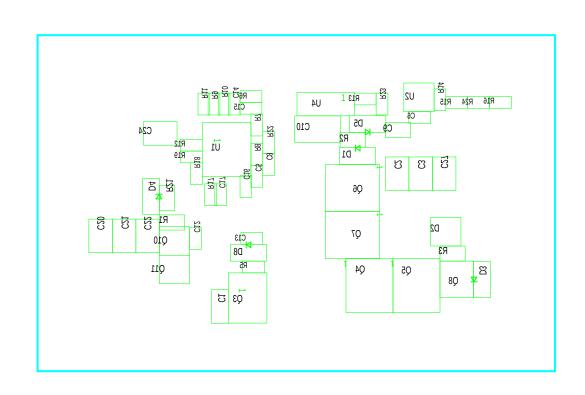
Г	TEXASI	NSTRUMENT:	<u> </u>	Co	opper L	ayer Nan	ne	Silks	creen	SM	lask	PΝ	lask	Asse	embly	Drill Drawing
	ILAAJI	INSTINUINE		Тор	Inte	ernal	Bot	Тор	Bot	Тор	Bot	Top	Bot	Top	Bot	51111 Bitaning
В	PMP67	753	Rev. A			L3										
Dat	e: {Start Date}	Filename: PMP6753 REVA	Engineer: Brian I		PCB	Dsgnr: Bri	an K.	Modi	fied Date:	(Modification	Date}				Software	PADs v9.2



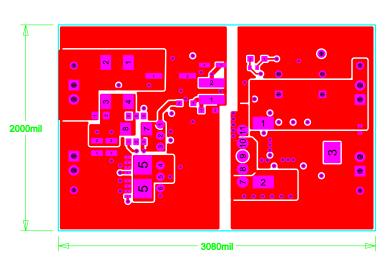
	TEYASI	NSTRUMENT:	ς	Co	pper L	ayer Nan	ne	Silks	creen	SN	lask	PΜ	lask	Asse	mbly	Drill Drawing
L	ΙΕΛΑΟΙ	NOTIVONILIVI		Тор	Inte	ernal	Bot	Top	Bot	Top	Bot	Top	Bot	Тор	Bot	Dim Diaming
E	Board No. PMP67	753	Rev.				L4									
Da	ate: {Start Date}	Filename: PMP6753 REVA	Engineer: Brian K		PCE	Bri Bri	an K.	Modi	fied Date:	(Modification	Date}				Software F	PADs v9.2



ſ	TEXASI	NSTRUMENTS	3	Cop	per La	ayer Nan	ne	Silks	creen	SN	lask	PΜ	ask	Asse	mbly	Drill Drawing
L	ILAAJI	INSTINUINE		Тор	Inte	rnal	Bot	Top	Bot	Тор	Bot	Тор	Bot	Тор	Bot	Dim Diaming
	Board No. PMP67	753	Rev. A	L1										TA		
ı	Date: {Start Date}	Filename: PMP6753 REVA	Engineer: Brian K		PCB I	Dsgnr: Bri	an K.	Modif	ied Date: {	Modification	Date}				Software F	PADs v9.2



Γ	TEYASI	NSTRUMENT:	3	Co	pper	Layer Nan	ne	Silks	creen	SN	lask	PΜ	ask	Asse	mbly	Drill Drawing
L	ILAAJI	NOTIVONILIVIA		Тор	In	ternal	Bot	Тор	Bot	Top	Bot	Top	Bot	Тор	Bot	51111 51CHINING
	Board No. PMP67	753	Rev.				L4								ВА	
Da	ate: {Start Date}	Filename: PMP6753 REVA	Engineer: Brian K		P	CB Dsgnr: Bri	an K.	Modi	ied Date: {	(Modification	Date}				Software F	PADs v9.2



Γ	TEYASI	NSTRUMENT	ς		C	pper L	ayer Nar	ne	Silks	creen	SN	lask	PΝ	lask	Asse	mbly	Drill Drawing
L	ILAAJI	NOTIVOMENT	<u> </u>		Тор	Inte	ernal	Bot	Тор	Bot	Тор	Bot	Тор	Bot	Тор	Bot	J.III J.Q.IIIIg
Bos	rd No. PMP67	753	Rev. A		L1												FB
Date:	{Start Date}	Filename: PMP6753 REVA		Engineer: Brian K.		PCE	B Dsgnr: Br	ian K.	Modi	lied Date:	(Modification	Date}				Software	PADs v9.2

	F	ABRICATION	I CHA	RT		
FINISHED	SILKSCREEN	SOLDERMA	SK	FINISHE	D COP	PER WEIGHT
THICKNESS				EXTERNAL		INTERNAL
□ 0.031	LAYER 1	LAYER 1		☐ 1 OZ.		☐ 1 OZ.
0.062	LAYER 2	LAYER 2		2 OZ.		2 OZ.
□ 0.093	□ NONE	☐ NONE		OTHER _		OTHER
□ 0.125						
DESIGN	TRACE/GAP S	PACING		LAYER C	OUNT	
SMD	0.010/0.010			SINGLE SIDED		2 LAYER
☐ THRU-HOLE	0.008/0.007			4 LAYER		6 LAYER
■ MIX	0.006/0.006			8 LAYER		10 LAYER
				OTHER		

		OTHER
N	OTES: UNLESS	S OTHERWISE SPECIFIED
1.	MATERIAL:	ALL MATERIALS, INCLUDING BUT NOT LIMITED TO BASE LAMINATE, BONDING MATERIALS AND SOLDERMASK COATINGS FORMING THE FINISHED PRINTED CIRCUIT BOARD SHALL MEET
2.	BASE LAMINATE:	UL-796 REQUIREMENTS AND BE ROHS COMPLIANT AND HAVE A FLAMMABILITY OF UL94V-0. PLASTIC SHEET, LAMINATED METAL CLAD, ONE OR TWO SIDES, BASE MATERIAL NEMA TYPE FR-4 OR EQUIVALENT, W/Tg =140 Deg C OR HIGHER. MINIMUM COMPOSITION TEMP (Td) OF 320 Deg c.
		GLASS EPOXY RESIN, COPPER-CLAD IN ACCORDANCE WITH 4 LAYER STACK-UP, COMPLIANT WITH LEAD FREE PROCESS.
3.	SOLDERMASK:	SOLDERMASK OVER BARE COPPER (SMOBC) USING LIQUID PHOTO-IMAGEABLE SOLDERMASK IN ACCORDANCE WITH IPC-SM-840. COLOR: GREEN. MINOR SOLDERMASK ADJUSTMENTS TO FACILITATE PCB FAB AND OR ASSEMBLY IS ALLOWED PROVIDED NO DEFECTS ARE CREATED TO FINAL ASSEMBLY AS A RESULT.
4.	TOLERANCES:	UNLESS OTHERWISE SPECIFIED PCB TOLERANCES SHALL BE +/005 INCHES, HOLE DIAMETERS SHALL BE +/003 INCHES.
5.	PLATING:	HOLES REQUIRING PLATING, SEE HOLE CHART, TO HAVE 1 OZ. (0.0014) MIN. THK MIN. THICK COPPER.
6.	FINISH:	PLATE WITH ROHS COMPLIANT, IMMERSION SILVER PREFERRED, IMMERSION TIN OR Sn/Ag/Cu, WITH RMA FLUX, 0.0003" to .0005" THICK ALL EXPOSED AREAS
7	LECEND.	AS COATED, NO ACTIVE FLUXES ARE ACCEPTABLE.
1.	LEGEND:	IF REQUIRED, SILKSCREEN LEGEND(S) WITH WHITE NON-CONDUCTIVE EPOXY INK.
8.	MARKINGS:	BOARD MUST BEAR VENDOR'S IDENTIFICATION CODE (ETCH OR WHITE NON-CONDUCTIVE INK). LOCATION OPTIONAL.
9.	WORKMANSHIP:	BOARD IS TO BE MANUFACTURED PER IPC-A-600 CLASS 2 REQUIREMENTS OR BETTER.
10.	DOCUMENTATION:	PCB VENDOR IS REQUIRED TO RETURN ANY AND ALL DOCUMENTS SUPPLIED OR ULTIMATELY PURCHASED BY TEXTINSTRUMENTS UPON COMPLETION OF PURCHASE ORDER.
11.	DRILL SIZES:	HOLE DIAMETERS SHOWN ARE FINISHED SIZES AFTER PLATING UNLESS OTHERWISE NOTED.
12.	PANEL BORDER:	ANY METAL IN BORDER AREA INCLUDING PART NUMBER, DATECODE AND/OR REVISION LETTERS MUST BE COVERED WITH SOLDERMASK.

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13. PROCESS CHANGES:

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