

Load Disconnect for the TPS6510x

Jeff Falin

ABSTRACT

Many boost converters have an external rectifier diode. The input voltage of the TPS6510x main boost converter is connected to the output voltage when the device is disabled. Due to the direct pass from the input to the output, the converter has no short-circuit protection. This application report describes how to use a PNP transistor and some passive components to disconnect the boost converter's input voltage from the output voltage (known as load disconnect) when the device is disabled as well as when under short-circuit conditions.

Trademarks

All trademarks are the property of their respective owners.

1 Description

Figure 1 shows a portion of the schematic for the TPS65100EVM that has been modified to include PNP transistor $Q_{(LD)}$, resistor $R_{(LD)}$, and capacitor $C_{(LD)}$, all of which form the load disconnect circuit.



Figure 1. TPS65100 With Load Disconnect

1



Revision History

www.ti.com

The SUP is connected before $Q_{(LD)}$, but the feedback network and output capacitor are connected after $Q_{(LD)}$. This allows a regulated output voltage even with the use of the transistor $Q_{(LD)}$. Select $Q_{(LD)}$ to have a low saturation voltage ($V_{(SAT)}$) and high beta (B) in order to prevent degradation of efficiency. $C_{(LD)}$ is required to set a defined bias operation point for $Q_{(LD)}$, and should be between 0.1 μ F and 1 μ F. $R_{(LD)}$ should be sized according to the following equation:

$$R_{(LD)} \leq \frac{B_{(MIN)} \times (V_{(OUT1)} + V_{(SAT)} \min - V_{IN} \max - V_{(be)} \max)}{I_{OUT} \max}$$

where $B_{(MIN)}$ is the minimum beta, $V_{(SAT)}$ min min is the minimum saturation voltage, $V_{(be)}$ max is the maximum base to emitter voltage of $Q_{(LD)}$ and I_{OUT} max is the desired short-circuit current limit. The selected I_{OUT} max should be at least higher than the maximum load current in the application to allow design margin for load transient.

Figure 2 shows the output voltage (CH1, set to 10 V), the output current (CH3, set to 100 mA) and the input current (CH4) before and after a short-circuit event.



Figure 2. TPS65100 Before and After Short Circuit

The TPS6510x must be reenabled or the input voltage power cycled after a short-circuit event in order to reset the IC internal-protection circuitry and restart the device.

Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Cł	nanges from Original (May 2004) to A Revision	Page
•	Deleted references to TPS6514x throughout document.	1

IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ('TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your noncompliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products http://www.ti.com/sc/docs/stdterms.htm), evaluation modules, and samples (http://www.ti.com/sc/docs/stdterms.htm), evaluation

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2017, Texas Instruments Incorporated