Data Converters/Audio

Automotive Data Converters/Audio Solutions

6-In/6-Out, 2-DAC/2-ADC, 100-dB/93-dB, 24-bit Audio CODEC with Integrated PLL & Embedded MiniDSP

TLV320AIC3254-Q1

The TLV320AlC3254-Q1 is a very flexible low power and low voltage stereo audio CODEC with integrated miniDSP.

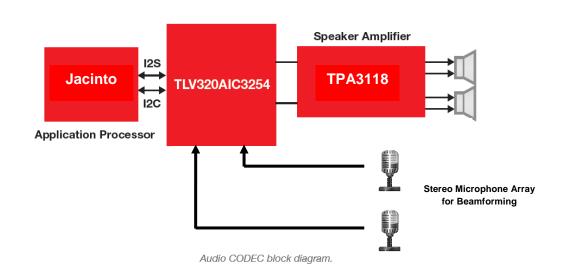
The miniDSP can run advanced audio processing algorithms, like echo and noise cancellation, while offloading host processor. The miniDSP cores are fully software controlled.

The 'AIC3254 supports PowerTuneTM technology which lets the user set the power consumption vs. SNR trade-off under any usage model.

Digital microphone support is also provided for improved system level noise immunity. It has a PLL which accepts any input clock between 512 kHz - 50MHz and excellent SNR and THD performance.

In addition, amplifier functionality such as mic bias, preamp, and stereo headphone drivers are integrated in the codec, reducing solution size and parts count.

- Stereo audio DAC with 100dB SNR.
- Stereo Audio ADC with 93-dB SNR
- Embedded miniDSP
- Stereo headphone outputs.
- Stereo line outputs.
- Programmable microphone bias
- Programmable PLL



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112/106/100-dB, 2-ch, 32-bit, Ultralow Out-of-Band noise Audio DAC with 2.1-Vrms ground centered outputs and integrated PLL

PCM5102A-Q1/PCM5101A-Q1/PCM5100A-Q1

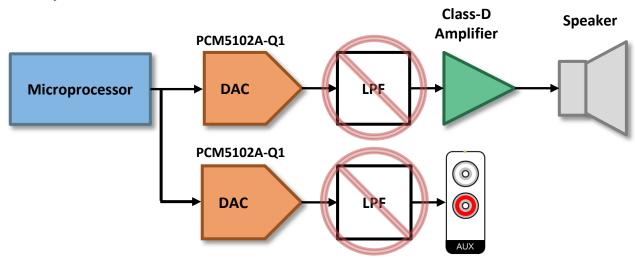
The PCM510xA-Q1 provides 2.1-VRMS ground centered outputs, allowing designers to eliminate DC blocking capacitors on the output, as well as external muting circuits traditionally associated with single supply line drivers.

The integrated PLL on the device removes the requirement for a system clock (commonly known as master clock), allowing a 3-wire I2S connection and reducing system electromagnetic interference (EMI).

The PCM510xA-Q1 family of Audio DACs uses advanced current segment architecture to greatly reduce out-of-band noise, which could make the traditional 20kHz low-pass filter a thing of the past in many automotive audio systems.

The PCM510xA-Q1 family of Audio DACs offers up to 20-dB lower out-of-band noise, reducing EMI and aliasing in downstream amplifiers and analog-to-digital converters (ADCs) from traditional 100-kHz OBN measurements all the way to 3 MHz.

- No DC blocking capacitors required.
- Ultra-Low Out-of-Band Noise; no Low Pass Filter required
- 2.1-Vrms ground centered outputs
- Integrated PLL
- Single 3.3V supply



Audio Amplifiers/Audio

Automotive Audio Amplifiers/Audio Solutions

1-Ch, Analog Input Automotive Class-D Audio Amplifier with Load Dump and I2C Diagnostics

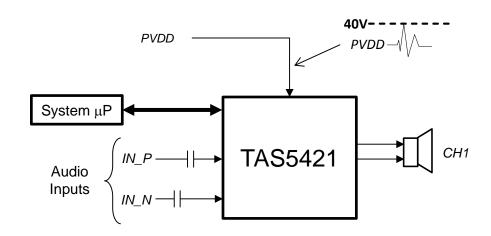
TAS5421-Q1

The TAS5421-Q1 is a mono digital audio amplifier, ideal for use in automotive emergency call (eCall), telematics, instrument cluster, and infotainment applications.

The TAS5421-Q1 provides 4-W Output Power into 4 Ω at less than 10% THD+N from a 5-Vdc supply (and up to 22 W into 4 Ω at less than 10% THD+N from a 14.4-Vdc automotive battery). The wide operating voltage range and excellent efficiency make the device ideal for start-stop support or running from a backup battery when required.

TAS5421-Q1's integrated load-dump protection reduces external voltage clamp cost and size, and the onboard load diagnostics report the status of the speaker through I2C.

- Mono Class-D Audio Amplifier
- 4-W Output Power at 5V at 10% THD+N
- 22-W Output Power at 14.4V at 10% THD+N
- 4.5-V to 18-V Operating Range
- Differential Analog Input
- Load Diagnostic Functions:
 - Open and Shorted
 Output Load
 - Output-to-Power and -Ground Shorts
- Protection and Monitoring Functions:
 - Short-Circuit Protection
 - 40-V Load Dump Protection



Data Converters/Audio

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110-dB/103-dB, 4-ch, 4-ADCs, 24-bit Audio ADC with Universal Front End, I2C or SPI control, and integrated PLL

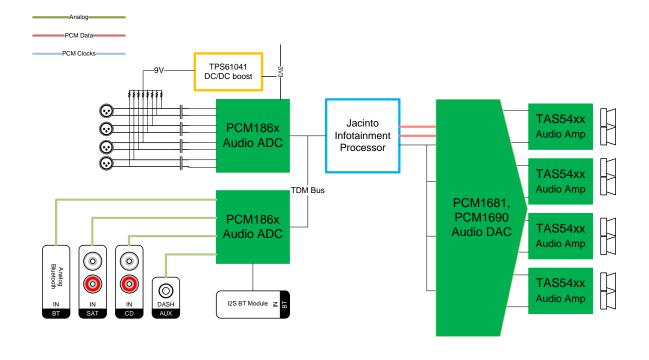
PCM1865/PCM1864

The PCM186x's highly flexible audio front end supports input levels from small-mV microphone inputs to 2.1V_{RMS} line Inputs without external resistor dividers. The PCM186x family integrates many system-level functions that assist or replace some DSP functions.

The PCM186x also differentiates itself by integrating an onchip Phase Locked Loop (PLL) that can generate real audio-rate clocks from any clock source between 1MHz and 50MHz. It can have their PLL programmed to generate audio clocks based on any incoming clock rate. For example, a 12MHz clock in the system can be used to generate clocks for a 44.1kHz system.

All these features are available using a single 3.3V power supply.

- Up to 110dB Dynamic Range
- Integrated PLL
- Universal Analog Mic Input, 2.1V_{RMS} Full Scale; No need for external resistor dividers
- Universal Front End: 2 V_{RMS} MUX, MIX, PGA, Aux ADC and up to 4 independent mono ADCs; No need for external programmable-gain amplifier



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