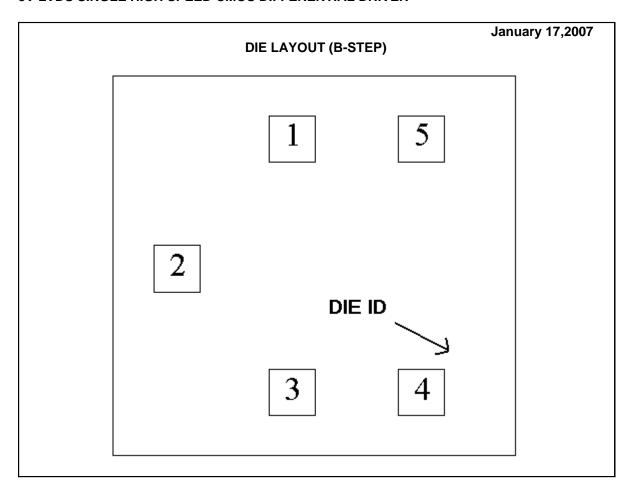


DS90LV011A MDC MWC 3V LVDS SINGLE HIGH SPEED CMOS DIFFERENTIAL DRIVER



DIE/WAFER CHARACTERISTICS

DIL/WAI EN CHARACTERISTICS								
Fabrication Attributes		General Die Information						
Physical Die Identification	DS90LV011AB	Bond Pad Opening Size (min)	102μm x 102μm					
Die Step	В	Bond Pad Metalization	Al_ 0.5%Cu					
Phys	Physical Attributes		PECVDOX+NITRIDE					
Wafer Diameter	200mm	Back Side Metal	BARE BACK					
Die Size (Drawn)	889μm x 838μm 35.0mils x 33.0mils	Back Side Connection	GND					
Thickness	254µm Nominal							
Min Pitch	285μm Nominal							

Special Assembly Requirements:	
Note: Actual die size is rounded to the nearest micron.	



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3V LVDS SINGLE HIGH SPEED CMOS DIFFERENTIAL DRIVER

		<u> </u>				
	Die Bond Pad	Coordinate I	Locations (B -	Step)		
(Reference	d to die center, coordin	nates in µm) <mark>NC</mark>	C = No Connect	ion, <mark>N.U</mark> . =	Not U	sed
SIGNAL	PAD#	X/Y COORDINATES		PAD SIZE		
NAME	NUMBER	X	Υ	Χ		Υ
VDD	1	-49	280	102	Х	102
GND	2	-304	-7	102	Х	102
TTL IN	3	-49	-280	102	Х	102
OUT +	4	236	-280	102	Х	102
OUT -	5	236	280	102	X	102



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