# **Application Note 2212 LM3017 Evaluation Board**

## **User's Guide**



Literature Number: SNOU094 September 2012



### Application Note 2212 LM3017 Evaluation Board

#### 1 Introduction

The LM3017 is a versatile low-side NFET controller incorporating true shutdown and input side current limiting. The ability to drive an external high-side NMOS provides for true isolation of the load from the input. Linear current limiting on the input ensures that inrush and short-circuit currents are always under control. The high switching frequency allows for the smallest components and overall solution size. In shutdown mode, the LM3017 draws 1µA from the input supply. The LM3017 incorporates built in thermal shutdown, cycle-by-cycle current limit, short circuit protection, output over-voltage protection, and soft-start. This evaluation board demonstrates the flexibility of the LM3478 in a boost topology.

#### 2 Features

- 8V to 12V Input Voltage Range
- 15V Output Voltage (Default Setting)
- Up to 1000 mA Output Current
- Low Shutdown Current (< 1μA)</li>
- 600 KHz Fixed Switching Frequency
- True Shutdown
- PCB Size: 27.5 mm x 46.5 mm

#### 3 Operation Modes

The part can operate in three different modes by varying the voltage on the EN/MODE pin:

- V<sub>EN</sub> < 0.4V Shutdown Mode
- 1.6V < V<sub>EN</sub> < 2.2V Run Mode
- V<sub>EN</sub> > 2.6V Standby Mode

Note that the EN pin should not be left floating.

#### 4 Adjusting the Output Voltage

The output voltage can be changed from 15V to another voltage by adjusting the feedback resistors using the following equation:

$$V_{OUT} = V_{FB}(1 + (R_{FBT}/R_{FBB})) \tag{1}$$

Where  $V_{FB}$  is 1.275V. For more information on components selection and features see literature number SNOSC66.



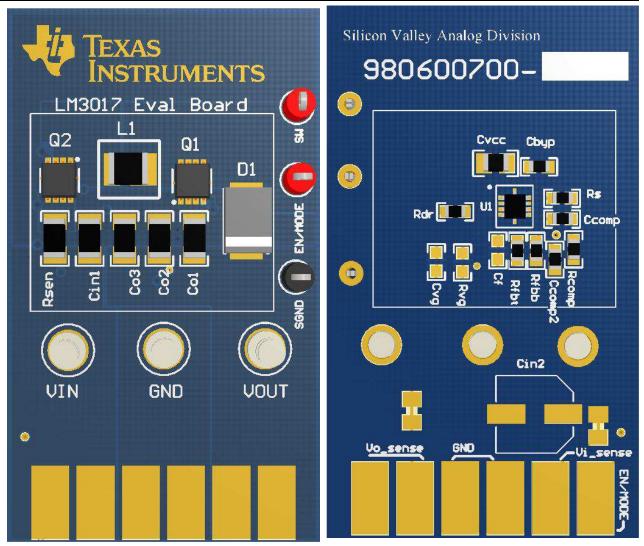


Figure 1. LM3017 Eval Board - Top View

Figure 2. LM3017 Eval Board - Bottom View

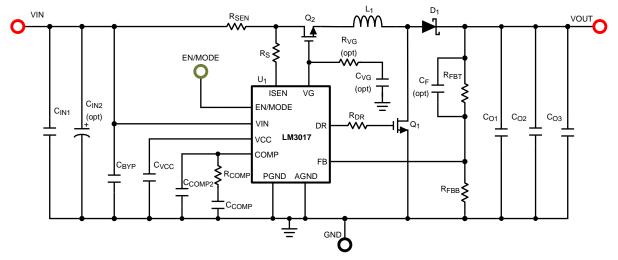


Figure 3. LM3017 Evaluation Board Schematic



Optional components www.ti.com

Table 1. Bill of Materials (BOM) LM3017

Designation	Description	Size	Manufacturer Part #	Vendor
C <sub>IN1</sub>	Cap 22µF 25V X5R	1206	GRM31CR61E226KE15L	Murata
C <sub>IN2</sub>	optional			
C <sub>O1</sub> ,C <sub>O2</sub> , C <sub>O3</sub>	Cap 22µF 25V X5R	1206	GRM31CR61E226KE15L	Murata
C <sub>COMP</sub>	Cap 0.01µF	0603	C0603C103J1RACTU	Kemet
C <sub>COMP2</sub>	Cap 100pF	0603	C1608C0G1H101J	TDK
C <sub>BYP</sub>	Cap 0.1µF 25V X7R	0603	06033C104KAT2A	AVX
C <sub>vcc</sub>	Cap 0.47µF 16V X7R	0805	C2012X7R1C474K	TDK
C <sub>VG</sub>	optional			
C <sub>F</sub>	optional			
R <sub>VG</sub>	optional			
R <sub>COMP</sub>	RES, 3.4kΩ, 1%, 0.1W	0603	CRCW06033K40FKEA	Vishay
R <sub>FBT</sub>	RES, 21.5kΩ, 1%, 0.1W	0603	CRCW060321K5FKEA	Vishay
R <sub>FBB</sub>	RES, 2kΩ, 1%, 0.1W	0603	CRCW06032K00FKEA	Vishay
R <sub>DR</sub>	RES, 0Ω, 1%, 0.1W	0603	CRCW06030000Z0EA	Vishay
Rs	RES, 100Ω, 1%, 0.1W	0603	CRCW0603100RFKEA	Vishay
R <sub>SEN</sub>	RES, 0.03Ω, 1%, 1W	1206	WSLP1206R0300FEA	Vishay
Q <sub>1</sub>	NexFET <sup>TM</sup> N-CH, 25V, 60A, $R_{DS(on)}$ = 4.4m $\Omega$	8-SON	CSD16323Q3	TI
$Q_2$	NexFET <sup>TM</sup> N-CH, 25V, 60A, $R_{DS(on)}$ = 4.3m $\Omega$	8-SON	CSD16340Q3	TI
D <sub>1</sub>	Diode Schottky, 30V, 2A	SMB	20BQ030TRPBF	Vishay
L <sub>1</sub>	Shielded Inductor, 4.7µH, 2.3A	4mm L x 4.5mm W x 1.85mm H	MPI4040R3-4R7-R	Coiltronics
U <sub>1</sub>	LM3017		LM3017LE/NOPB	TI

#### 5 Optional components

 $R_{v_G}$ ,  $C_{v_G}$  sustain the voltage generated by the internal charge pump.

 $\mathbf{C}_{\text{IN2}}$  is an input bulk capacitor. The bulk capacitor should belocated near the Power-supply connection point. The purpose of the bulk capacitor is to overcome the inductive effects of bench wiring.

**C**<sub>F</sub> increases the gain of the dynamic loop during load transients

#### 6 Test Setup

**Table 2. Demonstration Board Quick Setup Procedures** 

Step	Description	Notes	
1	Connect a power supply to V <sub>IN</sub> and GND terminals	V <sub>IN</sub> range: 8V to 12V	
2	Connect a power supply to EN/MODE and GND terminals	EN/MODE range : 0V to 5V	
3	Turn on $V_{IN}$ with 0A load applied and $V_{EN} = 0V$ , check $V_{OUT}$	V <sub>OUT</sub> = 0V	
4	Turn on $V_{IN}$ with 0A load applied and $V_{EN}$ = 2.7V, check $V_{OUT}$	$V_{OUT} = V_{IN} - V_{diode}$	
5	Apply a 0.5A load, check V <sub>OUT</sub>	$V_{OUT} = V_{IN} - V_{diode}$	
6	with 0A load applied reduce $V_{\text{EN}}$ to 1.8V, check $V_{\text{OUT}}$	V <sub>OUT</sub> = 15V	
7	Apply a 1.0A load, check V <sub>OUT</sub>	V <sub>OUT</sub> = 15V	



www.ti.com Test Setup

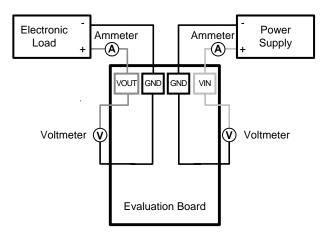


Figure 4. Efficiency Measurements

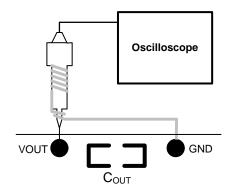


Figure 5. Voltage Ripple Measurements

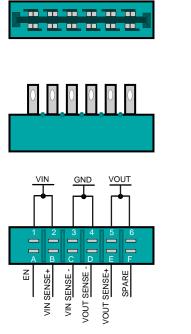
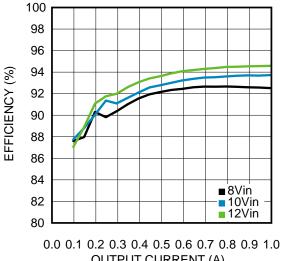


Figure 6. Edge Connector Schematic

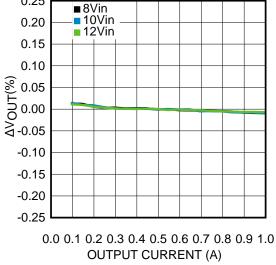


#### 7 Typical Performance Characteristics for LM3017 Evaluation Board



**OUTPUT CURRENT (A)** 

Figure 7. Efficiency VS. **Load Current** 



0.25

Figure 8. Load Regulation

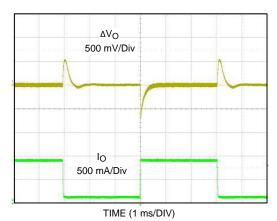


Figure 9. Load Transient Waveforms 100mA to 900mA, **8V**<sub>IN</sub>

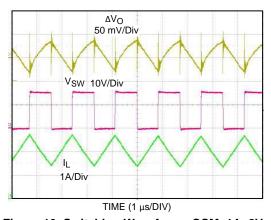


Figure 10. Switching Waveforms CCM, 1A, 8V<sub>IN</sub>

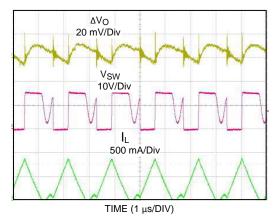


Figure 11. Switching Waveforms DCM, 150mA, 8V<sub>IN</sub>

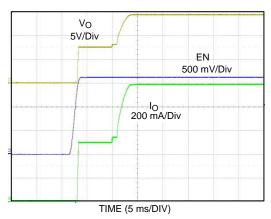


Figure 12. Startup Waveforms V<sub>IN</sub> = 8V



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#### 8 Layout

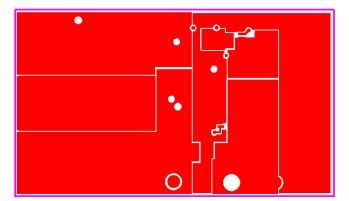


Figure 13. Top Layer

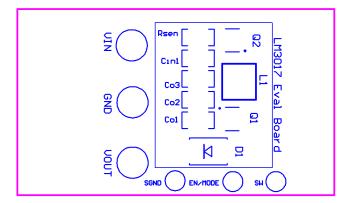


Figure 14. Top Silkscreen

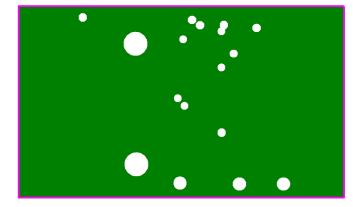


Figure 15. Mid Layer 1



Layout www.ti.com

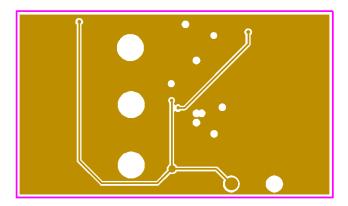


Figure 16. Mid Layer 2

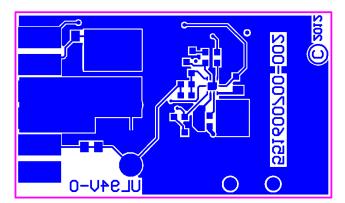


Figure 17. Bottom Layer

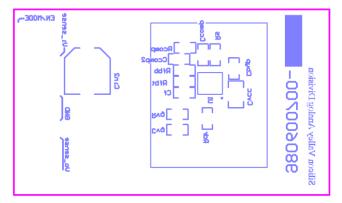


Figure 18. Bottom Silkscreen

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- · Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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- 2. Use EVMs only after User obtains the license of Test Radio Station as provided in Radio Law of Japan with respect to EVMs, or
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