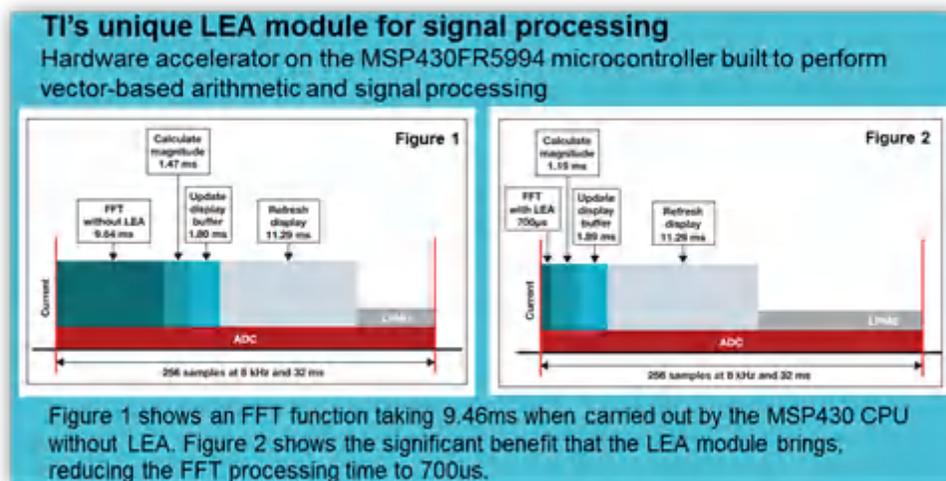




Operating without any central processing unit (CPU) interventions, the LEA module is essentially a low-power co-processor that performs operations and triggers an interrupt when the requested function has been completed. It can operate either concurrently to the main CPU, allowing multiple tasks to be completed in parallel, or it can operate while the CPU is in a low-power sleep mode, triggering an interrupt once the specified task has been completed.

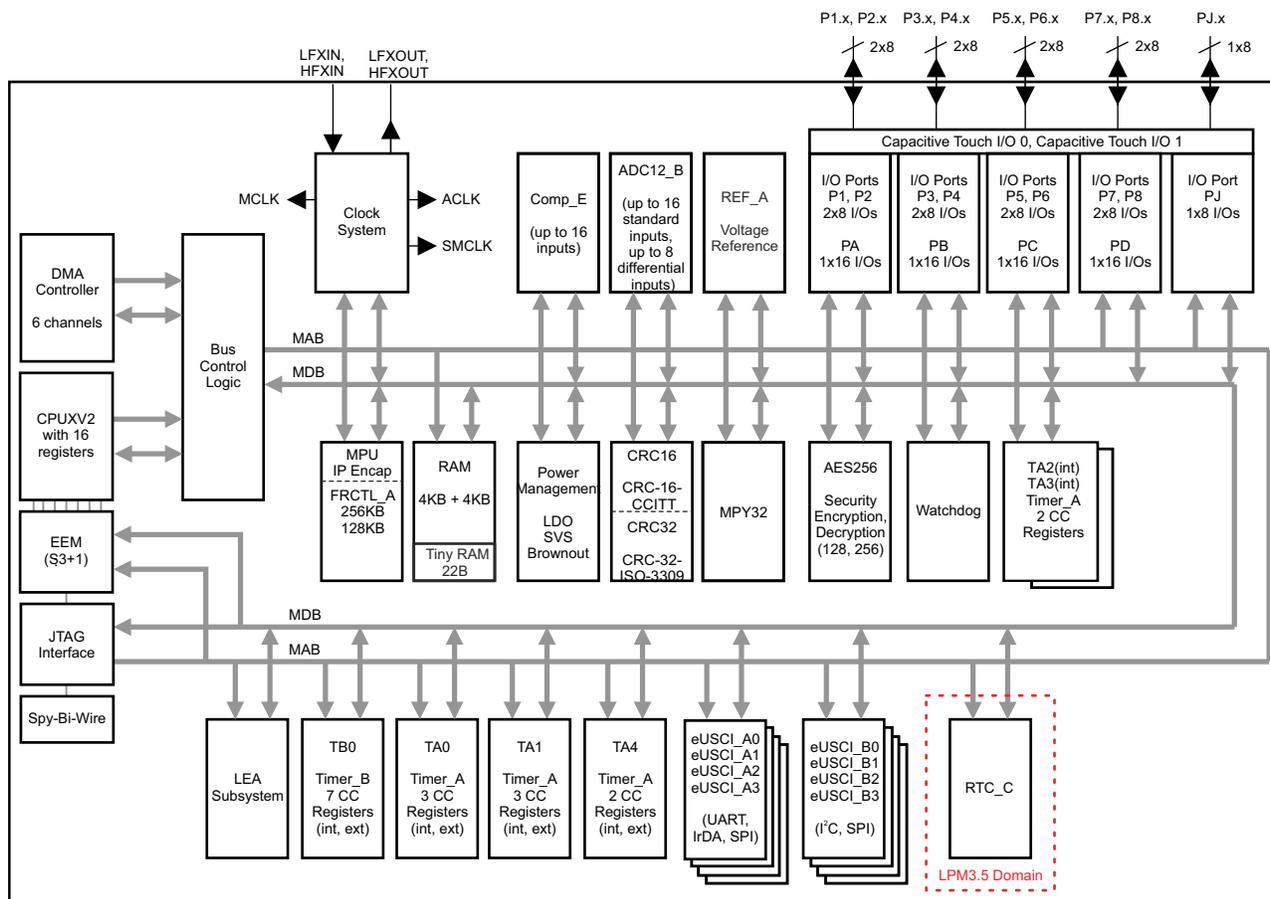
The LEA module operates with a pre-defined set of commands, each of which is highly optimized for maximum performance with minimum energy usage. These commands are made easy to use through the use of the [MSP DSP Library](#), a library of over 50 dedicated functions that are built and specifically optimized for MSP430 MCUs to enable signal processing functions of the programmers choosing. When the LEA module is available on a device, the compiler will automatically use it to optimize performance beyond the MSP430 MCU C-code optimization techniques that have already been implemented. We have created a new [Filtering and Signal Processing TI Design](#) reference design that enables the user to see just how significant the improvement in processing speed is over non-LEA module-enabled MCU's. This TI Design reference design is based on the new [MSP-EXP430FR5994](#) MCU LaunchPad™ development kit. This kit includes a built-in programming and debugging interface, [EnergyTrace++](#) technology to help developers optimize their code for the greatest energy efficiency, user LED's & push buttons and 40 pin headers to allow the LaunchPad kit to be used with a wide variety of compatible [BoosterPack™](#) plug-in modules. You can find out more about the LaunchPad kit and [BoosterPack ecosystem here](#).



**Figure 2. Comparison of MSP430 MCU with and without LEA**

To further help developers to understand the added performance that the LEA module can bring, we have released a new [Benchmarking the Signal Processing Capabilities of the Low-Energy Accelerator](#) application note. This application note covers the performance of the advanced signal processing while maintaining the ultra-low-power consumption on a 16-bit MSP430™ FRAM MCU in comparison to an ARM® Cortex®-M0+ based MCU.

The first devices to include the new LEA module are the [MSP430FR5994 MCUs](#). This is the latest in a long line of 16-bit ultra-low-power microcontrollers, featuring 256 KB of ferroelectric random access memory ([FRAM](#)), 8-KB of shared SRAM, a breadth of analog peripherals, and the LEA vector math accelerator module. Offering very-low active power consumption of around 120uA / MHz and RTC modes below 500nA's, the LEA module is a great peripheral for bringing an increase in processing performance, without the usual corresponding increase in energy consumption.



- A. The device has 8KB of RAM, and 4KB of the RAM is shared with the LEA subsystem. The CPU has priority over the LEA subsystem.
- B. The LEA subsystem is available on the MSP430FR599x MCUs only.

**Figure 3. Functional Block Diagram**

The MSP430FR5994 device also includes a 12-bit 200K sample ADC, a 16-channel analog comparator, AES256 hardware accelerator, up to eight serial ports in a combination of UART, SPI and I2C, a real-time clock with alarms and calendar.

In conclusion, if your current or future embedded design has a need to incorporate FFT's, FIR's, IIR's or other vector processing tasks whilst maintaining low current consumption then the LEA module might be the peripheral you have been looking for. Operating at ~67uA / MHz, it is more energy efficient than simply clocking an MCU at 10's of MHz's to achieve the same throughput.

Additional resources:

- Download our [white paper](#) about setting a new standard for MCU performance while minimizing energy consumption.
- Learn how the LEA module can be used in smart fault indicators in this [white paper](#).
- Read our [blog post](#) on updating those hard-to-reach industrial machines.
- The [MSP430FR5994](#) MCU is now shipping in production quantities for the BGA and QFN packages and will be in volume production with the LQFP package in March.

## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to [TI's Terms of Sale](#) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2023, Texas Instruments Incorporated