# Technical Article **Opto-emulators explained: Why you should upgrade your optocoupler technology**



Luke Trowbridge



Optocouplers, also known as photocouplers, opto-isolators and optical isolators, have long been an option for designers seeking galvanic isolation for their system signals. Around since the 1970s, these semiconductor devices have played an important role in providing safety isolation for industrial and automotive end equipment. However, despite significant advancements, there appears to be a limitation to their progress in electrical characteristics, high-voltage reliability and integration capabilities, prompting designers to explore alternatives.

Technologies such as capacitive and magnetic isolation have emerged as alternatives, offering better overall performance compared to optocouplers. Texas Instruments (TI) has invested in silicon dioxide (SiO<sub>2</sub>)-based digital isolation technology since the early 2000s, providing digital isolator products that offer the same functionality as optocouplers with some distinct advantages.

# Bridging the gap: Introducing opto-emulators

Texas Instruments opto-emulators combine the benefits of traditional optocouplers and TI's  $SiO_2$ -based isolation technology. Opto-emulators are pin-to-pin compatible with the industry's most popular optocouplers, facilitating seamless integration into existing designs while providing equivalent signal behavior. These products appear and behave just like optocouplers from a design engineer's perspective but leverage TI's  $SiO_2$  technology for the isolation barrier. With the barrier effectively blocking high-voltage signals and preventing ground loops – ensuring system safety and stability – you can harness the advantages of  $SiO_2$  isolation, which include enhanced electrical characteristics, improved high-voltage reliability, and the potential to integrate additional system functionality. By creating this type of semiconductor product, our goal is to give you the best of both worlds.

1



Traditional optocouplers use an LED to transmit digital or analog information across an isolation barrier where a phototransistor detects the signal on the other side; see Figure 1. It is a well-known fact that the LEDs used in optocouplers have an aging or degradation effect over their lifetime. This property of LEDs is a significant headache to system designers and the No. 1 complaint we hear at TI. Additionally, the insulating material used in an optocoupler ranges from just air to epoxies or mold compounds. Table 1 clearly shows the difference in the isolation strength of an optocoupler vs. an opto-emulator using a an SiO<sub>2</sub> dielectric.



# Figure 1. Typical optocoupler construction

Insulator Materials	Technology	Dielectric Strength
Air	Optocouplers	~1 V <sub>RMS</sub> /µm
Epoxies	Optocouplers	~20 V <sub>RMS</sub> /µm
Silica Filled Mold Compounds	Optocouplers	~100 V <sub>RMS</sub> /µm
SiO <sub>2</sub>	Opto-emulators	~500 V <sub>RMS</sub> /µm

Fable 1. The dielectric strength of various insulating mate	rials
---	-------

With opto-emulators using TI's SiO<sub>2</sub>-based isolation barrier to achieve signal isolation, it's possible to avoid both of these common optocoupler pitfalls. Figure 2 shows the internal construction of a TI opto-emulator, where the functional behavior of a traditional optocoupler is emulated on the transmit and receive circuits and SiO<sub>2</sub> provides the high-voltage isolation.





2



### The advantages of opto-emulators

By integrating advanced isolation technology, opto-emulators can overcome the limitations associated with traditional optocouplers, enabling superior performance and reliability. Let's discuss a few of the advantages of opto-emulators:

### Lower power consumption

Traditional optocouplers require upfront overdesigning to help compensate for the inevitable aging effects of the LED, requiring additional forward current ( $I_F$ ) throughout the design's lifetime. TI opto-emulators can save you as much as 80% on your power budget by offering a much lower  $I_F$  and supply current.

### Improved common-mode transient immunity (CMTI)

While a common digital optocoupler specifies a CMTI of approximately 15 kV/ $\mu$ s, the ISOM8710 has a minimum CMTI of 125 kV/ $\mu$ s, enabling its use in applications with very high common-mode switching noise or high ringing noise.

### A stable and tight current transfer ratio (CTR)

Forget the days of paying extra for a tighter CTR range. TI opto-emulators such as the ISOM8110 come standard with a variety of tight CTR ranges that are stable over temperature.

### Fast data rates

Typical high-speed optocouplers support data rates from 1 Mbps up to 10 Mbps, while the ISOM8710 supports 25 Mbps. This support allows for higher throughput and enables the use of opto-emulators in a variety of high-speed applications.

### Bandwidth

The ISOM8110 supports a high bandwidth of 680 kHz, enabling a reduction in the size of mandatory magnetics (inductor and transformer). The wide bandwidth enables improved transient responses for secondary-side regulated flyback converters. The improved transient response allows for a reduction in size of output capacitors, freeing up board space and reducing overall system cost, especially in high-switching-frequency gallium nitride designs.

#### Wide temperature range

The average optocoupler supports a temperature range from 0°C to +85°C. While some optocouplers support a wider temperature range, that feature comes at an additional cost. TI opto-emulators support a temperature range as wide as -55°C to +125°C as the standard offering, and more automotive-qualified devices will be available in 2024.

#### **Reliable isolation**

Opto-emulators have improved high-voltage capabilities, making them suitable for applications demanding reliable isolation. TI opto-emulators leverage SiO<sub>2</sub> for the insulating barrier, providing 500 V/ $\mu$ m of isolation. This is significantly stronger than the air (1 V/ $\mu$ m) used in many optocouplers on the market.

#### Conclusion

Opto-emulators represent a significant advancement in signal isolation technology, combining the familiarity of optocouplers with the advantages of SiO<sub>2</sub>-based isolation. These devices empower you to meet the demands of modern systems, ensuring enhanced performance, reliability and safety. By leveraging opto-emulators, you can optimize your designs and embrace the new era of isolation technology.



#### **Additional resources**

If you're ready to upgrade your designs to opto-emulators, try TI's cross-reference search, where you can upload the optocouplers you use in your designs today and find the right opto-emulator to match.

Read the white paper, "Addressing High-Voltage Design Challenges with Reliable and Affordable Isolation Technologies," the application note, "Introduction to Opto-Emulators," and Opto-emulators product portfolio page.

4

# IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2024, Texas Instruments Incorporated