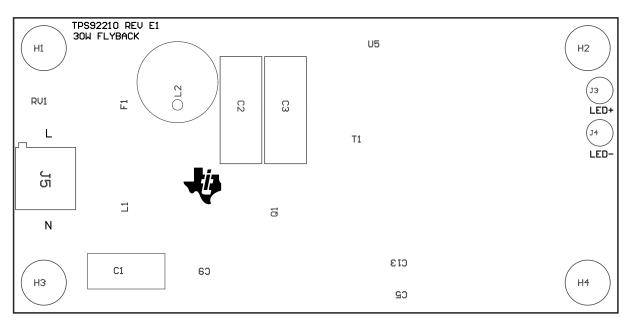
ZZ1 These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3 ■These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

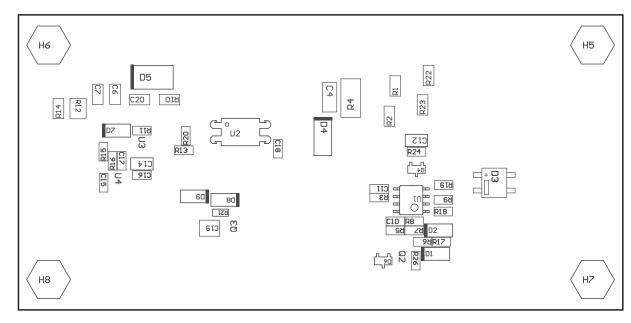
ZZ4 Install 'C8' bent over horizontally & apply RTV167 to secure to the PCB surface, bend radius should follow best pratice guidelines.

ZZ5 ■'C8' is a non-IPC compliant, custom footprint



## COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED. ASSEMBLY VARIANT: [No Variations]

PCB VIEWED FROM TOP SIDE	BOARD #: PMP6023-1	REV: E1	SUN REU:	: Not In VersionControl
IGNORE -> Top Overlay				
PLOT NAME = Top Assembly Drawing	GENERATED : 8/14/2	2014 1:03:03	PM	TEXAS INSTRUMENTS



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED. ASSEMBLY VARIANT: [No Variations]

PCB VIEWED FROM BOTTOM SIDE	BOARD #: PMP6023-1	REV: E1	SUN REU:	: Not In VersionControl
	Bottom Overlay <- JACKDI			
PLOT NAME = Bottom Assembly Drawing	GENERATED : 8/14/2	2014 1:03:04	PM	TEXAS INSTRUMENTS

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