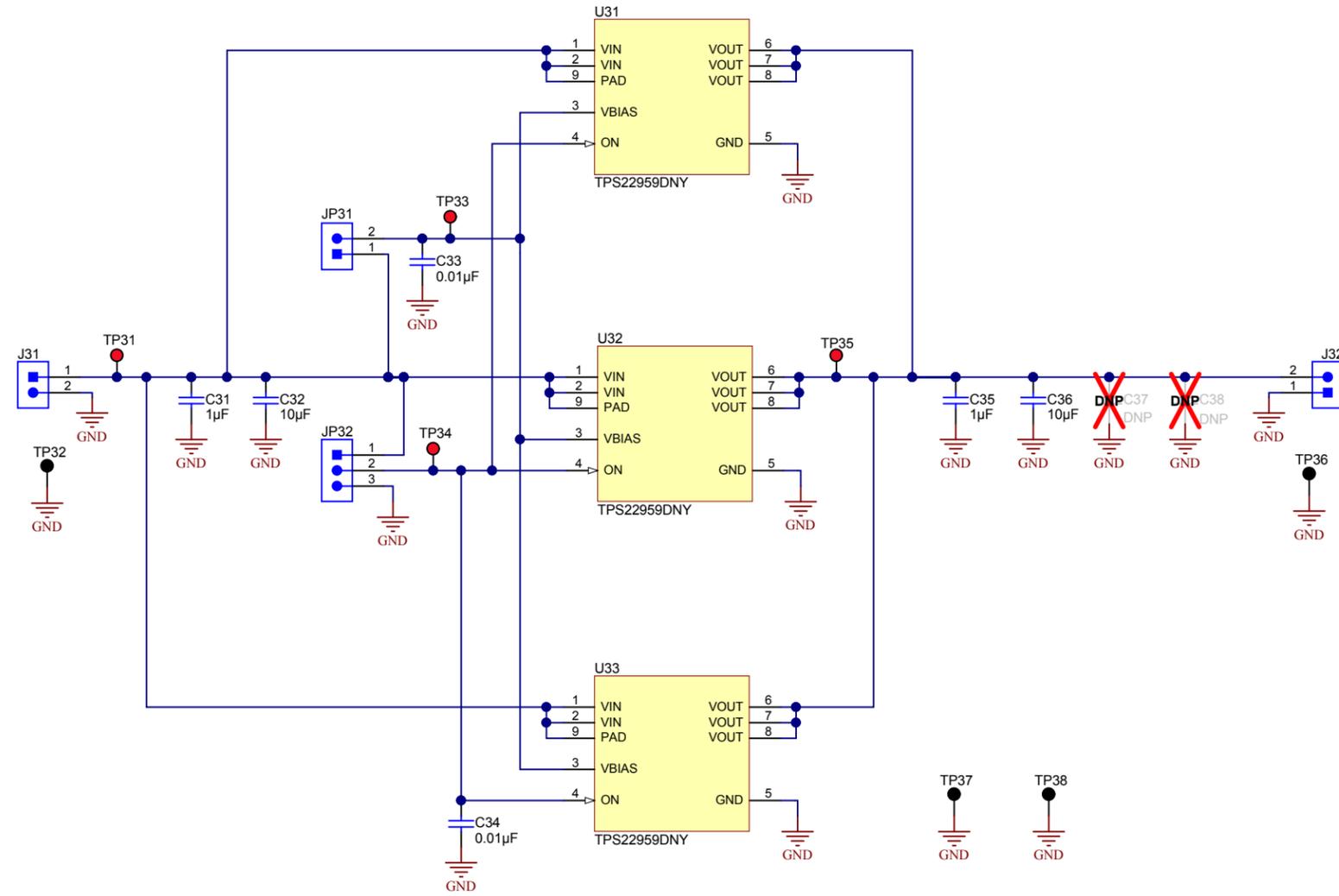


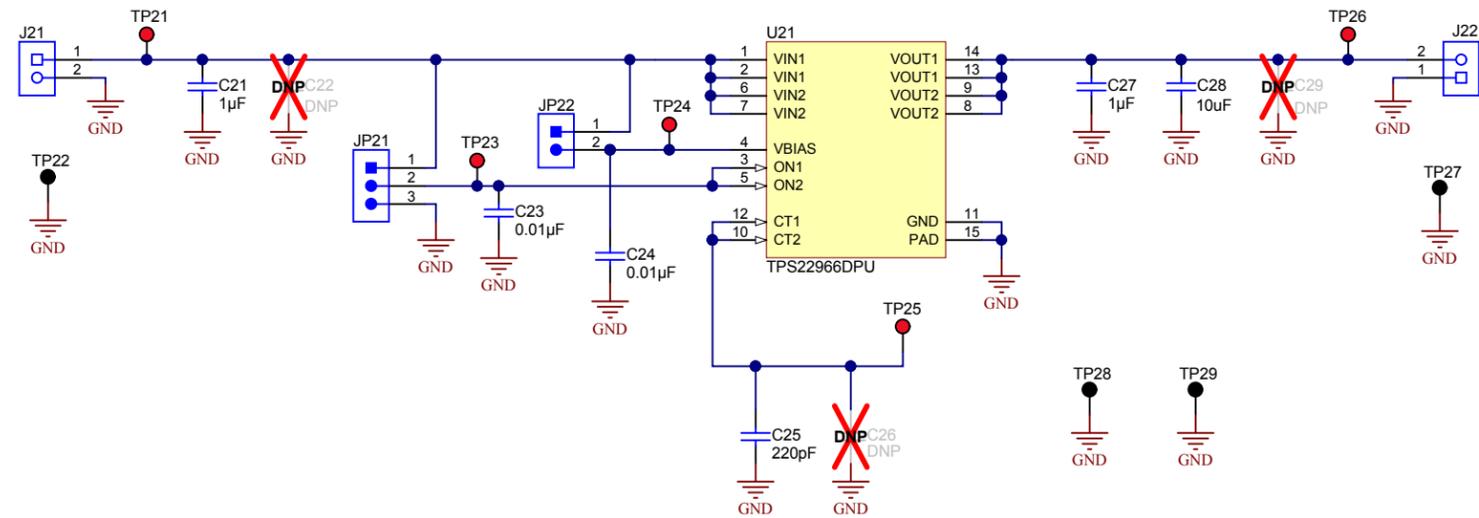
TPS22959 in Parallel for 30A



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Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 10/20/2014	 TEXAS INSTRUMENTS http://www.ti.com
TID #: N/A	Project Title: Parellel Load Switch Reference Design	Sheet: 2 of 3	
Number: TIDA-00513	Rev: A	File: 30A Parallel.SchDoc	Size: B
SVN Rev: Version control disabled	Engineer: Adam Hoover	Contact: http://www.ti.com/support	

TPS22966 in Parallel for 10A



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Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 11/14/2014
TID #: N/A	Project Title: Parellel Load Switch Reference Design	
Number: TIDA-00513	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: Rev. A	Sheet: 2 of 3
Drawn By:	File: 10A Parallel.SchDoc	Size: B
Engineer: Adam Hoover	Contact: http://www.ti.com/support	



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H9 SJ-5303 (CLEAR) H10 SJ-5303 (CLEAR) H11 SJ-5303 (CLEAR) H12 SJ-5303 (CLEAR)

~~DNP~~ FID1 ~~DNP~~ FID2 ~~DNP~~ FID3

PCB Number: TIDA-00513
PCB Rev: A

PCB
LOGO
Texas Instruments

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

SH-JP21 SH-JP22 SH-JP31 SH-JP32

Orderable: EVM_orderable	Designed for: Public Release	Mod. Date: 3/15/2015	 http://www.ti.com <small>© Texas Instruments 2014</small>
TID #: N/A	Project Title: Parallel Load Switch Reference Design		
Number: TIDA-00513	Rev: A	Sheet: 3 of 3	
SVN Rev: Version control disabled	Assembly Variant: Rev. A		
Drawn By: Adam Hoover	File: Hardware_SchDoc	Size: B	
Engineer: Adam Hoover	Contact: http://www.ti.com/support		

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