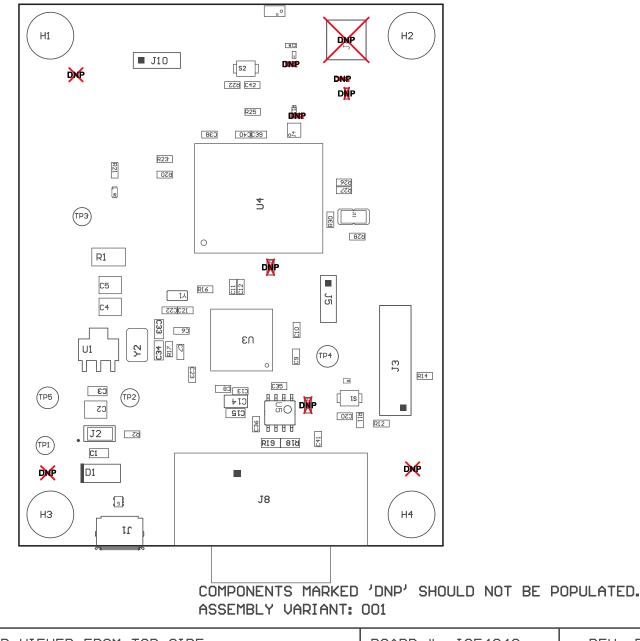
ZZ1 ■ These assemblies are ESD sensitive, ESD precautions shall be observed.

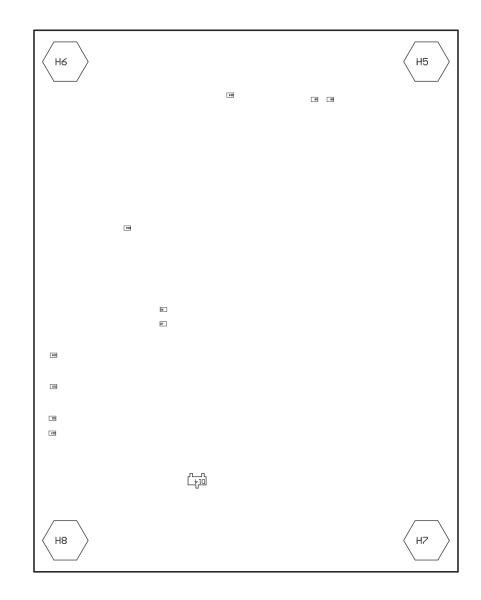
ZZ2 These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3 ■These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ4 ■ Place SH-J2 on J2, SH-J11 on J11



PCB VIEWED FROM TOP SIDE	BOARD #: ISE4040	REV: E2	SVN REV	Not In VersionControl		
	TID #: TIDA-00380					
PLOT NAME = Top Assembly Drawing	GENERATED : 9/22/2	2015 10:23:15	AM	TEXAS INSTRUMENTS		



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED. ASSEMBLY VARIANT: 001

PCB VIEWED FROM BOTTOM SIDE	BOARD #: ISE4040	REV: E2	SVN REV:	Not In VersionControl		
	TID #: TIDA-00380					
PLOT NAME = Bottom Assembly Drawing	GENERATED : 9/22/2	2015 10:23:17	AM	TEXAS INSTRUMENTS		

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