## Preface

1 About SYS/BIOS
   1.1 What is SYS/BIOS? ......................................... 11
   1.2 How are SYS/BIOS and TI-RTOS Related? ............... 12
   1.3 How are SYS/BIOS and XDCtools Related? ............... 13
       1.3.1 SYS/BIOS as a Set of Packages .................. 14
       1.3.2 Configuring SYS/BIOS Using XDCtools .......... 16
       1.3.3 XDCtools Modules and Runtime APIs .......... 18
   1.4 SYS/BIOS Packages and APIs ........................................ 19
       1.4.1 SYS/BIOS Object Creation .......................... 19
       1.4.2 POSIX Thread Support ................................ 22
   1.5 Using C++ with SYS/BIOS .................................... 22
       1.5.1 Memory Management .................................. 22
       1.5.2 Name Mangling ...................................... 22
       1.5.3 Calling Class Methods from the Configuration .... 23
       1.5.4 Class Constructors and Destorctors ............. 24
   1.6 For More Information ...................................... 24
       1.6.1 Using the API Reference Help System ............ 25

2 SYS/BIOS Configuration and Building .......................... 26
   2.1 Creating a SYS/BIOS Project with the TI Resource Explorer ... 27
   2.2 Adding SYS/BIOS Support to an Existing Project ........ 28
   2.3 Configuring SYS/BIOS Applications .......................... 29
       2.3.1 Opening a Configuration File with XGCONF ....... 30
       2.3.2 Performing Tasks with XGCONF .................... 31
       2.3.3 Saving the Configuration ......................... 31
       2.3.4 About the XGCONF views .......................... 32
       2.3.5 Using the Available Products View ............... 33
       2.3.6 Using the Outline View ............................ 34
       2.3.7 Using the Property View ........................... 35
       2.3.8 Using the Problems View ........................... 39
       2.3.9 Finding and Fixing Errors ......................... 39
       2.3.10 Accessing the Global Namespace ................. 40
   2.4 Building SYS/BIOS Applications ............................ 41
       2.4.1 Understanding the Build Flow ...................... 41
       2.4.2 Rules for Working with CCS Project Properties ... 42
       2.4.3 Building an Application with GCC ............... 42
       2.4.4 Running and Debugging an Application in CCS .... 44
       2.4.5 Compiler and Linker Optimization ............... 45

3 Threading Modules .................................................. 47
   3.1 SYS/BIOS Startup Sequence ..................... 48
   3.2 Overview of Threading Modules .................. 49
3.2.1 Types of Threads ................................................................. 50
3.2.2 Choosing Which Types of Threads to Use ................................. 51
3.2.3 A Comparison of Thread Characteristics ................................. 52
3.2.4 Thread Priorities ................................................................. 53
3.2.5 Yielding and Preemption ...................................................... 54
3.2.6 Hooks ................................................................................. 56
3.3 Using SYS/BIOS on SMP Systems ............................................. 58
3.4 Hardware Interrupts ............................................................... 58
3.4.1 Creating Hwi Objects .......................................................... 59
3.4.2 Hardware Interrupt Nesting and System Stack Size ...................... 59
3.4.3 Hwi Hooks ........................................................................... 60
3.5 Software Interrupts ................................................................. 66
3.5.1 Creating Swi Objects ............................................................ 67
3.5.2 Setting Software Interrupt Priorities ....................................... 68
3.5.3 Software Interrupt Priorities and System Stack Size .................... 69
3.5.4 Execution of Software Interrupts ............................................ 70
3.5.5 Using a Swi Object’s Trigger Variable ..................................... 71
3.5.6 Benefits and Tradeoffs ......................................................... 74
3.5.7 Synchronizing Swi Functions ............................................... 75
3.5.8 Swi Hooks ........................................................................... 75
3.6 Tasks .............................................................................. 82
3.6.1 Creating Tasks ................................................................. 83
3.6.2 Task Execution States and Scheduling ................................. 84
3.6.3 Task Stacks ....................................................................... 86
3.6.4 Testing for Stack Overflow .................................................. 87
3.6.5 Task Hooks ....................................................................... 88
3.6.6 Task Yielding for Time-Slice Scheduling ............................... 95
3.7 The Idle Loop .................................................................... 100
3.8 Example Using Hwi, Swi, and Task Threads ................................. 101
4 Synchronization Modules .......................................................... 106
4.1 Semaphores .................................................................. 107
4.1.1 Semaphore Example ........................................................ 109
4.2 Event Module .................................................................... 113
4.2.1 Implicitly Posted Events ..................................................... 116
4.3 Gates ........................................................................... 119
4.3.1 Preemption-Based Gate Implementations ............................. 120
4.3.2 Semaphore-Based Gate Implementations .............................. 120
4.3.3 Priority Inversion .............................................................. 121
4.3.4 Configuring the SYS/BIOS Gate Type ................................. 121
4.4 Mailboxes ........................................................................ 122
4.5 Queues ......................................................................... 124
4.5.1 Basic FIFO Operation of a Queue ....................................... 124
4.5.2 Iterating Over a Queue ...................................................... 125
4.5.3 Inserting and Removing Queue Elements ............................. 125
4.5.4 Atomic Queue Operations ................................................ 125
5 Timing Services .................................................................... 126
5.1 Overview of Timing Services ............................................... 127
5.2 Clock .......................................................................... 127
6 Support Modules ......................................................... 133
   6.1 Modules for Application Support and Management .................. 134
   6.2 BIOS Module ......................................................... 134
   6.3 System Module ...................................................... 135
       6.3.1 SysMin Module ..................................................... 137
       6.3.2 SysCallback Module .............................................. 137
   6.4 Program Module .................................................... 137
   6.5 Startup Module ..................................................... 138
   6.6 Reset Module ....................................................... 138
   6.7 Error Module ........................................................ 139
   6.8 Text Module ........................................................ 140

7 Memory ............................................................... 141
   7.1 Background ........................................................ 142
   7.2 Memory Map ........................................................ 143
       7.2.1 Choosing an Available Platform .................................. 143
       7.2.2 Creating a Custom Platform ..................................... 144
   7.3 Placing Sections into Memory Segments .............................. 148
       7.3.1 Configuring Simple Section Placement ............................ 149
       7.3.2 Configuring Section Placement Using a SectionSpec .......... 149
       7.3.3 Providing a Supplemental Linker Command File .............. 150
       7.3.4 Default Linker Command File and Customization Options .... 151
   7.4 Sections and Memory Mapping for MSP430, Stellaris M3, and C28x . 152
   7.5 Stacks ............................................................... 152
       7.5.1 System Stack ...................................................... 152
       7.5.2 Task Stacks ....................................................... 153
   7.6 Cache Configuration ................................................. 154
       7.6.1 Configure Cache Size Registers at Startup .................... 154
       7.6.2 Configure Parameters to Set MAR Registers .................. 154
       7.6.3 Cache Runtime APIs ............................................. 154
   7.7 Dynamic Memory Allocation ........................................ 155
       7.7.1 Memory Policy .................................................... 155
       7.7.2 Specifying the Default System Heap ............................ 155
       7.7.3 Using the xdc.runtime.Memory Module .......................... 156
       7.7.4 Specifying a Heap for Module Dynamic Instances ............ 157
       7.7.5 Using malloc() and free() ....................................... 158
   7.8 Heap Implementations ............................................... 158
       7.8.1 HeapMin .......................................................... 159
       7.8.2 HeapMem ........................................................ 159
       7.8.3 HeapBuf ........................................................ 160
       7.8.4 HeapMultiBuf .................................................... 161
       7.8.5 HeapTrack ....................................................... 164

8 Hardware Abstraction Layer ........................................... 165
   8.1 Hardware Abstraction Layer APIs .................................. 166
   8.2 HWI Module ......................................................... 167
       8.2.1 Associating a C Function with a System Interrupt Source .... 167
<table>
<thead>
<tr>
<th>Section</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>8.2.2 Hwi Instance Configuration Parameters</td>
<td>167</td>
</tr>
<tr>
<td>8.2.3 Creating a Hwi Object Using Non-Default Instance Configuration Parameters</td>
<td>168</td>
</tr>
<tr>
<td>8.2.4 Enabling and Disabling Interrupts</td>
<td>169</td>
</tr>
<tr>
<td>8.2.5 A Simple Example Hwi Application</td>
<td>170</td>
</tr>
<tr>
<td>8.2.6 The Interrupt Dispatcher</td>
<td>171</td>
</tr>
<tr>
<td>8.2.7 Registers Saved and Restored by the Interrupt Dispatcher</td>
<td>172</td>
</tr>
<tr>
<td>8.2.8 Additional Target/Device-Specific Hwi Module Functionality</td>
<td>172</td>
</tr>
<tr>
<td>8.3 Timer Module</td>
<td>174</td>
</tr>
<tr>
<td>8.3.1 Target/Device-Specific Timer Modules</td>
<td>177</td>
</tr>
<tr>
<td>8.4 Cache Module</td>
<td>179</td>
</tr>
<tr>
<td>8.4.1 Cache Interface Functions</td>
<td>179</td>
</tr>
<tr>
<td>8.5 HAL Package Organization</td>
<td>180</td>
</tr>
<tr>
<td>9 Instrumentation</td>
<td>182</td>
</tr>
<tr>
<td>9.1 Overview of Instrumentation</td>
<td>183</td>
</tr>
<tr>
<td>9.2 Load Module</td>
<td>183</td>
</tr>
<tr>
<td>9.2.1 Load Module Configuration</td>
<td>184</td>
</tr>
<tr>
<td>9.2.2 Obtaining Load Statistics</td>
<td>184</td>
</tr>
<tr>
<td>9.3 Error Handling</td>
<td>185</td>
</tr>
<tr>
<td>9.4 Instrumentation Tools in Code Composer Studio</td>
<td>187</td>
</tr>
<tr>
<td>9.5 Performance Optimization</td>
<td>189</td>
</tr>
<tr>
<td>9.5.1 Configuring Logging</td>
<td>189</td>
</tr>
<tr>
<td>9.5.2 Configuring Diagnostics</td>
<td>190</td>
</tr>
<tr>
<td>9.5.3 Choosing a Heap Manager</td>
<td>190</td>
</tr>
<tr>
<td>9.5.4 Hwi Configuration</td>
<td>190</td>
</tr>
<tr>
<td>9.5.5 Stack Checking</td>
<td>190</td>
</tr>
<tr>
<td>A Rebuilding SYS/BIOS</td>
<td>191</td>
</tr>
<tr>
<td>A.1 Overview</td>
<td>192</td>
</tr>
<tr>
<td>A.2 Prerequisites</td>
<td>192</td>
</tr>
<tr>
<td>A.3 Building SYS/BIOS Using the bios.mak Makefile</td>
<td>192</td>
</tr>
<tr>
<td>A.4 Building Your Project Using a Rebuilt SYS/BIOS</td>
<td>195</td>
</tr>
<tr>
<td>B Timing Benchmarks</td>
<td>196</td>
</tr>
<tr>
<td>B.1 Timing Benchmarks</td>
<td>197</td>
</tr>
<tr>
<td>B.2 Interrupt Latency</td>
<td>197</td>
</tr>
<tr>
<td>B.3 Hwi-Hardware Interrupt Benchmarks</td>
<td>197</td>
</tr>
<tr>
<td>B.4 Swi-Software Interrupt Benchmarks</td>
<td>198</td>
</tr>
<tr>
<td>B.5 Task Benchmarks</td>
<td>199</td>
</tr>
<tr>
<td>B.6 Semaphore Benchmarks</td>
<td>201</td>
</tr>
<tr>
<td>C Size Benchmarks</td>
<td>204</td>
</tr>
<tr>
<td>C.1 Overview</td>
<td>205</td>
</tr>
<tr>
<td>C.2 Constructed Application Sizes</td>
<td>206</td>
</tr>
<tr>
<td>C.2.1 Constructed Task Application</td>
<td>206</td>
</tr>
<tr>
<td>C.2.2 Constructed Semaphore Application</td>
<td>207</td>
</tr>
<tr>
<td>C.2.3 Constructed Mutex Application</td>
<td>207</td>
</tr>
<tr>
<td>C.2.4 Constructed Clock Application</td>
<td>208</td>
</tr>
<tr>
<td>C.3 Created Module Application Sizes</td>
<td>209</td>
</tr>
<tr>
<td>C.3.1 Created Task Application</td>
<td>209</td>
</tr>
<tr>
<td>C.3.2 Created Semaphore Application</td>
<td>210</td>
</tr>
</tbody>
</table>
D  Minimizing the Application Footprint  .......................................................... 213
   D.1  Overview  ................................................................. 214
   D.2  Reducing Data Size  ..................................................... 214
      D.2.1  Removing the malloc Heap  ..................................... 214
      D.2.2  Reducing the Size of Stacks  ................................. 214
      D.2.3  Setting the Default Task Stack Size. ...................... 215
      D.2.4  Disabling Named Modules  .................................... 215
      D.2.5  Leaving Text Strings Off the Target. ...................... 215
      D.2.6  Reduce the Number of atexit Handlers .................... 215
   D.3  Reducing Code Size  .................................................. 216
      D.3.1  Use the Custom Build SYS/BIOS Libraries ................ 216
      D.3.2  Disabling Logging  ............................................. 216
      D.3.3  Setting Memory Policies  .................................... 216
      D.3.4  Disabling Core Features  .................................... 216
      D.3.5  Eliminating printf() ........................................... 217
      D.3.6  Disabling RTS Thread Protection  .......................... 217
      D.3.7  Disable Task Stack Overrun Checking ...................... 217
      D.3.8  Cortex-M3/M4 Exception Management  ....................... 217
   D.4  Basic Size Benchmark Configuration Script  ............................. 218

E  Deprecated Input/Output Modules ............................................................. 222
   E.1  GIO Drivers and TI-RTOS. ........................................... 223
   E.2  Overview of the GIO Model .......................................... 223
   E.3  Configuring Drivers in the Device Table  ........................... 224
      E.3.1  Configuring the GIO Module  ................................ 226
   E.4  Using GIO APIs ....................................................... 227
      E.4.1  Constraints When Using GIO APIs  ......................... 227
      E.4.2  Creating and Deleting GIO Channels  ....................... 228
      E.4.3  Using GIO_read() and GIO_write() — The Standard Model  . 230
      E.4.4  Using GIO_issue(), GIO_reclaim(), and GIO_prime() — The Issue/Reclaim Model . 232
      E.4.5  GIO_abort() and Error Handling  ............................ 234
   E.5  Using GIO in Various Thread Contexts. ............................ 235
      E.5.1  Using GIO with Tasks ......................................... 235
      E.5.2  Using GIO with Swis  ......................................... 236
      E.5.3  Using GIO with Events  ....................................... 236
   E.6  GIO and Synchronization Mechanisms. .................................. 237
      E.6.1  Using GIO with Generic Callbacks. .......................... 237

F  IOM Interface ............................................................... 238
   F.1  Mini-Interface Overview ........................................... 239

G  Revision History ............................................................... 248
   Index ................................................................. 252
## List of Figures

<table>
<thead>
<tr>
<th>Figure</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>3-1</td>
<td>Thread Priorities</td>
<td>53</td>
</tr>
<tr>
<td>3-2</td>
<td>Preemption Scenario</td>
<td>55</td>
</tr>
<tr>
<td>3-3</td>
<td>Using Swi_inc() to Post a Swi</td>
<td>72</td>
</tr>
<tr>
<td>3-4</td>
<td>Using Swi_andn() to Post a Swi</td>
<td>73</td>
</tr>
<tr>
<td>3-5</td>
<td>Using Swi_dec() to Post a Swi</td>
<td>73</td>
</tr>
<tr>
<td>3-6</td>
<td>Using Swi_or() to Post a Swi</td>
<td>74</td>
</tr>
<tr>
<td>3-7</td>
<td>Execution Mode Variations</td>
<td>85</td>
</tr>
<tr>
<td>4-1</td>
<td>Trace Window Results from Example 4-4</td>
<td>113</td>
</tr>
<tr>
<td>B–1</td>
<td>Hardware Interrupt to Blocked Task</td>
<td>198</td>
</tr>
<tr>
<td>B–2</td>
<td>Hardware Interrupt to Software Interrupt</td>
<td>198</td>
</tr>
<tr>
<td>B–3</td>
<td>Post of Software Interrupt Again</td>
<td>199</td>
</tr>
<tr>
<td>B–4</td>
<td>Post Software Interrupt without Context Switch</td>
<td>199</td>
</tr>
<tr>
<td>B–5</td>
<td>Post Software Interrupt with Context Switch</td>
<td>200</td>
</tr>
<tr>
<td>B–6</td>
<td>Create a New Task without Context Switch</td>
<td>200</td>
</tr>
<tr>
<td>B–7</td>
<td>Create a New Task with Context Switch</td>
<td>200</td>
</tr>
<tr>
<td>B–8</td>
<td>Set a Task's Priority without a Context Switch</td>
<td>200</td>
</tr>
<tr>
<td>B–9</td>
<td>Lower the Current Task's Priority, Context Switch</td>
<td>201</td>
</tr>
<tr>
<td>B–10</td>
<td>Raise a Ready Task's Priority, Context Switch</td>
<td>201</td>
</tr>
<tr>
<td>B–11</td>
<td>Task Yield</td>
<td>201</td>
</tr>
<tr>
<td>B–12</td>
<td>Post Semaphore, No Waiting Task</td>
<td>202</td>
</tr>
<tr>
<td>B–13</td>
<td>Post Semaphore, No Context Switch</td>
<td>202</td>
</tr>
<tr>
<td>B–14</td>
<td>Post Semaphore with Task Switch</td>
<td>202</td>
</tr>
<tr>
<td>B–15</td>
<td>Pend on Semaphore, No Context Switch</td>
<td>202</td>
</tr>
<tr>
<td>B–16</td>
<td>Pend on Semaphore with Task Switch</td>
<td>203</td>
</tr>
</tbody>
</table>
## List of Tables

<table>
<thead>
<tr>
<th>Table</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1–1</td>
<td>TI-RTOS Components</td>
<td>13</td>
</tr>
<tr>
<td>1–2</td>
<td>XDCtools Modules Using in C Code and Configuration</td>
<td>18</td>
</tr>
<tr>
<td>1–3</td>
<td>Packages and Modules Provided by SYS/BIOS</td>
<td>19</td>
</tr>
<tr>
<td>1–4</td>
<td>Benefits of Various Object Creation Styles</td>
<td>21</td>
</tr>
<tr>
<td>3–1</td>
<td>Comparison of Thread Characteristics</td>
<td>52</td>
</tr>
<tr>
<td>3–2</td>
<td>Thread Preemption</td>
<td>55</td>
</tr>
<tr>
<td>3–3</td>
<td>Hook Functions by Thread Type</td>
<td>56</td>
</tr>
<tr>
<td>3–4</td>
<td>System Stack Use for Hwi Nesting by Target Family</td>
<td>60</td>
</tr>
<tr>
<td>3–5</td>
<td>System Stack Use for Swi Nesting by Target Family</td>
<td>69</td>
</tr>
<tr>
<td>3–6</td>
<td>Swi Object Function Differences</td>
<td>71</td>
</tr>
<tr>
<td>3–7</td>
<td>Task Stack Use by Target Family</td>
<td>86</td>
</tr>
<tr>
<td>5–1</td>
<td>Timeline for One-shot and Continuous Clocks</td>
<td>129</td>
</tr>
<tr>
<td>7–1</td>
<td>Heap Implementation Comparison</td>
<td>158</td>
</tr>
<tr>
<td>8–1</td>
<td>Proxy to Delegate Mappings</td>
<td>180</td>
</tr>
<tr>
<td>C–1</td>
<td>SYS/BIOS 6 Benchmark Applications</td>
<td>205</td>
</tr>
<tr>
<td>G–1</td>
<td>SPRUEX3V Revision History</td>
<td>248</td>
</tr>
<tr>
<td>G–2</td>
<td>SPRUEX3U Revision History</td>
<td>248</td>
</tr>
<tr>
<td>G–3</td>
<td>SPRUEX3T Revision History</td>
<td>249</td>
</tr>
<tr>
<td>G–4</td>
<td>SPRUEX3S Revision History</td>
<td>249</td>
</tr>
<tr>
<td>G–5</td>
<td>SPRUEX3R Revision History</td>
<td>249</td>
</tr>
<tr>
<td>G–6</td>
<td>SPRUEX3Q Revision History</td>
<td>249</td>
</tr>
<tr>
<td>G–7</td>
<td>SPRUEX3P Revision History</td>
<td>250</td>
</tr>
<tr>
<td>G–8</td>
<td>SPRUEX3O Revision History</td>
<td>250</td>
</tr>
<tr>
<td>G–9</td>
<td>SPRUEX3N Revision History</td>
<td>251</td>
</tr>
</tbody>
</table>
About This Manual

This manual describes SYS/BIOS, which is the Kernel component of TI-RTOS. SYS/BIOS is also called "TI-RTOS Kernel" in some documents. This document was published in conjunction with the release of SYS/BIOS 6.70, but it may be used with later versions of SYS/BIOS if changes to the software do not cause this document to be incorrect.

SYS/BIOS gives developers of mainstream applications on Texas Instruments devices the ability to develop embedded real-time software. SYS/BIOS provides a small firmware real-time library and easy-to-use tools for real-time tracing and analysis.

Versions of SYS/BIOS prior to 6.30 were called DSP/BIOS.

Notational Conventions

This document uses the following conventions:

- Program listings, program examples, and interactive displays are shown in a special typeface. Examples use a bold version of the special typeface for emphasis.

Here is a sample program listing:

```c
#include <xdc/runtime/System.h>
int main(){
    System_printf("Hello World!\n");
    return (0);
}
```

- Square brackets ([ and ]) identify an optional parameter. If you use an optional parameter, you specify the information within the brackets. Unless the square brackets are in a bold typeface, do not enter the brackets themselves.
Related Documentation From Texas Instruments

See the detailed list and links in Section 1.6.

Related Documentation

You can use the following books to supplement this reference guide:


Programming in C, Kochan, Steve G., Hayden Book Company


American National Standard for Information Systems-Programming Language C X3.159-1989, American National Standards Institute (ANSI standard for C); (out of print)

Trademarks

The Texas Instruments logo and Texas Instruments are registered trademarks of Texas Instruments. Trademarks of Texas Instruments include: TI, Code Composer, Code Composer Studio, DSP/BIOS, SPOX, TMS320, TMS320C54x, TMS320C55x, TMS320C62x, TMS320C64x, TMS320C67x, TMS320C28x, TMS320C5000, TMS320C6000 and TMS320C2000.

Windows is a registered trademark of Microsoft Corporation.

Linux is a registered trademark of Linus Torvalds.

All other brand or product names are trademarks or registered trademarks of their respective companies or organizations.

June 22, 2020
Chapter 1
SPRUEX3V—June 2020

About SYS/BIOS

This chapter provides an overview of SYS/BIOS and describes its relationship to TI-RTOS and other TI-RTOS components.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.1</td>
<td>What is SYS/BIOS?</td>
</tr>
<tr>
<td>1.2</td>
<td>How are SYS/BIOS and TI-RTOS Related?</td>
</tr>
<tr>
<td>1.3</td>
<td>How are SYS/BIOS and XDCtools Related?</td>
</tr>
<tr>
<td>1.4</td>
<td>SYS/BIOS Packages and APIs</td>
</tr>
<tr>
<td>1.5</td>
<td>Using C++ with SYS/BIOS</td>
</tr>
<tr>
<td>1.6</td>
<td>For More Information</td>
</tr>
</tbody>
</table>
# 1.1 What is SYS/BIOS?

SYS/BIOS is a scalable real-time kernel. It is designed for applications that require real-time scheduling and synchronization or real-time instrumentation. SYS/BIOS provides preemptive multi-threading, hardware abstraction, real-time analysis, and configuration tools. SYS/BIOS helps minimize memory and CPU requirements on the target.

SYS/BIOS is the “TI-RTOS Kernel” component of the TI-RTOS product. Both names refer to the same component. You may see the “TI-RTOS Kernel” name in other documents and on Texas Instruments websites. This new name does not require any code changes on your part; directory and module names are not affected by this change.

You can install SYS/BIOS by installing TI-RTOS from the CCS App Center (choose Help > CCS App Center in CCS) or by downloading and installing it as a standalone product. CCS v6.0 or higher is required.

SYS/BIOS requires no up-front or run-time license fees.

SYS/BIOS provides the following benefits:

- All SYS/BIOS objects can be configured statically or dynamically.
- To minimize memory size, the APIs are modularized so that only those APIs that are used by the program need to be bound into the executable program. In addition, statically-configured objects reduce code size by eliminating the need to include object creation calls.
- Error checking and debug instrumentation is configurable and can be completely removed from production code versions to maximize performance and minimize memory size.
- Almost all system calls provide deterministic performance to enable applications to reliably meet real-time deadlines.
- To improve performance, instrumentation data (such as logs and traces) is formatted on the host.
- The threading model provides thread types for a variety of situations. Hardware interrupts, software interrupts, tasks, idle functions, and periodic functions are all supported. You can control the priorities and blocking characteristics of threads through your choice of thread types.
- Structures to support communication and synchronization between threads are provided. These include semaphores, mailboxes, events, gates, and variable-length messaging.
- Dynamic memory management services offering both variable-sized and fixed-sized block allocation.
- An interrupt dispatcher handles low-level context save/restore operations and enables interrupt service routines to be written entirely in C.
- System services support the enabling/disabling of interrupts and the plugging of interrupt vectors, including multiplexing interrupt vectors onto multiple sources.
1.2 How are SYS/BIOS and TI-RTOS Related?

TI-RTOS is a scalable, one-stop embedded tools ecosystem for TI devices. It scales from a real-time multitasking kernel (SYS/BIOS) to a complete RTOS solution including additional middleware components and device drivers. By providing essential system software components that are pre-tested and pre-integrated, TI-RTOS enables you to focus on differentiating your application.

SYS/BIOS is the "TI-RTOS Kernel" component of TI-RTOS. Both "SYS/BIOS" and "TI-RTOS Kernel" refer to the same component. You may see the "TI-RTOS Kernel" name in other documents and on Texas Instruments websites. This new name does not require any code or other changes on your part; directory and module names are not affected by this change.

TI-RTOS is not installed automatically as part of the Code Composer Studio v6.0 installation. You can install TI-RTOS from the CCS App Center (choose Help > CCS App Center in CCS). Choose the version of TI-RTOS for your device family. If you use devices in multiple families, you can install multiple TI-RTOS versions.

If you do not use CCS, you can download and install TI-RTOS as a standalone product. In addition to the Texas Instruments Code Generation Tools, TI-RTOS includes support for the IAR and GNU tool chains. You can also download and install SYS/BIOS as a standalone product without the other TI-RTOS components.

TI-RTOS is provided with full source code and requires no up-front or runtime license fees.

The components of TI-RTOS are as follows. Some components are not available for all device families.

<table>
<thead>
<tr>
<th>TI-RTOS Component</th>
<th>Name</th>
<th>PDF Documentation Location</th>
</tr>
</thead>
<tbody>
<tr>
<td>TI-RTOS Instrumentation</td>
<td>UIA</td>
<td>System Analyzer User's Guide -- SPRUH43</td>
</tr>
<tr>
<td>TI-RTOS Networking</td>
<td>NDK</td>
<td>TI Network Developer's Kit (NDK) Guide -- SPRU523</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TI Network Developer's Kit (NDK) API Reference -- SPRU524</td>
</tr>
<tr>
<td>TI-RTOS File System</td>
<td>FatFS</td>
<td>Core SDK User's Guide (HTML file included in installation)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>USB Library API Guide (in TI Resource Explorer)</td>
</tr>
<tr>
<td>TI-RTOS Drivers and Board Initialization</td>
<td>Drivers *Ware, TI-RTOS examples</td>
<td>TI Drivers Runtime API Reference (doxygen content in TI Resource Explorer)</td>
</tr>
</tbody>
</table>

This document refers to the directory where SYS/BIOS is installed as the BIOS_INSTALL_DIR. If you install SYS/BIOS as part of TI-RTOS, this directory will have a path similar to C:\ti\tiRTOS_<target>\_2_##_##_##\products\bios_6_##_##_##, where C:\ti will be the directory where you installed CCS, <target> is the device family, and # is a digit in the version number. If you installed SYS/BIOS as a standalone product, BIOS_INSTALL_DIR is the directory where you installed it.
1.3 How are SYS/BIOS and XDCtools Related?

XDCtools provides the underlying core tooling needed by TI-RTOS and its components, including SYS/BIOS. You must have both XDCtools and SYS/BIOS installed in order to use SYS/BIOS.

XDCtools is installed automatically during the CCS installation.

If you install TI-RTOS or SYS/BIOS as a standalone product (outside of CCS), you will also need to download and install XDCtools. The SYS/BIOS release notes provide information about the versions of XDCtools that are compatible with your version of SYS/BIOS. If you install a new, standalone version of SYS/BIOS, you may need to install a new version of XDCtools.

XDCtools is important to SYS/BIOS users because:

• XDCtools provides the technology that users use to configure the SYS/BIOS and XDCtools modules used by the application. See Section 1.3.2.

• XDCtools provides the tools used to build the configuration file. This build step generates source code files that are then compiled and linked with your application code. See Section 1.3.2.

• XDCtools provides a number of modules and runtime APIs that SYS/BIOS leverages for memory allocation, logging, system control, and more. See Section 1.3.3.

XDCtools is sometimes referred to as "RTSC" (pronounced "rit-see"—Real Time Software Components), which is the name for the open-source project within the Eclipse ecosystem for providing reusable software components (called "packages") for use in embedded systems. For documentation about XDCtools modules, see the online help within CCS. For information about packaging of reusable software components and details about the tooling portion of XDCtools, see the RTSC-pedia web site.

1.3.1 SYS/BIOS as a Set of Packages

SYS/BIOS and XDCtools are sets of "packages," each of which delivers a subset of the product's functionality. XDCtools uses a naming convention for packages to aid readability and to ensure that packages delivered from different sources don't have namespace collisions that will pose problems for the system integrator. If you are familiar with the Java package naming convention, you will find it to be quite similar.

SYS/BIOS packages conform to this convention with names that consist of a hierarchical naming pattern; each level is separated by a period ("."). Usually, the highest level of the name is the vendor ("ti"), followed by the product ("sysbios"), and then followed by the module and submodule names (for example, "knl").

These names have the added benefit of reflecting the physical layout of the package within the file system where SYS/BIOS has been installed. For example, the ti.sysbios.knl package files can be found in the following folder:

BIOS_INSTALL_DIR\bios_6_##_##\packages\ti/sysbios\knl

See Section 1.4 for a partial list of the packages provided by SYS/BIOS and Section 1.3.3 for a partial list of the modules provided by XDCtools.
You can picture the architecture of the tools used to create applications as shown in the following figure. The xdc.runtime package provided by XDCtools contains modules and APIs your application can use along with the modules and APIs in SYS/BIOS.
1.3.2 Configuring SYS/BIOS Using XDCtools

Configuration is an essential part of using SYS/BIOS and is used for the following purposes:

- It specifies the modules and packages that will be used by the application.
- It can statically create objects for the modules that are used by the application.
- It validates the set of modules used explicitly and implicitly to make sure they are compatible.
- It statically sets parameters for the system, modules, and objects to change their runtime behavior.

An application's configuration is stored in one or more script files with a file extension of *.cfg. These are parsed by XDCtools to generate corresponding C source code, C header, and linker command files that are then compiled and linked into the end application. The following diagram depicts a build flow for a typical SYS/BIOS application.

The configuration (*.cfg) file uses simple JavaScript-like syntax to set properties and call methods provided by objects. You can create and modify a configuration file in the following ways:

- Using the visual configuration tool (XGCONF) in CCS.
- Editing the text of the configuration in the cfg Script tab in the XGCONF editor in CCS.
- Editing the *.cfg file directly with a text editor.
The following figure shows the XGCONF configuration tool in CCS being used to configure a static SYS/BIOS Task instance. You can see this configuration for yourself in the “Static Example” SYS/BIOS project template in CCS.

The Task instance named “task0” set up in the configuration tool corresponds to the following configuration script:

```javascript
var Task = xdc.useModule('ti.sysbios.knl.Task');
Task.numPriorities = 16;
Task.idleTaskStackSize = 1024;

var tskParams = new Task.Params;
  tskParams.arg0 = 1;
  tskParams.arg1 = 2;
  tskParams.priority = 15;
  tskParams.stack = null;
  tskParams.stackSize = 1024;
var task0 = Task.create('&task0Fxn', tskParams);
```
1.3.3  XDCtools Modules and Runtime APIs

XDCtools contains several modules that provide basic system services your SYS/BIOS application will need to operate successfully. Most of these modules are located in the xdc.runtime package in XDCtools. By default, all SYS/BIOS applications automatically add the xdc.runtime package during build time.

The functionality provided by XDCtools for use in your C code and configuration file can be roughly divided into four categories. The modules listed in the following table are in the xdc.runtime package, unless otherwise noted.

<table>
<thead>
<tr>
<th>Category</th>
<th>Modules</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>System Services</td>
<td>System</td>
<td>Basic low-level &quot;system&quot; services. For example, character output, printf-like output, and exit handling. See Section 9.3. Proxies that plug into this module include xdc.runtime.SysMin and xdc.runtime.SysStd. See Section 6.3.</td>
</tr>
<tr>
<td></td>
<td>Startup</td>
<td>Allows functions defined by different modules to be run before main(). See Section 6.5.</td>
</tr>
<tr>
<td></td>
<td>Defaults</td>
<td>Sets event logging, assertion checking, and memory use options for all modules for which you do not explicitly set a value. See Section 7.7.1 and Section 9.5.1.1.</td>
</tr>
<tr>
<td></td>
<td>Main</td>
<td>Sets event logging and assertion checking options that apply to your application code.</td>
</tr>
<tr>
<td></td>
<td>Program</td>
<td>Sets options for runtime memory sizes, program build options, and memory sections and segments. This module is used as the &quot;root&quot; for the configuration object model. This module is in the xdc.cfg package. See Section 3.4.1, Section 6.4, and Section 7.3.2.</td>
</tr>
<tr>
<td>Memory Management</td>
<td>Memory</td>
<td>Creates/frees memory heaps statically or dynamically. See Section 7.7.</td>
</tr>
<tr>
<td>Diagnostics</td>
<td>Log and Loggers</td>
<td>Allows events to be logged and then passes those events to a Log handler. Proxies that plug into this module include xdc.runtime.LoggerBuf and xdc.runtime.LoggerSys. See Section 9.2.1 and Section 3.6.4.</td>
</tr>
<tr>
<td></td>
<td>Error</td>
<td>Allows raising, checking, and handling errors defined by any modules. See Section 6.7 and Section 9.3.</td>
</tr>
<tr>
<td></td>
<td>Diags</td>
<td>Allows diagnostics to be enabled/disabled at either configuration- or runtime on a per-module basis. See Section 9.5.1.</td>
</tr>
<tr>
<td></td>
<td>Timestamp and Providers</td>
<td>Provides time-stamping APIs that forward calls to a platform-specific time stamper (or one provided by CCS). See Section 5.5.</td>
</tr>
<tr>
<td></td>
<td>Text</td>
<td>Provides string management services to minimize the string data required on the target. See Section 6.8.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Gate</td>
<td>Protects against concurrent access to critical data structures. See Section 4.3.</td>
</tr>
<tr>
<td></td>
<td>Sync</td>
<td>Provides basic synchronization between threads using wait() and signal() functions. See Section E.6.</td>
</tr>
</tbody>
</table>
1.4 SYS/BIOS Packages and APIs

SYS/BIOS provides the following packages:

Table 1–3.  Packages and Modules Provided by SYS/BIOS

<table>
<thead>
<tr>
<th>Package</th>
<th>Description</th>
<th>See</th>
</tr>
</thead>
<tbody>
<tr>
<td>ti.sysbios.family.*</td>
<td>Contains target/device-specific functions.</td>
<td>Section 8.5</td>
</tr>
<tr>
<td>ti.sysbios.gates</td>
<td>Contains several implementations of the IGateProvider interface for use in various situations. These include GateHwi, GateSwi, GateTask, GateMutex, and GateMutexPri.</td>
<td>Section 4.3</td>
</tr>
<tr>
<td>ti.sysbios.hal</td>
<td>Contains Hwi, Timer, Seconds, and Cache modules</td>
<td>Section 8.2, Section 8.3, Section 8.4, Section 8.4.</td>
</tr>
<tr>
<td>ti.sysbios.heaps</td>
<td>Provides several implementations of the XDCtools IHeap interface. These include HeapBuf (fixed-size buffers), HeapMem (variable-sized buffers), and HeapMultiBuf (multiple fixed-size buffers).</td>
<td>Chapter 7</td>
</tr>
<tr>
<td>ti.sysbios.interfaces</td>
<td>Contains interfaces for modules to be implemented, for example, on a device or platform basis.</td>
<td>--</td>
</tr>
<tr>
<td>ti.sysbios.knl</td>
<td>Contains modules for the SYS/BIOS kernel, including Swi, Task, Idle, and Clock. Also contains modules related to inter-process communication: Event, Mailbox, and Semaphore.</td>
<td>Chapter 3, Chapter 4, Chapter 5</td>
</tr>
<tr>
<td>ti.sysbios.utils</td>
<td>Contains the Load module, which provides global CPU load as well as thread-specific load.</td>
<td>Section 9.2</td>
</tr>
</tbody>
</table>

Each SYS/BIOS package provides one or more modules. Each module, in turn, provides APIs for working with that module. APIs have function names of the form Module_actionDescription(). For example, Task_setPri() sets the priority of a Task thread.

In order to use a module, your application must include the standard SYS/BIOS header file and the header file for that module. For example:

```c
#include <xdc/std.h>       /* initializes XDCtools */
#include <ti/sysbios/BIOS.h>     /* initializes SYS/BIOS */
#include <ti/sysbios/knl/Task.h> /* initializes Task module */
```

The API functions provided by each module differ.

1.4.1 SYS/BIOS Object Creation

Several modules support the creation of instance objects. Such modules include Hwi, Task, Swi, Semaphore, Mailbox, Queue, Event, Clock, Timer, and various types of Gate and Heap modules. For example, the Task module allows you to create several Task objects. Each Task object corresponds to a thread that has its own function, priority, and timing.
Such instance objects can be created in the following three ways.

- **Module_create()** The create APIs require a heap to allow dynamic memory allocation of the object. All module create functions require that an Error_Block structure be passed to the creation function. For example, this code fragment creates and posts a Semaphore object:

```c
#include <ti/sysbios/knl/Semaphore.h>
Semaphore_Handle semaphore0;
Semaphore_Params semaphoreParams;
Error_Block eb;
...
Error_init(&eb);
Semaphore_Params_init(&semaphoreParams);
semaphore0 = Semaphore_create(0, &semaphoreParams, &eb);
if (semaphore0 == NULL) {
  ...
} Semaphore_post(semaphore0);
```

- **Module_construct()** The construct APIs must be passed an object structure instead of dynamically allocating the object from a heap. Avoiding dynamic memory allocation helps reduce the code footprint. Most Module_construct() APIs do not require an Error_Block; this also helps reduce the code footprint. Some Module_construct() APIs may allocate memory internally. For example, Task_construct() allocates a task stack if you do not provide one. For example, this code fragment constructs and posts a Semaphore object:

```c
#include <ti/sysbios/knl/Semaphore.h>
Semaphore_Struct semaphore0Struct;
Semaphore_Params semaphoreParams;
...
Semaphore_Params_init(&semaphoreParams);
Semaphore_construct(&semaphore0Struct, 0, &semaphoreParams);
...
Semaphore_post(Semaphore_handle(&semaphore0Struct));
```

- **Static creation** allows you to specify the object in the application’s *.cfg file. No heap is needed if you are statically allocating objects. A structure for the object is added to the source file generated for the configuration. See Chapter 2 for details about configuration. For example, the following *.cfg statements configure a Semaphore object. The Semaphore would be posted with the same Semaphore_post() call used with Semaphore_create().

```c
var semaphore0Params = new Semaphore.Params();
Program.global.semaphore0 = Semaphore.create(0, semaphore0Params);
```

Statically created objects cannot be destroyed at runtime.

The TI-RTOS examples use the Module_construct() mechanism, which is a nice compromise between static and dynamic creation. However, each style of object creation has its own pros and cons as listed in Table 1–4.
Table 1–4. Benefits of Various Object Creation Styles

<table>
<thead>
<tr>
<th>Flexible vs. Fixed Application</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Flexible: dynamic object creation and destruction</td>
<td>Flexible: dynamic object creation and destruction</td>
<td>Fixed: no dynamic object creation or destruction</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Programming Flow</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Simple</td>
<td>Application must manage object Structs (instead of just Handles). Most construct calls cannot fail (see specific APIs for details).</td>
<td>Simple. Application uses the *.cfg file to create objects.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Dynamic Memory Allocation</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Yes</td>
<td>Goal to minimize or eliminate dynamic memory allocation</td>
<td>Goal to minimize or eliminate dynamic memory allocation</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Code Footprint</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Larger</td>
<td>Simple</td>
<td>Simple</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Runtime Object View (ROV)</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Always works for created objects</td>
<td>ROV does not show constructed objects that are embedded in an application structure or on a stack.</td>
<td>Always works for configured objects</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Object Destruction</th>
<th>Dynamic Module_create()</th>
<th>Dynamic Module_construct()</th>
<th>Static Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td>Yes, with Module_delete()</td>
<td>Yes, with Module_destruct()</td>
<td>No</td>
<td></td>
</tr>
</tbody>
</table>

See Appendix C of this document and the TI-RTOS Kernel Documentation Overview (in BIOS_INSTALL_DIR/docs/Documentation_Overview.html for more information.

Notice that each of the methods of object creation uses a Module_Params structure. These structures contain instance configuration parameters to control how the instance behaves. For example, the Semaphore_Params structure is defined by SYS/BIOS as follows:

```c
typedef struct Semaphore_Params {   // Instance config-params structure
    IInstance_Params *instance;     // Common per-instance configuration
    Event_Handle     event;         // Event instance to use if non-NULL
    UInt             eventId;       // eventId if using Events
    Semaphore_Mode   mode;          // Semaphore mode: COUNTING or BINARY
} Semaphore_Params;
```

The following application code creates the Semaphore parameters structure, initializes the structure with the default parameters, and sets the "mode" parameter to BINARY to specify that this is a binary semaphore. Following this code, you would use either Semaphore_create() or Semaphore_construct() to create an instance.

```c
Semaphore_Params semaphoreParams;

Semaphore_Params_init(&semaphoreParams);
semParams.mode = Semaphore_Mode_BINARY;
```

For information about the parameter structure and its individual parameters for any module, see the API Reference Help System described in Section 1.6.1.
1.4.2 POSIX Thread Support

SYS/BIOS also provides a subset of the POSIX thread (pthread) APIs. These include pthread threads, mutexes, read-write locks, barriers, and condition variables. The pthread APIs can simplify porting applications from a POSIX environment to SYS/BIOS, as well as allowing the same code to be compiled to run in a POSIX environment and with SYS/BIOS. As the pthread APIs are built on top of the SYS/BIOS Task and Semaphore modules, some of the POSIX APIs can be called from SYS/BIOS Tasks.

For details about the POSIX thread APIs supported by SYS/BIOS, see the TI-POSIX User's Guide in the /docs/tiposix/Users_Guide.html file within the TI-RTOS Kernel installation. For more detail, see the official POSIX specification and documentation of the generic POSIX implementation.

1.5 Using C++ with SYS/BIOS

SYS/BIOS applications can be written in C or C++. An understanding of several issues regarding C++ and SYS/BIOS can help to make C++ application development proceed smoothly. These issues concern memory management, name mangling, calling class methods from configured properties, and special considerations for class constructors and destructors.

SYS/BIOS provides an example that is written in C++. The example code is in the bigtime.cpp file in the packages\ti\sysbios\examples\generic\bigtime directory of the SYS/BIOS installation.

1.5.1 Memory Management

The functions new and delete are the C++ operators for dynamic memory allocation and deallocation. For TI targets, these operators use malloc() and free(). SYS/BIOS provides reentrant versions of malloc() and free() that internally use the xdc.runtime.Memory module and (by default) the ti.sysbios.heaps.HeapMem module.

1.5.2 Name Mangling

The C++ compiler implements function overloading, operator overloading, and type-safe linking by encoding a function's signature in its link-level name. The process of encoding the signature into the linkname is referred to as name mangling.

Name mangling could potentially interfere with a SYS/BIOS application since you use function names within the configuration to refer to functions declared in your C++ source files. To prevent name mangling and thus to make your functions recognizable within the configuration, it is necessary to declare your functions in an extern C block as shown in the following code fragment from the bigtime.cpp example:

```cpp
/*
 * Extern "C" block to prevent name mangling
 * of functions called within the Configuration Tool
 */
extern "C" {
    /* Wrapper functions to call Clock::tick() */
    void clockTask(Clock clock);
    void clockPrd(Clock clock);
    void clockIdle(void);
} // end extern "C"
```
This extern C block allows you to refer to the functions within the configuration file. For example, if you have a Task object that should run clockTask() every time the Task runs, you could configure a Task as follows:

```c
var task0Params = new Task.Params();
task0Params.instance.name = "task0";
task0Params.arg0 = $externPtr("cl3");
Program.global.task0 = Task.create("&clockTask", task0Params);
```

Notice that in the configuration example above, the arg0 parameter of the Task is set to $externPtr("cl3"). The C++ code to create a global clock object for this argument is as follows:

```c
/* Global clock objects */
Clock cl3(3); /* task clock */
```

Functions declared within the extern C block are not subject to name mangling. Since function overloading is accomplished through name mangling, function overloading has limitations for functions that are called from the configuration. Only one version of an overloaded function can appear within the extern C block. The code in the following example would result in an error.

```c
extern "C" {                          // Example causes ERROR
    Int addNums(Int x, Int y);
    Int addNums(Int x, Int y, Int z); // error, only one version
    // of addNums is allowed
}
```

While you can use name overloading in your SYS/BIOS C++ applications, only one version of the overloaded function can be called from the configuration.

Default parameters is a C++ feature that is not available for functions called from the configuration. C++ allows you to specify default values for formal parameters within the function declaration. However, a function called from the configuration must provide parameter values. If no values are specified, the actual parameter values are undefined.

### 1.5.3 Calling Class Methods from the Configuration

Often, the function that you want to reference within the configuration is the member function of a class object. It is not possible to call these member functions directly from the configuration, but it is possible to accomplish the same action through wrapper functions. By writing a wrapper function which accepts a class instance as a parameter, you can invoke the class member function from within the wrapper.

A wrapper function for a class method is shown in the following code fragment from the bigtime.cpp example:

```c
/* ======== clockPrd ========
* Wrapper function for PRD objects calling Clock::tick()
*/
void clockPrd(Clock clock)
{
    clock.tick();
    return;
}
```

Any additional parameters that the class method requires can be passed to the wrapper function.
1.5.4 Class Constructors and Destructors

Any time that a C++ class object is instantiated, the class constructor executes. Likewise, any time that a class object is deleted, the class destructor is called. Therefore, when writing constructors and destructors, you should consider the times at which the functions are expected to execute and tailor them accordingly. It is important to consider what type of thread will be running when the class constructor or destructor is invoked.

Various guidelines apply to which SYS/BIOS API functions can be called from different SYS/BIOS threads (tasks, software interrupts, and hardware interrupts). For example, memory allocation APIs such as Memory_alloc() and Memory_calloc() cannot be called from within the context of a software interrupt. Thus, if a particular class is instantiated by a software interrupt, its constructor must avoid performing memory allocation.

Similarly, it is important to keep in mind the time at which a class destructor is expected to run. Not only does a class destructor execute when an object is explicitly deleted, but also when a local object goes out of scope. You need to be aware of what type of thread is executing when the class destructor is called and make only those SYS/BIOS API calls that are appropriate for that thread. For further information on function callability, see the CDOC online documentation.

1.6 For More Information

You can read the following additional documents to learn more about TI-RTOS, SYS/BIOS, XDCtools, and Code Composer Studio:

- **SimpleLink SDK**
  - *SimpleLink MCU SDK User's Guide* (HTML file included in the SDK installation)
  - TI Resource Explorer (online or installed within CCS) contains links to many other documents

- **TI-RTOS Kernel (SYS/BIOS)**
  - Documentation Overview ([BIOS_INSTALL_DIR/docs/Documentation_Overview.html](BIOS_INSTALL_DIR/docs/Documentation_Overview.html))
  - TI’s E2E Community lets you submit your questions.
  - TI-RTOS Kernel Product Folder on TI.com
  - Embedded Software Download Page

- **XDCtools**
  - RTSC-Pedia Wiki
  - Embedded Software Download Page

- **Code Composer Studio (CCS)**
  - CCS online help. Choose Help > Help Contents in CCS.
  - Code Composer forum on TI’s E2E Community
1.6.1 Using the API Reference Help System

The API Reference help for TI-RTOS, SYS/BIOS, and the other TI-RTOS components is called "CDOC".

1. Open TI Resource Explorer either within CCStudio or online.
2. Expand Software then the tree for your version of the SimpleLink SDK. Open Documents then Documentation Overview.
3. In the Kernel Documentation section, click the link for the TI-RTOS Kernel Runtime APIs and Configuration (cdoc).
4. Click "+" next to a repository to expand its list of packages. Click "+" next to a package name to see the list of modules it provides. Select a package or module to see its reference information.

In addition to the SYS/BIOS modules, this list includes all the modules provided by TI-RTOS, the TI-RTOS components that are available for your device family, and XDCtools.

The SYS/BIOS configuration property and C API documentation is within the "ti.sysbios" package. To view reference documentation on memory allocation, logs, timestamps, asserts, and system, expand the "xdc.runtime" package.

Each reference page is divided into two main sections:

• **C Reference.** This section has blue table borders. It begins with a table of the APIs you can call from your application’s C code. It also lists C structures, typedefs, and constants that are defined by including this module’s header file. A description of the module’s use follows; often this includes a table of the calling contexts from which each API can be called. Detailed syntax for the functions, typedefs, structures, and constants follows.

• **Configuration Reference.** This section has blue-gray table borders. You can jump to this section in any topic by clicking the Configuration settings link near the top of the page. This section provides information you can use when you are configuring the application in the *.cfg file (either using XGCONF or editing the source code for the configuration file directly). This section lists the types, structures, and constants defined by the module. It also lists parameters you can configure on a module-wide basis, and parameters that apply to individual instances you create.

The Documentation Overview in the TI Resource Explorer provides a number of other links, including a link to the TI Drivers Runtime APIs reference documentation.

If you installed SYS/BIOS as a standalone tool, you can open the online help for SYS/BIOS by opening the BIOS_INSTALL_DIR\docs\Bios_APIs.html file in a web browser.
This chapter describes how to configure and build SYS/BIOS applications.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.1 Creating a SYS/BIOS Project with the TI Resource Explorer</td>
<td>27</td>
</tr>
<tr>
<td>2.2 Adding SYS/BIOS Support to an Existing Project</td>
<td>28</td>
</tr>
<tr>
<td>2.3 Configuring SYS/BIOS Applications</td>
<td>29</td>
</tr>
<tr>
<td>2.4 Building SYS/BIOS Applications</td>
<td>41</td>
</tr>
</tbody>
</table>
2.1 Creating a SYS/BIOS Project with the TI Resource Explorer

The steps for creating a SYS/BIOS example project differ depending on which product package you installed:

**Processor SDK:** For example, for C665x targets. Refer to the PDK documentation.

**SimpleLink SDK:** For example, for CC32xx targets. Refer to the SDK documentation.

**TI-RTOS:** For example, TI-RTOS for Tiva. Refer to the TI-RTOS documentation.

**Standalone SYS/BIOS:** Follow these steps:

1. In Code Composer Studio, choose **Project > New > CCS Project**.

2. In the New CCS Project Wizard, select your target. Then, under **Project templates and examples**, select one of the SYS/BIOS examples. A description of the selected example is shown to the right of the example list.

   To get started with SYS/BIOS, you can choose one of the Generic Examples, such as the Log Example or Task Mutex Example. When you are ready to create a production project, you might choose the "Minimal" or "Typical" example depending on how memory-limited your target is. For some device families, device-specific SYS/BIOS examples are also provided.

3. Click **Finish**. You can expand the project to view or change the source code and configuration file.

4. Build the project using CCS. If you want to change any build options, right click on the project and select **Properties** from the context menu. For example, you can change compiler, linker, and RTSC (XDCtools) options.

5. Create a Target Configuration within CCS. Launch the configuration and connect to your target.

6. Launch a debug session for the project in CCS and switch to the **CCS Debug** perspective.
2.2 Adding SYS/BIOS Support to an Existing Project

If you created a SYS/BIOS project using the TI Resource Explorer, a configuration file is automatically added to your project and SYS/BIOS support is automatically enabled.

**Note:** Applications that can use SYS/BIOS are referred to as having RTSC support enabled. RTSC is Real-Time Software Components, which is implemented by the XDCtools component. See Section 1.3 for details.

If you start with an empty CCS project template, you can add a configuration file for use with SYS/BIOS to your CCS project by choosing File > New > RTSC Configuration File. If the project does not have RTSC support enabled, you will be asked if you want to enable RTSC support for the current project.
2.3 Configuring SYS/BIOS Applications

You configure SYS/BIOS applications by modifying the *.cfg configuration file in the project. These files are written in a scripting language that is a superset of JavaScript. While you can edit this file with a text editor, CCS provides a graphical configuration editor called XGCONF.

XGCONF is useful because it gives you an easy way to view the available options and your current configuration. Since modules and instances are activated behind-the-scenes when the configuration is processed, XGCONF is a useful tool for viewing the effects of these internal actions and for detecting conflicts.

For example, the following figure shows the XGCONF configuration tool in Code Composer Studio used to configure a static SYS/BIOS Swi (software interrupt) instance.

The `cfg Script` tab shows that the code to create this Swi would be as follows:

```javascript
var Swi = xdc.useModule('ti.sysbios.knl.Swi');
/* Create a Swi Instance and manipulate its instance parameters. */
var swiParams = new Swi.Params;
swiParams.arg0 = 0;
swiParams.arg1 = 1;
swiParams.priority = 7;
Program.global.swi0 = Swi.create('&swi0Fxn', swiParams);
/* Create another Swi Instance using the default instance parameters */
Program.global.swi1 = Swi.create('&swi1Fxn');
```
2.3.1 Opening a Configuration File with XGCONF

To open XGCONF, follow these steps:

1. Make sure you are in the CCS Edit perspective of CCS. If you are not in that perspective, click the perspective link in the upper-right corner to switch back.

2. Double-click on the *.cfg configuration file in the Project Explorer tree. While XGCONF is opening, the CCS status bar shows that the configuration is being processed and validated. (If your project does not yet contain a *.cfg file, see Section 2.2.)

3. When XGCONF opens, you see the Welcome sheet for SYS/BIOS. This sheet provides links to SYS/BIOS documentation resources.

4. Click the System Overview link to see a handy overview of the main modules you can use in SYS/BIOS applications (see page 35).

Note: If the configuration is shown in a text editor instead of XGCONF, right-click on the *.cfg file and choose Open With > XGCONF.
2.3.2 Performing Tasks with XGCONF

The following list shows the configuration tasks you can perform with XGCONF and provides links to explain how:

- Make more modules available. See page 34.
- Find a module. See page 33 and page 34.
- Add a module to the configuration. See page 33.
- Delete a module from the configuration. See page 34.
- Add an instance to the configuration. See page 34.
- Delete an instance from the configuration. See page 34.
- Change property values for a module. See page 35.
- Change property values for an instance. See page 35.
- Get help about a module. You can right-click in most places in XGCONF for and choose Help to get information about a specific module or property. See page 34 and page 35.
- Configuring the memory map and section placement. The configuration file allows you to specify which sections and heaps are used by various SYS/BIOS modules, but not their placement on the target. Memory mapping and section placement is described in Chapter 7.
- Save the configuration or revert to the last saved file. See page 31.
- Fix errors in the configuration. See page 39.

You can open multiple configuration files at the same time. However, using XGCONF is resource intensive and opening more than one file may slow down your system.

2.3.3 Saving the Configuration

If you have modified the configuration, you can press Ctrl+S to save the file. Or, choose File > Save from the CCS menu bar.

In the cfg Script tab of a configuration, you can right-click and choose to Revert File to reload the last saved configuration file or Save to save the current configuration to a file.

See Section 2.3.9 for information about the validation checks performed when you save a configuration with XGCONF.
2.3.4 About the XGCONF views

The XGCONF tool is made up of several panes that are used together:

1. **Available Products view** lets you add modules to your configuration. By default, this view appears in the lower-left corner of the window. See Section 2.3.5.

2. **Outline view** shows modules used in your current configuration and lets you choose the module to display in the Properties view. By default, this view appears on the right side of the window. Two display modes are provided: a user configuration list and a configuration results tree. See Section 2.3.6.

3. **Property view** shows the property settings of the selected module or instance and lets you make changes. You can also use the **cfg Script** tab in the Property view to modify the script directly. See Section 2.3.7.

4. **Problems view** appears if errors and warnings are detected in the configuration during validation. See Section 2.3.8.
### 2.3.5 Using the Available Products View

The **Available Products** view lists the packages and modules available for use in your configuration. It lists both modules you are already using and modules you can add to your configuration. The list is organized first by the software component and then using functional categories.

Modules you can configure are listed in this tree. Modules that do not apply to your target or are only used internally are hidden in this tree.

#### Finding Modules

To find a particular module, expand the tree to see modules. To find the SYS/BIOS modules, expand the **TI-RTOS** item and then the **Products** item.

If you don’t know where a module is located or several modules have similar names, type text in the "type filter text" box. For example, you can type "gate" to find all the Gate implementations in XDCtools, SYS/BIOS, and any other repositories. You can use * and ? as wildcard characters.

If you want to look for a module using its full path within the repository, right-click and choose **Show Repositories**. After the category-based tree, you will see an **All Repositories** node. You can expand this node to find particular modules. For example, the full path to the SYS/BIOS Task module is `ti.sysbios.knl.Task`.

Note that if you turn on **Show Repositories**, all modules are listed. This includes modules that do not apply to your target family and some modules (often shown as red balls) that you cannot add to the configuration.

#### Adding Modules and Instances to the Configuration

To start using a module, right-click and choose **Use** `<module>` . For example, choosing **Use Swi** adds the ability to create and configure software interrupts to your application. You can also drag modules from the Available Products view to the Outline view to add them to the configuration.

When you select a module in the Available Products view, you see the properties you can set for that module in the Property view (whether you are using it yet or not). When you add use of a module to the configuration, that module is shown in the Outline view.

You can get help on a particular module by right-clicking on the module name and choosing **Help** from the pop-up menu.

Adding a module to the configuration causes an `xdc.useModule()` statement to be added to the configuration script.
Managing the Available Products List

When you open a configuration file with XGCONF, the package repositories that your application is set to use in the Properties dialog are scanned for modules and the results are listed here.

You can add or remove products by right-clicking and choosing Add/Change Products. (This opens the dialog you see by choosing Project > Properties from the CCS menus, then choosing the CCS General category and the RTSC tab.) Check boxes next to versions of products you want to be able to use. If a product isn't listed, click the Add button to browse for a package repository in the file system. When you click OK, the Available Products list is refreshed.

If TI-RTOS is selected, all the components included with TI-RTOS—including SYS/BIOS—will be available for use.

You can open the Package Repository Path Browser by right-clicking and choosing Check Path. This tool lists all the repositories and packages on the package path, shows the effects of adding repositories or changing the order of locations in the path, and sorts the packages by various fields.

If there is a problem with the display or you have changed something in the file system that should affect the modules shown, you can right-click and choose Refresh View.

2.3.6 Using the Outline View

The Outline view shows modules and instances that are available for configuration in your *.cfg file. You can view the Outline in two ways:

- **Show User Configuration.** Select the icon. This is the easier-to-use view. This view mode shows a flat list of only those modules directly referenced in the *.cfg file and instances created in the *.cfg file. You can use this view to add instances of modules and delete the use of a module use from the configuration.

- **Show Configuration Results.** Select the icon. This is the more advanced view. This mode shows a tree view of all modules and instances that are used both implicitly (behind the scenes) and explicitly (because they are referenced directly in the *.cfg file). You can edit any module that does not have the "locked" icon. You can "unlock" some locked modules by choosing to add them to your configuration. Instances that are shown as "locked" are used internally, and you should not attempt to modify them.

As in the Available Products view, you can type filter text at the top of the Outline view to find modules and instances by name.

To create an instance, right-click on a module and choose New <module>. For example, New Semaphore. Notice that not all modules let you create instance objects.

If you want to delete an instance from the configuration, right-click on that instance in the Outline view, and select Delete <name> from the menu.

When you select a module or instance in the Outline view, you see the properties you can set for that module in the Property view.

You can get help on a particular module by right-clicking on the module name and choosing Help from the pop-up menu. Help on SYS/BIOS and XDCtools configuration is in the blue-gray (Configuration settings) section of the online documentation. For each module, the configuration help follows the sections that document that module’s C APIs.
Some modules have a red ball next to them, while others have a blue ball. The blue ball indicates that this is a target module, which provides code or data that can be referenced at runtime on the embedded target. The red ball indicates that this is a meta-only module, which exists in the configuration but does not directly exist on the target.

To stop using a module in the configuration, right-click on that module in the Outline view, and select Stop Using <module> from the menu. Deleting a module from the configuration removes the corresponding xdc.useModule() statement and any instances of the module from the configuration script. If deleting the module would result in an invalid script, an error message is displayed and the module is not deleted. This can happen if a different module or instance refers to it in the script, for example in an assignment to a proxy or a configuration parameter.

2.3.7 Using the Property View

If you select a module or instance in the Outline view or Available Products view, the Property view shows properties for the selected item. There are several ways to view the properties.

- **System Overview.** This sheet provides a block diagram overview of the modules in TI-RTOS or SYS/BIOS. See page 35.

- **Module, Instance, or Basic.** This layout organizes the properties visually. See page 37.

- **Advanced.** This layout provides a tabular list of property names and lets you set values in the table. See page 38.

- **cfg Script.** The cfg script editor lets you edit the configuration script using a text editor. See page 38.

All the property sheets you have viewed are accessible from the tabs at the bottom of the Property view.

You can use the arrow buttons in the upper-right of the Property view to move through sheets you have already viewed. The Home icon returns you to the BIOS module System Overview.

For numeric fields, you can right-click on a field and choose Set Radix to choose whether to display this field as a decimal or hex value.

Point to a field with your mouse for brief information about a property. Right-click on a field and choose Help to jump directly to the documentation for that property. Click the Help icon to get documentation for the current module. Help on SYS/BIOS and XDCtools configuration is in the blue-gray section of the online documentation. For each module, the configuration help follows the blue sections that document that module’s C APIs.

**System Overview Block Diagram**

The System Overview shows all of the core modules in SYS/BIOS as blocks. A green checkmark shows the modules that are currently used by your configuration. You can add other modules in the diagram to your configuration by right-clicking on the block and choosing Use. You can configure any module by clicking on it.
To return to the System Overview from some other property sheet, select the BIOS module in the Outline View and click the System Overview button.

You can add object instances to the configuration by right-clicking on a module and choosing the New command.
Module and Instance Property Sheets

The Module and Instance property sheets organize properties into categories and provides brief descriptions of some properties. Checkboxes, selection fields, and text fields are provided depending on the type of values a property can have.

Click the Module button to see and modify global properties for the module. In the Module property sheet for optional modules, you can uncheck the Add <module> to my configuration box to stop using a module. If you aren’t using a module, you can add it to your configuration by checking the box.

Click the Instance button to see properties for instances. A list on the left side of the page shows the instances that have been created and lets you add or remove instances.

Some advanced properties aren’t shown in the Module and Instance sheets. For these, see the Advanced sheet (page 38).

As you change properties, they are applied to the configuration and validated. However, they are not saved until you save the configuration file.

You can fold up or reopen portions of the property sheet by clicking the arrow next to a section heading.
### Advanced Properties Sheet

The **Advanced** layout provides a tabular list of property names and lets you set values in the table.

![Advanced Properties Sheet](image)

To modify the value of a property, click on a row in the Value column and type or select a new value.

When you type a value for a property, XGCONF checks to make sure the type of the value matches the type expected for the property. This is separate from the more extensive validation checks that are performed when you save a configuration.

For many modules, the Advanced layout has both a **Basic** tab and an **xdc.runtime** tab. The Basic tab shows the same properties as the Basic view, but in tabular form. The xdc.runtime tab shows `<module>.common$` properties inherited by the module. For example, these include properties related to the memory used by the module and any instances, the diagnostics settings for the module, and the Gate object used by this module if any. In addition, events, asserts, and errors that can occur for this module are listed. See the online documentation for xdc.runtime.Types for more about the common$ properties.

Point to a field with your mouse for brief information about a property. Right-click on a field and choose Help to jump directly to the documentation for that property. Click the Help icon to get documentation for the current module.

#### Cfg Script Editor

The cfg script editor lets you edit the configuration script using a text editor by choosing the **cfg Script** tab. Some advanced scripting features are available only by editing the script directly. For more information see links from the [http://rtsc.eclipseprojects.io/docs-tip/RTSC_Scripting_Primer](http://rtsc.eclipseprojects.io/docs-tip/RTSC_Scripting_Primer) wiki page.

You can use Ctrl+S to save any changes you make. You can right-click on the file and choose **Undo Typing** to undo the most recent graphical editing operation, or **Revert File** to return to the most recently saved version of this file. When you save the file, it is validated and the other panes are refreshed to reflect your changes.

When you select a module or instance in the Outline view, the **cfg Script** tab highlights all the lines of the configuration file that are related to that module or instance.
2.3.8 Using the Problems View

The **Problems** view lists all errors and warnings detected during a build or validation of the configuration script. This view is automatically displayed whenever new errors or warnings are detected.

The Outline view shows an error icon next to any modules or instances for which problems were detected. If you select such a module, the Properties view shows a red X icon next to the properties that are incorrectly set.

If you double-click on an item in the Problems view while the **cfg Script** tab is displayed, the **cfg Script** tab highlights the statement that caused the error or warning, and an error icon is shown in the left margin. Position indicators for any problems also appear to the right of the scrollbar.

Depending on the type of problem, the validation may only report the first item found in the configuration. For example, a syntax error such as an unknown variable in the script may prevent later problems, such as invalid values being assigned to a property, from being reported. Such problems can be detected after you fix the syntax error and re-validate the configuration.

If there is not enough detail available to identify a specific configuration parameter, no problem icon will be shown in the Properties tab. A problem icon will still be shown on the module or instance in the Outline view.

You can sort the problem list by clicking on the headings. You can filter the problem list to display fewer items by clicking the **Filter** icon to open the Filters dialog.

2.3.9 Finding and Fixing Errors

A configuration is validated if you perform any of the following actions:

- Add a module to be used in the configuration
- Delete a module from use by the configuration
- Add a instance to the configuration
- Delete an instance from the configuration
- Save the configuration

You can force the configuration to be validated by saving the configuration or by clicking the **Refresh** icon.
Validation means that semantic checks are performed to make sure that, for example, objects that are referenced actually exist. These checks also make sure that values are within the valid ranges for a property. Some datatype checking is performed when you set a property in the Properties tab.

If you are editing the configuration code directly in the **cfg Script** tab, the configuration is validated only if you save the configuration.

While a configuration is being validated (this takes a few seconds), you see the a progress icon in the lower-right corner of the window.

![Validating mutex.cfg](image)

After the configuration has been validated, any errors that were detected in the configuration are shown in the Problems view. For example, if you delete the statement that creates an instance that is referenced by another configuration parameter, you will see an error.

See Section 2.3.8 for more about finding and fixing errors.

### 2.3.10 Accessing the Global Namespace

Many of the configuration examples in this document define variables in the `Program.global` namespace. For example:

```
Program.global.myTimer = Timer.create(1, "&myIsr", timerParams);
```

The Program module is the root of the configuration object model created by XDCtools; the Program module is implicitly used by configuration scripts; you do not need to add a `useModule` statement to make it available.

Variables defined in `Program.global` become global symbols that can be used to directly reference objects in C code. These objects are declared in a generated header file. In order to use these variables, your C code needs to include the generated header file as follows:

```
#include <xdc/cfg/global.h>
```

C code can then access these global symbols directly. For example:

```
Timer_reconfig(myTimer, tickFxn, &timerParams, &eb);
```

If you do not want to include the generated `global.h` file, you can declare external handles explicitly. For example, adding the following declaration to your C code would allow you to use the statically configured `myTimer` object in the previous example:

```
#include <ti/sysbios/hal/Timer.h>

extern Timer_Handle myTimer;
```

For more about the Program module, see [http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html](http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html).
2.4 Building SYS/BIOS Applications

When you build an application project, the associated configuration file is rebuilt if the configuration has
been changed. The folders listed in the "Includes" list of the CCS project tree (except for the compiler-
related folder) are folders that are on the package path.

To build a project, follow these steps:
1. Choose Project > Build Project.
2. Examine the log in the Console view to see if errors occurred.
3. After you build the project, look at the C/C++ Projects view. You can expand the Debug folder to see
the files that were generated by the build process.

For help with build errors, see the wiki page at http://rtsc.eclipseprojects.io/docs-tip/Trouble_Shooting.

2.4.1 Understanding the Build Flow

The build flow for SYS/BIOS applications begins with an extra step to process the configuration file (*.cfg)
in the project. The configuration file is processed by XDCtools. If you look at the messages printed during
the build, you will see a command line that runs the "xs" executable in the XDCtools component with the
"xdc.tools.configuro" tool specified. For example:

```
'Invoking: XDCtools'
"<xdc_install_dir>/xs" --xdcpath="<bios_install_dir>/packages;" xdc.tools.configuro
-o configPkg -t ti.targets.arm.elf.M3 -p ti.platforms.concertoM3:F28M35H52C1
-r release -c "C:/ccs/ccsv6/tools/compiler/tms470" "../example.cfg"
```

In CCS, you can control the command-line options used with XDCtools by choosing Project > Properties
from the menus and selecting the Build > XDCtools category.

Target settings for processing your individual project are in the RTSC tab of the CCS General category.
(RTSC is the name for the Eclipse specification implemented by XDCtools.)
When XDCtools processes your *.cfg file, code is placed in the `<project_dir>/<configuration>/configPkg` directory (where `<configuration>` is Debug or Release depending on your active CCS configuration. This code is compiled so that it can be linked with your final application. In addition, a compiler.opt file is created for use during program compilation, and a linker.cmd file is created for use in linking the application. You should not modify the files in the `<project_dir>/<configuration>/configPkg` directory after they are generated, since they will be overwritten the next time you build.

For command-line details about xdc.tools.configuro, see the RTSC-pedia reference topic. Configuro can also be used to build the configuration file with other build systems. For more information, see the wiki page at http://rtsc.eclipseprojects.io/docs-tip/Consuming_Configurable_Content.

### 2.4.2 Rules for Working with CCS Project Properties

After you have created a CCS project that contains a configuration file, you can change the properties of the project in CCS by right-clicking the project name and choosing **Properties**.

In the **CCS General** category of the Properties dialog, the **General** tab applies to compiler settings, and the **RTSC** tab applies to the "configuro" utility used to process the .cfg file.

If there is any platform-specific configuration in your .cfg file, you must change those settings in addition to any platform-related changes you make to the **CCS General > RTSC** settings.

If your configuration file is stored in a separate project from the project that contains your source code files, you should be careful about changing the CCS General settings for a configuration-only project. The build settings for the configuration project must match or be compatible with those of all application projects that reference the configuration project. So, if you change the build settings for a configuration project, you should also change the build settings for the application projects that use that configuration.

### 2.4.3 Building an Application with GCC

The instructions in this section can be used to build SYS/BIOS applications on Windows or Linux. If you are using a Windows machine, you can use the regular DOS command shell provided with Windows. However, you may want to install a Unix-like shell, such as Cygwin.

For Windows users, the XDCtools top-level installation directory contains `gmake.exe`, which is used in the commands that follow to run the Makefile. The `gmake` utility is a Windows version of the standard GNU "make" utility provided with Linux.

If you are using Linux, change the "gmake" command to "make" in the commands that follow.

**Requirements**

- You must have SYS/BIOS and XDCtools installed on the system where you intend to build the application.
- You must have the GCC compiler system installed on the system where you intend to build the application. For example, you can use the Linaro Baremetal GNU Toolchain as a compiler for Cortex-A8/A9/A15 and Cortex-M3/M4/M4F targets (that is, targets that support GNU). See the documentation links on the web page at the link above for installation instructions.
- Your application must have a configuration file (*.cfg) that configures the application’s use of XDCtools and SYS/BIOS.
Limitations

Note: XDCtools provides linker scripts for most Stellaris and Tiva devices. If you are building for a device that is not yet supported, you will need to modify one of these script files.

Description

The command line tools can be invoked directly or (preferably) the build can be managed with makefiles.

When the SYS/BIOS configuration file is built, the output will include:

- `compiler.opt`, which contains a set of options for the C compiler to use when compiling user applications. The file contains a set of #include options and pre-processor #defines. The `compiler.opt` file can be provided to GCC via the `@` option.

- `linker.cmd`, which contains a set of linker options for use when linking applications. The file contains a list of libraries and object files produced from the configuration. It also specifies memory placement for memory used by SYS/BIOS. This file should be passed to the linker using the `-Wl,-T,` option.

Procedure

Follow these steps to build a SYS/BIOS application with the GCC compiler:

1. Download the sample package from the page for your device family linked to by the TI-RTOS Kernel Device Addendum (`BIOS_INSTALL_DIR/docs/Device_Addendum.html`). Uncompress the sample package to the location where you will build your applications. The file contains:
   - `hello.c`, `clock.c`, and `task.c`: Three simple C code files are included—a simple “hello world” application, one that uses the SYS/BIOS Clock module, and one that uses the Task and Semaphore modules.
   - `app.cfg`: A simple shared configuration file for these applications.
   - `tm4c123gh6pm.lds`: A linker script for a Tiva TM4C123GH6PM device.
   - `Makefile`: A makefile that can be used to build the applications from the command line.

2. Open the `Makefile` with a text editor. Edit the first three lines of the sample `Makefile` to specify the locations on your system for M4TOOLS (the GCC compiler location), SYSBIOS, and XDCTOOLS. For example:

   ```
   M4TOOLS ?= /home/myusername/linaro/gcc-arm-none-eabi-4_7-2013q3
   SYSBIOS ?= /home/myusername/bios_6_##_##_##
   XDCTOOLS ?= /home/myusername/xdctools_3_##_##_##
   ```

3. If you are building for a device other than a Tiva TM4C123GH6PM, first check the XDCTOOLS/packages/ti/platforms/tiva/include_gnu directory to see if a linker script file is provided for your device.

4. If no linker script file is provided for your device, make a copy of the closest provided file and edit the “MEMORY” region specifications to make them correspond to the memory map for your device. You can specify your new linker script file as the LINKERCMD file in the `Makefile`.

5. If you are using a linker script file other than `tm4c123gh6pm.lds`, edit the `Makefile` to change the LINKERCMD setting.
6. If you are using Windows and the gmake utility provided in the top-level directory of the XDCtools installation, you should add the `<xdc_install_dir>` to your PATH environment variable so that the gmake executable can be found.

7. Build the three sample applications by running the following command. (If you are running the build on Linux, change all "gmake" commands to "make".)

   ```
gmake
   ```

8. You can clean the build by running the following command:

   ```
gmake clean
   ```

9. Once built, applications can be loaded and debugged with CCS. Within CCS, you can use Tools > Runtime Object View to browse SYS/BIOS kernel object states and various other tools for real-time analysis. Alternatively, you can use GDB or another debugger for basic debugging, but the ROV is not available outside of CCS.

For more information, such as the compiler and linker options needed by SYS/BIOS for the Cortex-A and Cortex-M device families, see the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html). Additional information about building with various compilers may be found on the RTSC-pedia wiki.

### 2.4.4 Running and Debugging an Application in CCS

If you haven't already created a default target configuration, follow these steps:


2. Type a filename for the target configuration, which will be stored as part of the CCS project. For example, you might type TCI6482sim.ccxml if that is the target you want to use. Then, click Finish.

3. In the Connection field for your target configuration, choose the type of connection you have to the target. Then type part of the target name in the Device filter field. For example, you might choose the "TI Simulator" connection and filter by "64xp" to find a C64x+ simulator.

4. Choose File > Save or click the Save icon to save your target configuration.

5. You can right-click on a target configuration and choose Set as Default Target to set which target configuration is used for debugging.

To debug an application, follow these steps:

1. Choose Target > Debug Active Project or click the Debug icon. This loads the program and switches you to the "Debug" perspective.

2. You can set breakpoints in your code if desired. Press F8 to run.

3. Use various tools provided with XDCtools and SYS/BIOS to debug the application. See Section 9.4 for a more detailed comparison.
2.4.5 **Compiler and Linker Optimization**

You can optimize your application for better performance and code size or to give you more debugging information by selecting different ways of compiling and linking your application. For example, you can do this by linking with versions of the SYS/BIOS libraries that were compiled differently.

The choices you can make related to compiler and linker optimization are located in the following places:

- **Build-Profile.** You see this field when you are creating a new CCS project or modifying the CCS General settings. We recommend that you use the "release" setting. The "release" option is preferred even when you are creating and debugging an application; the "debug" option is mainly intended for internal use by Texas Instruments. The "release" option results in a somewhat smaller executable that can still be debugged. This build profile primarily affects how Codec Engine and some device drivers are built.

  Note: The "whole_program" and "whole_program_debug" options for the Build-Profile have been deprecated, and are no longer supported. The option that provides the most similar result is to set the BIOS.libType configuration parameter to BIOS.LibType_Custom.

- **Configuration.** The drop-down field at the top of the Properties dialog allows you to choose between and customize multiple build configurations. Each configuration can have the compiler and linker settings you choose. Debug and Release are the default configurations available.

- **BIOS.libType configuration parameter.** You can set this parameter in XGCONF or by editing the *.cfg file in your project. This parameter lets you select one of several custom versions of the SYS/BIOS libraries to be built based on the needs of your application. See the table and discussion that follow for more information.

The options for the BIOS.libType configuration parameter are as follows:

<table>
<thead>
<tr>
<th>BIOS.libType</th>
<th>Logging/Asserts</th>
<th>Code Size</th>
<th>Runtime Performance</th>
</tr>
</thead>
<tbody>
<tr>
<td>Instrumented</td>
<td>On</td>
<td>Good</td>
<td>Good</td>
</tr>
<tr>
<td>(BIOS.LibType_Instrumented)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Non-Instrumented</td>
<td>Off</td>
<td>Better</td>
<td>Better</td>
</tr>
<tr>
<td>(BIOS.LibType_NonInstrumented)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Custom (Optimized)</td>
<td>As configured</td>
<td>Best</td>
<td>Best</td>
</tr>
<tr>
<td>(BIOS.LibType_Custom)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Debug</td>
<td>As configured</td>
<td>Largest</td>
<td>No optimization</td>
</tr>
<tr>
<td>(BIOS.LibType_Debug)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

For all libType options, the executable that is created contains only the modules and APIs that your application needs to access. If you have not used a particular module in your *.cfg file or your C code (and it is not required internally by a SYS/BIOS module that is used), that module is not linked with your application. Individual API functions that are not needed (either directly or indirectly) are also excluded during the linking phase of the build.

- **Instrumented.** (default) This option results in a SYS/BIOS library being built with all Asserts and Logs enabled. Your configuration file can additionally enable or disable various Diags and logging-related settings. However, note that the checks to see if Diags are enabled before outputting a Log event are always performed, which has an impact on performance even if you use the ALWAYS_ON or ALWAYS_OFF setting. The resulting code size when using this option may be too large to fit on some targets, such as C28x and MSP430.
• **Non-Instrumented.** This option results in a SYS/BIOS library being built with all Asserts and Logs disabled. No Assert or Diag settings are checked, and logging information is not available at runtime. The checking for Asserts and Diags is compiled out of the libraries, so runtime performance and code size are optimized. Checking of Error Blocks and handling errors in ways other than logging an event are still supported.

• **Custom (Optimized).** This option results in a SYS/BIOS library being built as configured by the user. This option is optimized to provide the best runtime performance and code size given the needs of your application. Instrumentation is available to whatever extent your application configures it. This build preserves enough debug information to make it still possible to step through the optimized code in CCS and locate global variables.

• **Debug (Non-Optimized).** This option results in a SYS/BIOS library being built as configured by the user but with no optimizations enabled and full debug information embedded in the library. The resulting application is fully debuggable; you can step into the code performed by SYS/BIOS APIs. Since no optimization is performed, the code size is large and the runtime performance is slower than with the custom libType.

All libType options except the Debug option use aggressive program optimizations that remove many initialized constants and small code fragments (often "glue" code) from the final executable image. Such classic optimizations as constant folding and function inlining are used, including across module boundaries.

The first time you build a project, the build will be longer due to the SYS/BIOS library build process. The libraries are stored in the "src" directory of your project. Subsequent builds may be faster; libraries do not need to be rebuilt unless you change one of the few configuration parameters that affect the build settings or use an additional module that wasn’t already used in the previous configuration.

The following example statements set the BIOS.libType configuration parameter:

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.libType = BIOS.LibType_Custom;
```

If you use the BIOS.LibType_Custom or BIOS.LibType_Debug option for the BIOS.libType, you can also set the BIOS.customCCOpts parameter to customize the C compiler command-line options used when compiling the SYS/BIOS libraries. If you want to change this parameter, it is important to first examine and understand the default command-line options used to compile the SYS/BIOS libraries for your target. You can see the default in XGCONF or by placing the following statement in your configuration script and building the project:

```javascript
print("customCCOpts =", BIOS.customCCOpts);
```

You must be careful not to cause problems for the SYS/BIOS compilation when you modify the BIOS.customCCOpts parameter. For example, the --program_level_compile option is required. (Some --define and --include_path options are used on the compiler command line but are not listed in the customCCOpts definition; these also cannot be removed.)

For example, to create a debuggable custom library, you can remove the -o3 option from the BIOS.customCCOpts definition by specifying it with the following string for a C64x+ target:

```bash
BIOS.customCCOpts = "-mv64p --abi=eabi -q -mi10 -mo -pdr -pden -pds=238 -pds=880 -pds1110 --program_level_compile -g";
```

For more about configuring BIOS.libType and BIOS.customCCOpts, see the Documentation Overview (BIOS_INSTALL_DIR/docs/Documentation_Overview.html). See **Appendix A** for information about how to rebuild SYS/BIOS manually.
This chapter describes the types of threads a SYS/BIOS program can use.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.1 SYS/BIOS Startup Sequence</td>
<td>48</td>
</tr>
<tr>
<td>3.2 Overview of Threading Modules</td>
<td>49</td>
</tr>
<tr>
<td>3.3 Using SYS/BIOS on SMP Systems</td>
<td>58</td>
</tr>
<tr>
<td>3.4 Hardware Interrupts</td>
<td>58</td>
</tr>
<tr>
<td>3.5 Software Interrupts</td>
<td>66</td>
</tr>
<tr>
<td>3.6 Tasks</td>
<td>82</td>
</tr>
<tr>
<td>3.7 The Idle Loop</td>
<td>100</td>
</tr>
<tr>
<td>3.8 Example Using Hwi, Swi, and Task Threads</td>
<td>101</td>
</tr>
</tbody>
</table>
3.1 SYS/BIOS Startup Sequence

The SYS/BIOS startup sequence is logically divided into two phases—those operations that occur prior to the application's "main()" function being called and those operations that are performed after the application's "main()" function is invoked. Control points are provided at various places in each of the two startup sequences for user startup functions to be inserted.

The "before main()" startup sequence is governed completely by the XDCtools runtime package. For more information about the boot sequence prior to main, refer to the "XDCtools Boot Sequence and Control Points" wiki page. The XDCtools runtime startup sequence is as follows:

1. Immediately after CPU reset, perform target/device-specific CPU initialization (beginning at c_int00). See the "Program Loading and Running" chapter in the Assembly Language Tools User’s Guide for your target family for details on this step and the cinit() step.
2. Prior to cinit(), run the table of "reset functions" (the xdc.runtime.Reset module provides this hook). The functions specified in the Reset.fxns[] array are called. These reset functions are called only on platforms where a reset is performed before running a program.
3. Run cinit() to initialize C runtime environment.
4. Run the user-supplied "first functions" (the xdc.runtime.Startup module provides this hook).
5. Run all the module initialization functions.
6. Run the user-supplied "last functions" (the xdc.runtime.Startup module provides this hook).
7. Run pinit().
8. Run main().

The "after main()" startup sequence is governed by SYS/BIOS and is initiated by an explicit call to the BIOS_start() function at the end of the application’s main() function. The SYS/BIOS startup sequence that runs when BIOS_start() is called is as follows:

1. **Startup Functions.** Run the user-supplied "startup functions" (see BIOS.startupFxns). If the system supports Timers, all statically created timers are initialized at this point using their static configuration. If a timer was configured to start "automatically," it is started here.
2. **Enable Hardware Interrupts.**
3. **Enable Software Interrupts.** If the system supports software interrupts (Swis) (see BIOS.swiEnabled), then the SYS/BIOS startup sequence enables Swis at this point.
4. **Task Startup.** If the system supports Tasks (see BIOS.taskEnabled), then task scheduling begins here. If there are no statically or dynamically created Tasks in the system, then execution proceeds directly to the idle loop.
The following configuration script excerpt installs a user-supplied startup function at every possible control point in the startup sequence. Configuration scripts have a file extension ".cfg" and used to configure modules and objects.

```javascript
/* get handle to xdc Reset module */
Reset = xdc.useModule('xdc.runtime.Reset');

/* install a "reset function" */
Reset.fxns[Reset.fxns.length++] = '&myReset';

/* get handle to xdc Startup module */
var Startup = xdc.useModule('xdc.runtime.Startup');

/* install a "first function" */
Startup.firstFxns[Startup.firstFxns.length++] = '&myFirst';

/* install a "last function" */
Startup.lastFxns[Startup.lastFxns.length++] = '&myLast';

/* get handle to BIOS module */
var BIOS = xdc.useModule('ti.sysbios.BIOS');

/* install a BIOS startup function */
BIOS addUserStartupFunction('&myBiosStartup');
```

### 3.2 Overview of Threading Modules

Many real-time applications must perform a number of seemingly unrelated functions at the same time, often in response to external events such as the availability of data or the presence of a control signal. Both the functions performed and when they are performed are important.

These functions are called threads. Different systems define threads either narrowly or broadly. Within SYS/BIOS, the term is defined broadly to include any independent stream of instructions executed by the processor. A thread is a single point of control that can activate a function call or an interrupt service routine (ISR).

SYS/BIOS enables your applications to be structured as a collection of threads, each of which carries out a modularized function. Multithreaded programs run on a single processor by allowing higher-priority threads to preempt lower-priority threads and by allowing various types of interaction between threads, including blocking, communication, and synchronization.

Real-time application programs organized in such a modular fashion—as opposed to a single, centralized polling loop, for example—are easier to design, implement, and maintain.

SYS/BIOS provides support for several types of program threads with different priorities. Each thread type has different execution and preemption characteristics. The thread types (from highest to lowest priority) are:

- **Hardware interrupts (Hwi)**, which includes Timer functions
- **Software interrupts (Swi)**, which includes Clock functions
- **Tasks** (Task)
- **Background thread** (Idle)
These thread types are described briefly in the following section and discussed in more detail in the rest of this chapter.

### 3.2.1 Types of Threads

The four major types of threads in a SYS/BIOS program are:

- **Hardware interrupt (Hwi) threads.** Hwi threads (also called Interrupt Service Routines or ISRs) are the threads with the highest priority in a SYS/BIOS application. Hwi threads are used to perform time critical tasks that are subject to hard deadlines. They are triggered in response to external asynchronous events (interrupts) that occur in the real-time environment. Hwi threads always run to completion but can be preempted temporarily by Hwi threads triggered by other interrupts, if enabled. See Section 3.4, *Hardware Interrupts*, page 58, for details about hardware interrupts.

- **Software interrupt (Swi) threads.** Patterned after hardware interrupts (Hwi), software interrupt threads provide additional priority levels between Hwi threads and Task threads. Unlike Hwis, which are triggered by hardware interrupts, Swis are triggered programmatically by calling certain Swi module APIs. Swis handle threads subject to time constraints that preclude them from being run as tasks, but whose deadlines are not as severe as those of hardware ISRs. Like Hwi's, Swi's threads always run to completion. Swis allow Hwis to defer less critical processing to a lower-priority thread, minimizing the time the CPU spends inside an interrupt service routine, where other Hwis can be disabled. Swis require only enough space to save the context for each Swi interrupt priority level, while Tasks use a separate stack for each thread. See Section 3.5, *Software Interrupts*, page 66, for details about Swis.

- **Task (Task) threads.** Task threads have higher priority than the background (Idle) thread and lower priority than software interrupts. Tasks differ from software interrupts in that they can wait (block) during execution until necessary resources are available. Tasks require a separate stack for each thread. SYS/BIOS provides a number of mechanisms that can be used for inter-task communication and synchronization. These include Semaphores, Events, Message queues, and Mailboxes. See Section 3.6, *Tasks*, page 82, for details about tasks.

- **Idle Loop (Idle) thread.** Idle threads execute at the lowest priority in a SYS/BIOS application and are executed one after another in a continuous loop (the Idle Loop). After main returns, a SYS/BIOS application calls the startup routine for each SYS/BIOS module and then falls into the Idle Loop. Each thread must wait for all others to finish executing before it is called again. The Idle Loop runs continuously except when it is preempted by higher-priority threads. Only functions that do not have hard deadlines should be executed in the Idle Loop. See Section 3.7, *The Idle Loop*, page 100, for details about the background thread.

Another type of thread, a Clock thread, is run within the context of a Swi thread that is triggered by a Hwi thread invoked by a repetitive timer peripheral interrupt. See Section 5.2 for details.

---

**Note:** SYS/BIOS also provides a subset of the POSIX thread (pthread) APIs. These include pthread threads, mutexes, read-write locks, barriers, and condition variables. The pthread APIs can simplify porting applications from a POSIX environment to SYS/BIOS, as well as allowing code to be compiled to run in both a POSIX environment and with SYS/BIOS. As the pthread APIs are built on top of the SYS/BIOS Task and Semaphore modules, some POSIX APIs can be called from SYS/BIOS Tasks.

For details about supported POSIX thread APIs, see the *TI-POSIX User’s Guide* in the /docs/tiposix/Users_Guide.html file in the TI-RTOS Kernel installation. For more, see the official POSIX specification and the generic POSIX implementation.
3.2.2 Choosing Which Types of Threads to Use

The type and priority level you choose for each thread in an application program has an impact on whether the threads are scheduled on time and executed correctly. SYS/BIOS static configuration makes it easy to change a thread from one type to another.

A program can use multiple types of threads. Here are some rules for deciding which type of object to use for each thread to be performed by a program.

- **Swi or Task versus Hwi.** Perform only critical processing within hardware interrupt service routines. Hwis should be considered for processing hardware interrupts (IRQs) with deadlines down to the 5-microsecond range, especially when data may be overwritten if the deadline is not met. Swis or Tasks should be considered for events with longer deadlines—around 100 microseconds or more. Your Hwi functions should post Swis or tasks to perform lower-priority processing. Using lower-priority threads minimizes the length of time interrupts are disabled (interrupt latency), allowing other hardware interrupts to occur.

- **Swi versus Task.** Use Swis if functions have relatively simple interdependencies and data sharing requirements. Use tasks if the requirements are more complex. While higher-priority threads can preempt lower priority threads, only tasks can wait for another event, such as resource availability. Tasks also have more options than Swis when using shared data. All input needed by a Swi’s function should be ready when the program posts the Swi. The Swi object’s trigger structure provides a way to determine when resources are available. Swis are more memory-efficient because they all run from a single stack.

- **Idle.** Create Idle threads to perform noncritical housekeeping tasks when no other processing is necessary. Idle threads typically have no hard deadlines. Instead, they run when the system has unused processor time. Idle threads run sequentially at the same priority. You may use Idle threads to reduce power needs when other processing is not being performed. In this case, you should not depend upon housekeeping tasks to occur during power reduction times.

- **Clock.** Use Clock functions when you want a function to run at a rate based on a multiple of the interrupt rate of the peripheral that is driving the Clock tick. Clock functions can be configured to execute either periodically or just once. These functions run as Swi functions.

- **Clock versus Swi.** All Clock functions run at the same Swi priority, so one Clock function cannot preempt another. However, Clock functions can post lower-priority Swi threads for lengthy processing. This ensures that the Clock Swi can preempt those functions when the next system tick occurs and when the Clock Swi is posted again.

- **Timer.** Timer threads are run within the context of a Hwi thread. As such, they inherit the priority of the corresponding Timer interrupt. They are invoked at the rate of the programmed Timer period. Timer threads should do the absolute minimum necessary to complete the task required. If more processing time is required, consider posting a Swi to do the work or posting a Semaphore for later processing by a task so that CPU time is efficiently managed.
### 3.2.3 A Comparison of Thread Characteristics

Table 3-1 provides a comparison of the thread types supported by SYS/BIOS.

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Hwi</th>
<th>Swi</th>
<th>Task</th>
<th>Idle</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Priority</strong></td>
<td>Highest</td>
<td>2nd highest</td>
<td>2nd lowest</td>
<td>Lowest</td>
</tr>
<tr>
<td><strong>Number of priority levels</strong></td>
<td>family/device-specific</td>
<td>Up to 32 (16 for MSP430 and C28x). Periodic functions run at the priority of the Clock Swi.</td>
<td>Up to 32 (16 for MSP430 and C28x). This includes 1 for the Idle Loop.</td>
<td>1</td>
</tr>
<tr>
<td><strong>Can yield and pend</strong></td>
<td>No, runs to completion except for preemption</td>
<td>No, runs to completion except for preemption</td>
<td>Yes</td>
<td>Should not pend. Pending would disable all registered Idle threads.</td>
</tr>
<tr>
<td><strong>Execution states</strong></td>
<td>Inactive, ready, running</td>
<td>Inactive, ready, running</td>
<td>Ready, running, blocked, terminated</td>
<td>Ready, running</td>
</tr>
<tr>
<td><strong>Thread scheduler disabled by</strong></td>
<td>Hwi_disable()</td>
<td>Swi_disable()</td>
<td>Task_disable()</td>
<td>Program exit</td>
</tr>
<tr>
<td><strong>Posted or made ready to run by</strong></td>
<td>Interrupt occurs</td>
<td>Swi_post(), Swi_andn(), Swi_dec(), Swi_inc(), Swi_or()</td>
<td>Task_create() and various task synchronization mechanisms (Event, Semaphore, Mailbox)</td>
<td>main() exits and no other thread is currently running</td>
</tr>
<tr>
<td><strong>Stack used</strong></td>
<td>System stack (1 per program)</td>
<td>System stack (1 per program)</td>
<td>Task stack (1 per task)</td>
<td>Task stack used by default (see Note 1)</td>
</tr>
<tr>
<td><strong>Context saved when preempts other thread</strong></td>
<td>Entire context minus saved-by-callee registers (as defined by the TI C compiler) are saved to system.</td>
<td>Certain registers saved to system.</td>
<td>Entire context saved to task stack</td>
<td>--Not applicable--</td>
</tr>
<tr>
<td><strong>Context saved when blocked</strong></td>
<td>--Not applicable--</td>
<td>--Not applicable--</td>
<td>Saves the saved-by-callee registers (see optimizing compiler user's guide for your platform).</td>
<td>--Not applicable--</td>
</tr>
<tr>
<td><strong>Share data with thread via</strong></td>
<td>Streams, lists, pipes, global variables</td>
<td>Streams, lists, pipes, global variables</td>
<td>Streams, lists, pipes, gates, mailboxes, message queues, global variables</td>
<td>Streams, lists, pipes, global variables</td>
</tr>
<tr>
<td><strong>Synchronize with thread via</strong></td>
<td>--Not applicable--</td>
<td>Swi trigger</td>
<td>Semaphores, events, mailboxes</td>
<td>--Not applicable--</td>
</tr>
</tbody>
</table>
3.2.4 Thread Priorities

Within SYS/BIOS, hardware interrupts have the highest priority. The priorities among the set of Hwi objects are not maintained implicitly by SYS/BIOS. The Hwi priority only applies to the order in which multiple interrupts that are ready on a given CPU cycle are serviced by the CPU. Hardware interrupts are preempted by another interrupt unless interrupts are globally disabled or when specific interrupts are individually disabled.

Swis have lower priority than Hwis. There are up to 32 priority levels available for Swis (16 by default). The maximum number of priority levels is 16 for MSP430 and C28x. Swis can be preempted by a higher-priority Swi or any Hwi. Swis cannot block.
Tasks have lower priority than Swis. There are up to 32 task priority levels (16 by default). The maximum number of priority levels is 16 for MSP430 and C28x. Tasks can be preempted by any higher-priority thread. Tasks can block while waiting for resource availability and lower-priority threads.

For Swis and Tasks, higher numbers equal higher priorities. That is, zero is the lowest priority level within the set of Swis and within the set of Tasks.

The background Idle Loop is the thread with the lowest priority of all. It runs in a loop when the CPU is not busy running another thread. When tasks are enabled, the Idle Loop is implemented as the only task running at priority 0. When tasks are disabled, the Idle Loop is fallen into after the application's "main()" function is called.

### 3.2.5 Yielding and Preemption

The SYS/BIOS thread schedulers run the highest-priority (highest priority number) thread that is ready to run except in the following cases:

- The thread that is running disables some or all hardware interrupts temporarily with Hwi_disable() or Hwi_disableInterrupt(), preventing hardware ISRs from running.
- The thread that is running disables Swis temporarily with Swi_disable(). This prevents any higher-priority Swi from preempting the current thread. It does not prevent Hwis from preempting the current thread.
- The thread that is running disables task scheduling temporarily with Task_disable(). This prevents any higher-priority task from preempting the current task. It does not prevent Hwis and Swis from preempting the current task.
- If a lower priority task shares a gating resource with a higher task and changes its state to pending, the higher priority task may effectively have its priority set to that of the lower priority task. This is called Priority Inversion and is described in Section 4.3.3.

Both Hwis and Swis can interact with the SYS/BIOS task scheduler. When a task is blocked, it is often because the task is pending on a semaphore which is unavailable. Semaphores can be posted from Hwis and Swis as well as from other tasks. If a Hwi or Swi posts a semaphore to unblock a pending task, the processor switches to that task if that task has a higher priority than the currently running task (after the Hwi or Swi completes).

When running either a Hwi or Swi, SYS/BIOS uses a dedicated system interrupt stack, called the system stack (sometimes called the ISR stack). Each task uses its own private stack. Therefore, if there are no Tasks in the system, all threads share the same system stack. For performance reasons, sometimes it is advantageous to place the system stack in precious fast memory. See Section 3.5.3 for information about system stack size and Section 3.6.3 for information about task stack size.

Table 3-2 shows what happens when one type of thread is running (top row) and another thread becomes ready to run (left column). The action shown is that of the newly posted (ready to run) thread.
Table 3-2. Thread Preemption

<table>
<thead>
<tr>
<th>Newly Posted Thread</th>
<th>Running Thread</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Hwi</td>
</tr>
<tr>
<td>Enabled Hwi</td>
<td>Preempts if enabled*</td>
</tr>
<tr>
<td>Disabled Hwi</td>
<td>Waits for reenable</td>
</tr>
<tr>
<td>Enabled, higher-priority Swi</td>
<td>Waits</td>
</tr>
<tr>
<td>Lower-priority Swi</td>
<td>Waits</td>
</tr>
<tr>
<td>Enabled, higher-priority Task</td>
<td>Waits</td>
</tr>
<tr>
<td>Low-priority Task</td>
<td>Waits</td>
</tr>
</tbody>
</table>

* On some targets, hardware interrupts can be individually enabled and disabled. This is not true on all targets. Also, some targets have controllers that support hardware interrupt prioritization, in which case a Hwi can only be preempted by a higher-priority Hwi.

Note that Table 3-2 shows the results if the type of thread that is posted is enabled. If that thread type is disabled (for example, by Task_disable), a thread cannot run case until its thread type is reenabled.

Figure 3-2 shows the execution graph for a scenario in which Swis and Hwis are enabled (the default), and a Hwi posts a Swi whose priority is higher than that of the Swi running when the interrupt occurs. Also, a second Hwi occurs while the first ISR is running and preempts the first ISR.

Figure 3-2. Preemption Scenario

[Diagram showing preemption scenario]
In Figure 3-2, the low-priority Swi is asynchronously preempted by the Hwis. The first Hwi posts a higher-priority Swi, which is executed after both Hwis finish executing.

Here is sample pseudo-code for the example depicted in Figure 3-2:

```c
// backgroundThread()
{
    Swi_post(Swi_B) /* priority = 5 */
}

Hwi_1 ()
{
    ...
}
Hwi_2 ()
{
    Swi_post(Swi_A) /* priority = 7 */
}
```

### 3.2.6 Hooks

Hwi, Swi, and Task threads optionally provide points in a thread’s life cycle to insert user code for instrumentation, monitoring, or statistics gathering purposes. Each of these code points is called a "hook" and the user function provided for the hook is called a "hook function".

The following hook functions can be set for the various thread types:

#### Table 3-3. Hook Functions by Thread Type

<table>
<thead>
<tr>
<th>Thread Type</th>
<th>Hook Functions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hwi</td>
<td>Register, Create, Begin, End, and Delete. See Section 3.4.3.</td>
</tr>
<tr>
<td>Swi</td>
<td>Register, Create, Ready, Begin, End, and Delete. See Section 3.5.8.</td>
</tr>
<tr>
<td>Task</td>
<td>Register, Create, Ready, Switch, Exit, and Delete. See Section 3.6.5.</td>
</tr>
</tbody>
</table>

Hooks are declared as a set of hook functions called "hook sets". You do not need to define all hook functions within a set, only those that are required by the application.

Hook functions can only be declared statically (in a configuration script) so that they may be efficiently invoked when provided and result in no runtime overhead when a hook function is not provided.

Except for the Register hook, all hook functions are invoked with a handle to the object associated with that thread as its argument (that is, a Hwi object, a Swi object, or a Task object). Other arguments are provided for some thread-type-specific hook functions.

You can define as many hook sets as necessary for your application. When more than one hook set is defined, the individual hook functions within each set are invoked in hook ID order for a particular hook type. For example, during Task_create() the order that the Create hook within each Task hook set is invoked is the order in which the Task hook sets were originally defined.

The argument to a thread’s Register hook (which is invoked only once) is an index (the "hook ID") indicating the hook set's relative order in the hook function calling sequence.
Each set of hook functions has a unique associated "hook context pointer". This general-purpose pointer can be used by itself to hold hook set specific information, or it can be initialized to point to a block of memory allocated by the Create hook function within a hook set if more space is required for a particular application.

An individual hook function obtains the value of its associated context pointer through the following thread-type-specific APIs: Hwi_getHookContext(), Swi_getHookContext(), and Task_getHookContext(). Corresponding APIs for initializing the context pointers are also provided: Hwi_setHookContext(), Swi_setHookContext(), and Task_setHookContext(). Each of these APIs take the hook ID as an argument.

The following diagram shows an application with three Hwi hook sets:

```
registerHookFunc0()       registerHookFunc1()       registerHookFunc2()
createHookFunc0()         createHookFunc1()         createHookFunc2()
beginHookFunc0()          beginHookFunc1()          beginHookFunc2()
endHookFunc0()            endHookFunc1()            endHookFunc2()
deleteHookFunc0()         deleteHookFunc1()         deleteHookFunc2()
```

The hook context pointers are accessed using Hwi_getHookContext() using the index provided to the three Register hook functions.

Just prior to invoking your ISR functions, the Begin Hook functions are invoked in the following order:

1. beginHookFunc0();
2. beginHookFunc1();
3. beginHookFunc2();

Likewise, upon return from your ISR functions the End Hook functions are invoked in the following order:

1. endHookFunc0();
2. endHookFunc1();
3. endHookFunc2();
3.3 Using SYS/BIOS on SMP Systems

SYS/BIOS can be used on Symmetric Multiprocessing (SMP) systems, such as dual-core ARM Cortex-M3/M4 and multi-core ARM Cortex-A15 sub-systems present on several TI SoC devices. Such systems are composed of two or more identical processor cores that share a common view of memory and peripherals. SMP/BIOS is an operational mode of SYS/BIOS that supports such systems.

APIs and configuration parameters in the BIOS, Core, Task, Idle, and Hwi modules let you control the multi-core behavior of SMP/BIOS.

3.4 Hardware Interrupts

Hardware interrupts (Hwis) handle critical processing that the application must perform in response to external asynchronous events. The SYS/BIOS target/device specific Hwi modules are used to manage hardware interrupts.

In a typical embedded system, hardware interrupts are triggered either by on-device peripherals or by devices external to the processor. In both cases, the interrupt causes the processor to vector to the ISR address.

Any interrupt processing that may invoke SYS/BIOS APIs that affect Swi and Task scheduling must be written in C or C++. The HWI_enter()/HWI_exit() macros provided in earlier versions of SYS/BIOS for calling assembly language ISRs are no longer provided.

Assembly language ISRs that do not interact with SYS/BIOS can be specified with Hwi_plug(). Such ISRs must do their own context preservation. They may use the "interrupt" keyword, C functions, or assembly language functions.

All hardware interrupts run to completion. If a Hwi is posted multiple times before its ISR has a chance to run, the ISR runs only one time. For this reason, you should minimize the amount of code performed by a Hwi function.

If interrupts are globally enabled—that is, by calling Hwi_enable()—an ISR can be preempted by any interrupt that has been enabled.

Hwis must not use the Chip Support Library (CSL) for the target. Instead, see Chapter 8 for a description of Hardware Abstraction Layer APIs.

Associating an ISR function with a particular interrupt is done by creating a Hwi object.
3.4.1 Creating Hwi Objects

The Hwi module maintains a table of pointers to Hwi objects that contain information about each Hwi managed by the dispatcher (or by generated interrupt stubs on platforms for which the Hwi dispatcher is not provided, such as the MSP430). To create a Hwi object dynamically, use calls similar to these:

```c
Hwi_Handle hwi0;
Hwi_Params hwiParams;
Error_Block eb;

... 

Error_init(&eb);
Hwi_Params_init(&hwiParams);

hwiParams.arg = 5;
hwi0 = Hwi_create(id, hwiFunc, &hwiParams, &eb);
if (hwi0 == NULL) {
    System_abort("Hwi create failed");
}
```

Here, hwi0 is a handle to the created Hwi object, id is the interrupt number being defined, hwiFunc is the name of the function associated with the Hwi, and hwiParams is a structure that contains Hwi instance parameters (enable/restore masks, the Hwi function argument, etc). Here, hwiParams.arg is set to 5. If NULL is passed instead of a pointer to an actual Hwi_Params struct, a default set of parameters is used. The "eb" is an error block that you can use to handle errors that may occur during Hwi object creation.

The corresponding static configuration Hwi object creation syntax is:

```javascript
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');
var hwiParams = new Hwi.Params;

hwiParams.arg = 5;
Program.global.hwi0 = Hwi.create(id, hwiFunc, hwiParams);
```

Here, the "hwiParams = new Hwi.Params" statement does the equivalent of creating and initializing the hwiParams structure with default values. In the static configuration world, no error block (eb) is required for the "create" function. The "Program.global.hwi0" name becomes a a runtime-accessible handle (symbol name = "hwi0") to the statically-created Hwi object.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

3.4.2 Hardware Interrupt Nesting and System Stack Size

When a Hwi runs, its function is invoked using the system stack. In the worst case, each Hwi can result in a nesting of the scheduling function (that is, the lowest priority Hwi is preempted by the next highest priority Hwi, which, in turn, is preempted by the next highest, ...). This results in an increasing stack size requirement for each Hwi priority level actually used.

The default system stack size is 4096 bytes. You can set the system stack size by adding the following line to your config script:

```c
Program.stack = yourStackSize;
```
The following table shows the amount of system stack required to absorb the worst-case Hwi interrupt nesting. This first number is the amount of system stack space required for the first priority level on a target. The second number shows the amount of stack space required for each subsequent priority level used in the application.

### Table 3–4. System Stack Use for Hwi Nesting by Target Family

<table>
<thead>
<tr>
<th>Target Family</th>
<th>Stack Consumed by First Hwi</th>
<th>Stack Consumed by Subsequent Nested Hwis</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>M3</td>
<td>176</td>
<td>80</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430</td>
<td>36</td>
<td>26</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430X</td>
<td>38</td>
<td>46</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430X_small</td>
<td>36</td>
<td>26</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C674</td>
<td>68</td>
<td>384</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C64P</td>
<td>68</td>
<td>384</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C64T</td>
<td>68</td>
<td>208</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C28_float</td>
<td>65</td>
<td>60</td>
<td>16-bit words</td>
</tr>
<tr>
<td>C28_large</td>
<td>65</td>
<td>46</td>
<td>16-bit words</td>
</tr>
<tr>
<td>Arm9</td>
<td>136</td>
<td>80</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>A8F</td>
<td>136</td>
<td>144</td>
<td>8-bit bytes</td>
</tr>
</tbody>
</table>

See Section 3.5.3 for information about system stack use by software interrupts and Section 3.6.3 for information about task stack size.

### 3.4.3 Hwi Hooks

The Hwi module supports the following set of Hook functions:

- **Register.** A function called before any statically created Hwis are initialized at runtime. The register hook is called at boot time before main() and before interrupts are enabled.
- **Create.** A function called when a Hwi is created. This includes Hwis that are created statically and those created dynamically using Hwi_create().
- **Begin.** A function called just prior to running a Hwi ISR function.
- **End.** A function called just after a Hwi ISR function finishes.
- **Delete.** A function called when a Hwi is deleted at runtime with Hwi_delete().
The following HookSet structure type definition encapsulates the hook functions supported by the Hwi module:

```c
typedef struct Hwi_HookSet {
    Void (*registerFxn)(Int);                 /* Register Hook */
    Void (*createFxn)(Handle, Error.Block *); /* Create Hook */
    Void (*beginFxn)(Handle);                 /* Begin Hook */
    Void (*endFxn)(Handle);                   /* End Hook */
    Void (*deleteFxn)(Handle);                /* Delete Hook */
};
```

Hwi Hook functions can only be configured statically.

### 3.4.3.1 Register Function

The register function is provided to allow a hook set to store its corresponding hook ID. This ID can be passed to `Hwi_setHookContext()` and `Hwi_getHookContext()` to set or get hook-specific context. The Register function must be specified if the hook implementation needs to use `Hwi_setHookContext()` or `Hwi_getHookContext()`.

The registerFxn hook function is called during system initialization before interrupts have been enabled.

The Register function has the following signature:

```c
Void registerFxn(Int id);
```

### 3.4.3.2 Create and Delete Functions

The Create and Delete functions are called whenever a Hwi is created or deleted. The Create function is passed an Error_Block that is to be passed to `Memory_alloc()` for applications that require additional context storage space.

The createFxn and deleteFxn functions are called with interrupts enabled (unless called at boot time or from `main()`).

These functions have the following signatures:

```c
Void createFxn(Hwi_Handle hwi, Error_Block *eb);
Void deleteFxn(Hwi_Handle hwi);
```

### 3.4.3.3 Begin and End Functions

The Begin and End hook functions are called with interrupts globally disabled. As a result, any hook processing function contributes to overall system interrupt response latency. In order to minimize this impact, carefully consider the processing time spent in a Hwi beginFxn or endFxn hook function.

The beginFxn is invoked just prior to calling the ISR function. The endFxn is invoked immediately after the return from the ISR function.

These functions have the following signatures:

```c
Void beginFxn(Hwi_Handle hwi);
Void endFxn(Hwi_Handle hwi);
```

When more than one Hook Set is defined, the individual hook functions of a common type are invoked in hook ID order.
### Hwi Hooks Example

The following example application uses two Hwi hook sets. The Hwi associated with a statically-created Timer is used to exercise the Hwi hook functions. This example demonstrates how to read and write the Hook Context Pointer associated with each hook set.

The configuration script and program output are shown after the C code listing.

This is the C code for the example:

```c
/* ======== HwiHookExample.c ========
 * This example demonstrates basic Hwi usage. */
#include <xdc/std.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Timestamp.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/hal/Timer.h>
#include <ti/sysbios/hal/Hwi.h>

extern Timer_Handle myTimer;
volatile Bool myEnd2Flag = FALSE;
Int myHookSetId1, myHookSetId2;

/* HookSet 1 functions */

/* ======== myRegister1 ========
 * invoked during Hwi module startup before main()
 * for each HookSet */
Void myRegister1(Int hookSetId)
{
    System_printf("myRegister1: assigned hookSet Id = %d\n", hookSetId);
    myHookSetId1 = hookSetId;
}

/* ======== myCreate1 ========
 * invoked during Hwi module startup before main()
 * for statically created Hwis */
Void myCreate1(Hwi_Handle hwi, Error_Block *eb)
{
    Ptr pEnv;
    pEnv = Hwi_getHookContext(hwi, myHookSetId1);
    System_printf("myCreate1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId1, (Ptr)0xdead1);
}
```
/* ======== myBegin1 ========
* invoked before Timer Hwi func */
Void myBegin1(Hwi_Handle hwi)
{
    Ptr pEnv;

    pEnv = Hwi_getHookContext(hwi, myHookSetId1);
    System_printf("myBegin1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId1, (Ptr)0xbeef1);
}

/* ======== myEnd1 ========
* invoked after Timer Hwi func */
Void myEnd1(Hwi_Handle hwi)
{
    Ptr pEnv;

    pEnv = Hwi_getHookContext(hwi, myHookSetId1);
    System_printf("myEnd1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId1, (Ptr)0xc0de1);
}

/* HookSet 2 functions */
/* ======== myRegister2 ========
* invoked during Hwi module startup before main
* for each HookSet */
Void myRegister2(Int hookSetId)
{
    System_printf("myRegister2: assigned hookSet Id = %d\n", hookSetId);
    myHookSetId2 = hookSetId;
}

/* ======== myCreate2 ========
* invoked during Hwi module startup before main
* for statically created Hwis */
Void myCreate2(Hwi_Handle hwi, Error_Block *eb)
{
    Ptr pEnv;

    pEnv = Hwi_getHookContext(hwi, myHookSetId2);
    /* pEnv should be 0 at this point. If not, there's a bug. */
    System_printf("myCreate2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId2, (Ptr)0xdead2);
}
/* ======== myBegin2 ========
 * invoked before Timer Hwi func */
Void myBegin2(Hwi_Handle hwi)
{
    Ptr pEnv;

    pEnv = Hwi_getHookContext(hwi, myHookSetId2);
    System_printf("myBegin2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId2, (Ptr)0xbeef2);
}

/* ======== myEnd2 ========
 * invoked after Timer Hwi func */
Void myEnd2(Hwi_Handle hwi)
{
    Ptr pEnv;

    pEnv = Hwi_getHookContext(hwi, myHookSetId2);
    System_printf("myEnd2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Hwi_setHookContext(hwi, myHookSetId2, (Ptr)0xc0de2);
    myEnd2Flag = TRUE;
}

/* ======== myTimerFunc ========
 * Timer interrupt handler */
Void myTimerFunc(UArg arg)
{
    System_printf("Entering myTimerHwi\n");
}

/* ======== myTaskFunc ======== */
Void myTaskFunc(UArg arg0, UArg arg1)
{
    System_printf("Entering myTask.\n");
    Timer_start(myTimer);
    /* wait for timer interrupt and myEnd2 to complete */
    while (!myEnd2Flag) {
        ;
    }
    System_printf("myTask exiting ...\n");
}
This is the configuration script for the example:

```c
/* pull in Timestamp to print time in hook functions */
xdc.useModule('xdc.runtime.Timestamp');

/* Disable Clock so that ours is the only Timer allocated */
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.clockEnabled = false;

var Idle = xdc.useModule('ti.sysbios.knl.Idle');
Idle.addFunc('&myIdleFunc');

/* Create myTask with default task params */
var Task = xdc.useModule('ti.sysbios.knl.Task');
var taskParams = new Task.Params();
Program.global.myTask = Task.create('&myTaskFunc', taskParams);

/* Create myTimer as source of Hwi */
var Timer = xdc.useModule('ti.sysbios.hal.Timer');
var timerParams = new Timer.Params();
timerParams.startMode = Timer.StartMode_USER;
timerParams.runMode = Timer.RunMode_ONESHOT;
timerParams.period = 1000; // 1ms
Program.global.myTimer = Timer.create(Timer.ANY, "&myTimerFunc", timerParams);

/* Define and add two Hwi HookSets
 * Notice, no deleteFxn is provided.
 */
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');
```
3.5 Software Interrupts

Software interrupts are patterned after hardware ISRs. The Swi module in SYS/BIOS provides a software interrupt capability. Software interrupts are triggered programmatically, through a call to a SYS/BIOS API such as Swi_post(). Software interrupts have priorities that are higher than tasks but lower than hardware interrupts. See the video introducing Swis for an overview.

Note: The Swi module should not be confused with the SWI instruction that exists on many processors. The SYS/BIOS Swi module is independent from any target/device-specific software interrupt features.

Swi threads are suitable for handling application tasks that occur at slower rates or are subject to less severe real-time deadlines than those of Hwis.
The SYS/BIOS APIs that can trigger or post a Swi are:

- `Swi_andn()`
- `Swi_dec()`
- `Swi_inc()`
- `Swi_or()`
- `Swi_post()`

The Swi manager controls the execution of all Swi functions. When the application calls one of the APIs above, the Swi manager schedules the function corresponding to the specified Swi for execution. To handle Swi functions, the Swi manager uses Swi objects.

If a Swi is posted, it runs only after all pending Hwis have run. A Swi function in progress can be preempted at any time by a Hwi; the Hwi completes before the Swi function resumes. On the other hand, Swi functions always preempt tasks. All pending Swis run before even the highest priority task is allowed to run. In effect, a Swi is like a task with a priority higher than all ordinary tasks.

---

**Note:**

Two things to remember about Swi functions are:

- A Swi function runs to completion unless it is interrupted by a Hwi or preempted by a higher-priority Swi.
- Any hardware ISR that triggers or posts a Swi must have been invoked by the Hwi dispatcher (or by generated interrupt stubs on platforms for which the Hwi dispatcher is not provided, such as the MSP430). That is, the Swi must be triggered by a function called from a Hwi object.

---

### 3.5.1 Creating Swi Objects

As with many other SYS/BIOS objects, you can create Swi objects either dynamically—with a call to `Swi_create()`—or statically in the configuration. Swis you create dynamically can also be deleted during program execution.

To add a new Swi to the configuration, create a new Swi object in the configuration script. Set the function property for each Swi to run a function when the object is triggered by the application. You can also configure up to two arguments to be passed to each Swi function.

As with all modules with instances, you can determine from which memory segment Swi objects are allocated. Swi objects are accessed by the Swi manager when Swis are posted and scheduled for execution.

For complete reference information on the Swi API, configuration, and objects, see the Swi module in the "ti.sysbios.knl" package documentation in the online documentation. (For information on running online help, see Section 1.6.1, *Using the API Reference Help System*, page 25.)
To create a Swi object dynamically, use a call with this syntax:

```c
Swi_Handle swi0;
Swi_Params swiParams;
Error_Block eb;

...

Error_init(&eb);
Swi_Params_init(&swiParams);

swi0 = Swi_create(swiFunc, &swiParams, &eb);
if (swi0 == NULL) {
    System_abort("Swi create failed");
}
```

Here, swi0 is a handle to the created Swi object, swiFunc is the name of the function associated with the Swi, and swiParams is a structure of type Swi_Params that contains the Swi instance parameters (priority, arg0, arg1, etc). If NULL is passed instead of a pointer to an actual Swi_Params struct, a default set of parameters is used. "eb" is an error block you can use to handle errors that may occur during Swi object creation.

**Note:**
Swi_create() cannot be called from the context of a Hwi or another Swi thread. Applications that dynamically create Swi threads must do so from either the context of the main() function or a Task thread.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

To create a Swi object in a configuration file, use statements like these:

```javascript
var Swi = xdc.useModule('ti.sysbios.knl.Swi');
var swiParams = new Swi.Params();
program.global.swi0 = Swi.create(swiParams);
```

### 3.5.2 Setting Software Interrupt Priorities

There are different priority levels among Swis. You can create as many Swis as your memory constraints allow for each priority level. You can choose a higher priority (higher priority number) for a Swi that handles a thread with a shorter real-time deadline, and a lower priority for a Swi that handles a thread with a less critical execution deadline.

The number of Swi priorities supported within an application is configurable up to a maximum 32. The maximum number of priority levels is 16 for MSP430 and C28x. The default number of priority levels is 16. The lowest priority level is 0. Thus, by default, the highest priority level is 15.

You cannot sort Swis within a single priority level. They are serviced in the order in which they were posted.
3.5.3  **Software Interrupt Priorities and System Stack Size**

When a Swi is posted, its associated Swi function is invoked using the system stack. While you can have up to 32 Swi priority levels on some targets, keep in mind that in the worst case, each Swi priority level can result in a nesting of the Swi scheduling function (that is, the lowest priority Swi is preempted by the next highest priority Swi, which, in turn, is preempted by the next highest, ...). This results in an increasing stack size requirement for each Swi priority level actually used. Thus, giving Swis the same priority level is more efficient in terms of stack size than giving each Swi a separate priority.

The default system stack size is 4096 bytes. You can set the system stack size by adding the following line to your config script:

```
Program.stack = yourStackSize;
```

**Note:** The Clock module creates and uses a Swi with the maximum Swi priority (that is, if there are 16 Swi priorities, the Clock Swi has priority 15).

The following table shows the amount of system stack required to absorb the worst-case Swi interrupt nesting. This first number is the amount of system stack space required for the first priority level on a target. The second number shows the amount of stack space required for each subsequent priority level used in the application.

**Table 3–5. System Stack Use for Swi Nesting by Target Family**

<table>
<thead>
<tr>
<th>Target Family</th>
<th>Stack Consumed by First Priority Level</th>
<th>Stack Consumed by Subsequent Priority Levels</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>M3</td>
<td>104</td>
<td>88</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430</td>
<td>78</td>
<td>32</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430X</td>
<td>90</td>
<td>60</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430X_small</td>
<td>78</td>
<td>32</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C674</td>
<td>108</td>
<td>120</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C64P</td>
<td>108</td>
<td>120</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C64T</td>
<td>108</td>
<td>120</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>C28_float</td>
<td>83</td>
<td>40</td>
<td>16-bit words</td>
</tr>
<tr>
<td>C28_large</td>
<td>81</td>
<td>34</td>
<td>16-bit words</td>
</tr>
<tr>
<td>Arm9</td>
<td>104</td>
<td>80</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>A8F</td>
<td>160</td>
<td>72</td>
<td>8-bit bytes</td>
</tr>
</tbody>
</table>

See Section 3.4.2 for information about system stack use by Hwis and Section 3.6.3 for information about task stack size.
3.5.4 **Execution of Software Interrupts**

Swis can be scheduled for execution with a call to Swi_andn(), Swi_dec(), Swi_inc(), Swi_or(), and Swi_post(). These calls can be used virtually anywhere in the program—Hwi functions, Clock functions, Idle functions, or other Swi functions.

When a Swi is posted, the Swi manager adds it to a list of posted Swis that are pending execution. The Swi manager checks whether Swis are currently enabled. If they are not, as is the case inside a Hwi function, the Swi manager returns control to the current thread.

If Swis are enabled, the Swi manager checks the priority of the posted Swi object against the priority of the thread that is currently running. If the thread currently running is the background Idle Loop, a Task, or a lower priority Swi, the Swi manager removes the Swi from the list of posted Swi objects and switches the CPU control from the current thread to start execution of the posted Swi function.

If the thread currently running is a Swi of the same or higher priority, the Swi manager returns control to the current thread, and the posted Swi function runs after all other Swis of higher priority or the same priority that were previously posted finish execution.

When multiple Swis of the same priority level have been posted, their respective Swi functions are executed in the order the Swis were posted.

There are two important things to remember about Swi:

- When a Swi starts executing, it must run to completion without blocking.
- When called from within a hardware ISR, the code calling any Swi function that can trigger or post a Swi must be invoked by the Hwi dispatcher (or by generated interrupt stubs on platforms for which the Hwi dispatcher is not provided, such as the MSP430). That is, the Swi must be triggered by a function called from a Hwi object.

Swi functions can be preempted by threads of higher priority (such as a Hwi or a Swi of higher priority). However, Swi functions cannot block. You cannot suspend a Swi while it waits for something—like a device—to be ready.

If a Swi is posted multiple times before the Swi manager has removed it from the posted Swi list, its Swi function executes only once, much like a Hwi is executed only once if the Hwi is triggered multiple times before the CPU clears the corresponding interrupt flag bit in the interrupt flag register. (See Section 3.5.5 for more information on how to handle Swis that are posted multiple times before they are scheduled for execution.)

Applications should not make any assumptions about the order in which Swi functions of equal priority are called. However, a Swi function can safely post itself (or be posted by another interrupt). If more than one is pending, all Swi functions are called before any tasks run.
3.5.5 **Using a Swi Object’s Trigger Variable**

Each Swi object has an associated 32-bit trigger variable for C6x targets and a 16-bit trigger variable for C5x, C28x, and MSP430 targets. This is used either to determine whether to post the Swi or to provide values that can be evaluated within the Swi function.

Swi_post(), Swi_or(), and Swi_inc() post a Swi object unconditionally:

- **Swi_post()** does not modify the value of the Swi object trigger when it is used to post a Swi.
- **Swi_or()** sets the bits in the trigger determined by a mask that is passed as a parameter, and then posts the Swi.
- **Swi_inc()** increases the Swi’s trigger value by one before posting the Swi object.
- **Swi_andn()** and **Swi_dec()** post a Swi object only if the value of its trigger becomes 0:
  - **Swi_andn()** clears the bits in the trigger determined by a mask passed as a parameter.
  - **Swi_dec()** decreases the value of the trigger by one.

Table 3-6 summarizes the differences between these functions.

<table>
<thead>
<tr>
<th>Action</th>
<th>Treats Trigger as Bitmask</th>
<th>Treats Trigger as Counter</th>
<th>Does not Modify Trigger</th>
</tr>
</thead>
<tbody>
<tr>
<td>Always post</td>
<td>Swi_or()</td>
<td>Swi_inc()</td>
<td>Swi_post()</td>
</tr>
<tr>
<td>Post if it becomes zero</td>
<td>Swi_andn()</td>
<td>Swi_dec()</td>
<td>—</td>
</tr>
</tbody>
</table>

The Swi trigger allows you to have tighter control over the conditions that should cause a Swi function to be posted, or the number of times the Swi function should be executed once the Swi is posted and scheduled for execution.

To access the value of its trigger, a Swi function can call Swi_getTrigger(). Swi_getTrigger() can be called only from the Swi object’s function. The value returned by Swi_getTrigger() is the value of the trigger before the Swi object was removed from the posted Swi queue and the Swi function was scheduled for execution.

When the Swi manager removes a pending Swi object from the posted object’s queue, its trigger is reset to its initial value. The initial value of the trigger should be set in the application’s configuration script. If while the Swi function is executing, the Swi is posted again, its trigger is updated accordingly. However, this does not affect the value returned by Swi_getTrigger() while the Swi function executes. That is, the trigger value that Swi_getTrigger() returns is the latched trigger value when the Swi was removed from the list of pending Swis. The Swi’s trigger however, is immediately reset after the Swi is removed from the list of pending Swis and scheduled for execution. This gives the application the ability to keep updating the value of the Swi trigger if a new posting occurs, even if the Swi function has not finished its execution.
For example, if a Swi object is posted multiple times before it is removed from the queue of posted Swis, the Swi manager schedules its function to execute only once. However, if a Swi function must always run multiple times when the Swi object is posted multiple times, Swi_inc() should be used to post the Swi as shown in Figure 3-3.

When a Swi has been posted using Swi_inc(), once the Swi manager calls the corresponding Swi function for execution, the Swi function can access the Swi object trigger to know how many times it was posted before it was scheduled to run, and proceed to execute the same function as many times as the value of the trigger.

Figure 3-3. Using Swi_inc() to Post a Swi

<table>
<thead>
<tr>
<th>Program Configuration</th>
<th>Trigger Value</th>
<th>Value returned by Swi_getTrigger</th>
</tr>
</thead>
<tbody>
<tr>
<td>Swi object mySwi, Function mySwiFxn</td>
<td>0</td>
<td>not callable outside Swi</td>
</tr>
<tr>
<td>-- Calls Swi_inc(&amp;mySwi)</td>
<td>1</td>
<td>not callable outside Swi</td>
</tr>
<tr>
<td>-- mySwi is posted again before it is scheduled for execution</td>
<td>2</td>
<td>not callable outside Swi</td>
</tr>
<tr>
<td>-- Swi manager removes mySwi from posted Swi queue</td>
<td>0</td>
<td>2</td>
</tr>
<tr>
<td>-- mySwiFxn is scheduled for execution</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>-- mySwiFxn begins execution</td>
<td>0</td>
<td>2</td>
</tr>
<tr>
<td>-- mySwiFxn is preempted by ISR that calls Swi_inc(&amp;mySwi)</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>-- mySwi is added to posted Swi queue</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>-- mySwiFxn continues execution</td>
<td>1</td>
<td>2</td>
</tr>
</tbody>
</table>

mySwiFxn()  
{  
  . . .  
  repetitions = SWI_getTrigger();  
  while (repetitions --) {  
    'run Swi function'  
  }  
  . . .  
}
If more than one event must always happen for a given Swi to be triggered, `Swi_andn()` should be used to post the corresponding Swi object as shown in Figure 3-4. For example, if a Swi must wait for input data from two different devices before it can proceed, its trigger should have two set bits when the Swi object is configured. When both functions that provide input data have completed their tasks, they should both call `Swi_andn()` with complementary bitmasks that clear each of the bits set in the Swi trigger default value. Hence, the Swi is posted only when data from both processes is ready.

**Figure 3-4. Using Swi_andn() to Post a Swi**

<table>
<thead>
<tr>
<th>Program Configuration</th>
<th>Trigger Value</th>
<th>Value returned by Swi_getTrigger</th>
</tr>
</thead>
<tbody>
<tr>
<td>Swi object mySwi, Function mySwiFxn</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>0...11</td>
<td>not callable outside Swi</td>
</tr>
</tbody>
</table>

Program Execution
---
-- Calls `Swi_andn(&mySwi, 0x1)`  
-- mySwi is not posted

---
-- Calls `Swi_andn(&mySwi, 0x2)`  
-- mySwi is posted

---
-- Swi manager removes mySwi from posted Swi queue  
-- mySwiFxn is scheduled for execution

---
-- mySwiFxn begins execution

If the program execution requires that multiple occurrences of the same event must take place before a Swi is posted, `Swi_dec()` should be used to post the Swi as shown in Figure 3-5. By configuring the Swi trigger to be equal to the number of occurrences of the event before the Swi should be posted and calling `Swi_dec()` every time the event occurs, the Swi is posted only after its trigger reaches 0; that is, after the event has occurred a number of times equal to the trigger value.

**Figure 3-5. Using Swi_dec() to Post a Swi**

<table>
<thead>
<tr>
<th>Program Configuration</th>
<th>Trigger Value</th>
<th>Value returned by Swi_getTrigger</th>
</tr>
</thead>
<tbody>
<tr>
<td>Swi object mySwi, Function mySwiFxn</td>
<td>2</td>
<td>not callable outside Swi</td>
</tr>
</tbody>
</table>

Program Execution
---
-- Calls `Swi_dec(&mySwi)`  
-- mySwi is not posted

---
-- Calls `Swi_dec(&mySwi)`  
-- mySwi is posted

---
-- Swi manager removes mySwi from posted Swi queue  
-- mySwiFxn is scheduled for execution

---
-- mySwiFxn begins execution
In some situations the Swi function can call different functions depending on the event that posted it. In that case the program can use Swi_or() to post the Swi object unconditionally when an event happens. This is shown in Figure 3-6. The value of the bitmask used by Swi_or() encodes the event type that triggered the post operation, and can be used by the Swi function as a flag that identifies the event and serves to choose the function to execute.

**Figure 3-6. Using Swi_or() to Post a Swi.**

![Diagram showing the use of Swi_or() to post a Swi](image)

In some situations the Swi function can call different functions depending on the event that posted it. In that case the program can use Swi_or() to post the Swi object unconditionally when an event happens. This is shown in Figure 3-6. The value of the bitmask used by Swi_or() encodes the event type that triggered the post operation, and can be used by the Swi function as a flag that identifies the event and serves to choose the function to execute.

**3.5.6 Benefits and Tradeoffs**

There are several benefits to using Swis instead of Hwis:

- By modifying shared data structures in a Swi function instead of a Hwi, you can get mutual exclusion by disabling Swis while a Task accesses the shared data structure (see page 75). This allows the system to respond to events in real-time using Hwis. In contrast, if a Hwi function modified a shared data structure directly, Tasks would need to disable Hwis to access data structures in a mutually exclusive way. Obviously, disabling Hwis may degrade the performance of a real-time system.

- It often makes sense to break long ISRs into two pieces. The Hwi takes care of the extremely time-critical operation and defers less critical processing to a Swi function by posting the Swi within the Hwi function.

Remember that a Swi function must complete before any blocked Task is allowed to run.
3.5.7  **Synchronizing Swi Functions**

Within an Idle, Task, or Swi function, you can temporarily prevent preemption by a higher-priority Swi by calling `Swi_disable()`, which disables all Swi preemption. To reenable Swi preemption, call `Swi_restore()`. Swis are enabled or disabled as a group. An individual Swi cannot be enabled or disabled on its own.

When SYS/BIOS finishes initialization and before the first task is called, Swis have been enabled. If an application wishes to disable Swis, it calls `Swi_disable()` as follows:

```c
key = Swi_disable();
```

The corresponding enable function is `Swi_restore()` where key is a value used by the Swi module to determine if `Swi_disable()` has been called more than once.

```c
Swi_restore(key);
```

This allows nesting of `Swi_disable() / Swi_restore()` calls, since only the outermost `Swi_restore()` call actually enables Swis. In other words, a task can disable and enable Swis without having to determine if `Swi_disable()` has already been called elsewhere.

When Swis are disabled, a posted Swi function does not run at that time. The interrupt is “latched” in software and runs when Swis are enabled and it is the highest-priority thread that is ready to run.

To delete a dynamically created Swi, use `Swi_delete()`. The memory associated with Swi is freed. `Swi_delete()` can only be called from the task level.

3.5.8  **Swi Hooks**

The Swi module supports the following set of Hook functions:

- **Register.** A function called before any statically created Swis are initialized at runtime. The register hook is called at boot time before main() and before interrupts are enabled.

- **Create.** A function called when a Swi is created. This includes Swis that are created statically and those created dynamically using `Swi_create()`.

- **Ready.** A function called when any Swi becomes ready to run.

- **Begin.** A function called just prior to running a Swi function.

- **End.** A function called just after returning from a Swi function.

- **Delete.** A function called when a Swi is deleted at runtime with `Swi_delete()`.

The following `Swi_HookSet` structure type definition encapsulates the hook functions supported by the Swi module:

```c
typedef struct Swi_HookSet {
    Void (*registerFxn)(Int);  /* Register Hook */
    Void (*createFxn)(Handle, Error.Block *); /* Create Hook */
    Void (*readyFxn)(Handle);   /* Ready Hook */
    Void (*beginFxn)(Handle);  /* Begin Hook */
    Void (*endFxn)(Handle);    /* End Hook */
    Void (*deleteFxn)(Handle); /* Delete Hook */
};
```

Swi Hook functions can only be configured statically.

When more than one Hook Set is defined, the individual hook functions of a common type are invoked in hook ID order.
3.5.8.1 Register Function

The Register function is provided to allow a hook set to store its corresponding hook ID. This ID can be passed to Swi_setHookContext() and Swi_getHookContext() to set or get hook-specific context. The Register function must be specified if the hook implementation needs to use Swi_setHookContext() or Swi_getHookContext().

The registerFxn function is called during system initialization before interrupts have been enabled.

The Register functions has the following signature:

\[ \text{Void registerFxn(Int id);} \]

3.5.8.2 Create and Delete Functions

The Create and Delete functions are called whenever a Swi is created or deleted. The Create function is passed an Error_Block that is to be passed to Memory_alloc() for applications that require additional context storage space.

The createFxn and deleteFxn functions are called with interrupts enabled (unless called at boot time or from main()).

These functions have the following signatures.

\[ \text{Void createFxn(Swi_Handle swi, Error_Block *eb);} \]
\[ \text{Void deleteFxn(Swi_Handle swi);} \]

3.5.8.3 Ready, Begin and End Functions

The Ready, Begin and End hook functions are called with interrupts enabled. The readyFxn function is called when a Swi is posted and made ready to run. The beginFxn function is called right before the function associated with the given Swi is run. The endFxn function is called right after returning from the Swi function.

Both readyFxn and beginFxn hooks are provided because a Swi may be posted and ready but still pending while a higher-priority thread completes.

These functions have the following signatures:

\[ \text{Void readyFxn(Swi_Handle swi);} \]
\[ \text{Void beginFxn(Swi_Handle swi);} \]
\[ \text{Void endFxn(Swi_Handle swi);} \]
3.5.8.4 **Swi Hooks Example**

The following example application uses two Swi hook sets. This example demonstrates how to read and write the Hook Context Pointer associated with each hook set.

The configuration script and program output are shown after the C code listing.

This is the C code for the example:

```c
/* ======== SwiHookExample.c ========
* This example demonstrates basic Swi hook usage */

#include <xdc/std.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Timestamp.h>

#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/hal/Timer.h>
#include <ti/sysbios/knl/Swi.h>

Swi_Handle mySwi;
Int myHookSetId1, myHookSetId2;

/* HookSet 1 functions */

/* ======== myRegister1 ========
* invoked during Swi module startup before main
* for each HookSet */
Void myRegister1(Int hookSetId)
{
    System_printf("myRegister1: assigned hookSet Id = %d\n", hookSetId);
    myHookSetId1 = hookSetId;
}

/* ======== myCreate1 ========
* invoked during Swi_create for dynamically created Swis */
Void myCreate1(Swi_Handle swi, Error_Block *eb)
{
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId1);

    /* pEnv should be 0 at this point. If not, there's a bug. */
    System_printf("myCreate1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId1, (Ptr)0xdead1);
}
```
void myReady1(Swi_Handle swi) {
    Ptr pEnv;
    pEnv = Swi_getHookContext(swi, myHookSetId1);
    System_printf("myReady1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId1, (Ptr)0xbeef1);
}

void myBegin1(Swi_Handle swi) {
    Ptr pEnv;
    pEnv = Swi_getHookContext(swi, myHookSetId1);
    System_printf("myBegin1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId1, (Ptr)0xfeeb1);
}

void myEnd1(Swi_Handle swi) {
    Ptr pEnv;
    pEnv = Swi_getHookContext(swi, myHookSetId1);
    System_printf("myEnd1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId1, (Ptr)0xc0de1);
}

void myDelete1(Swi_Handle swi) {
    Ptr pEnv;
    pEnv = Swi_getHookContext(swi, myHookSetId1);
    System_printf("myDelete1: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
}
/* HookSet 2 functions */

/* ======== myRegister2 ========
* invoked during Swi module startup before main
* for each HookSet */
Void myRegister2(Int hookSetId)
{
    System_printf("myRegister2: assigned hook Set Id = %d\n", hookSetId);
    myHookSetId2 = hookSetId;
}

/* ======== myCreate2 ========
* invoked during Swi_create for dynamically created Swis */
Void myCreate2(Swi_Handle swi, Error_Block *eb)
{
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId2);

    /* pEnv should be 0 at this point. If not, there's a bug. */
    System_printf("myCreate2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId2, (Ptr)0xdead2);
}

/* ======== myReady2 ========
* invoked when Swi is posted */
Void myReady2(Swi_Handle swi)
{
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId2);

    System_printf("myReady2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId2, (Ptr)0xbeef2);
}

/* ======== myBegin2 ========
* invoked just before Swi func is run */
Void myBegin2(Swi_Handle swi)
{
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId2);

    System_printf("myBegin2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId2, (Ptr)0xfeeb2);
}
/* ======= myEnd2 =======
 * invoked after Swi func returns */
Void myEnd2(Swi_Handle swi) {
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId2);

    System_printf("myEnd2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
    Swi_setHookContext(swi, myHookSetId2, (Ptr)0xc0de2);
}

/* ======= myDelete2 ========
 * invoked upon Swi deletion */
Void myDelete2(Swi_Handle swi) {
    Ptr pEnv;

    pEnv = Swi_getHookContext(swi, myHookSetId2);

    System_printf("myDelete2: pEnv = 0x%x, time = %d\n", pEnv, Timestamp_get32());
}

/* ======= mySwiFunc ======= */
Void mySwiFunc(UArg arg0, UArg arg1) {
    System_printf("Entering mySwi.\n");
}

/* ======= myTaskFunc ======= */
Void myTaskFunc(UArg arg0, UArg arg1) {
    System_printf("Entering myTask.\n");
    System_printf("Posting mySwi.\n");
    Swi_post(mySwi);

    System_printf("Deleting mySwi.\n");
    Swi_delete(&mySwi);

    System_printf("myTask exiting ...\n");
}

/* ======= myIdleFunc ======= */
Void myIdleFunc() {
    System_printf("Entering myIdleFunc().\n");
    System_exit(0);
}
This is the configuration script for the example:

```c
/* pull in Timestamp to print time in hook functions */
xdc.useModule('xdc.runtime.Timestamp');

/* Disable Clock so that ours is the only Swi in the application */
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.clockEnabled = false;

var Idle = xdc.useModule('ti.sysbios.knl.Idle');
Idle.addFunc('&myIdleFunc');

/* Create myTask with default task params */
var Task = xdc.useModule('ti.sysbios.knl.Task');
var taskParams = new Task.Params();
Program.global.myTask = Task.create('&myTaskFunc', taskParams);

/* Define and add two Swi Hook Sets */
var Swi = xdc.useModule('ti.sysbios.knl.Swi');

/* Hook Set 1 */
Swi.addHookSet({
    registerFxn: '&myRegister1',
    createFxn: '&myCreatel',
    readyFxn: '&myReady1',
    beginFxn: '&myBegin1',
    endFxn: '&myEndl',
    deleteFxn: '&myDelete1'
});
```
3.6 Tasks

SYS/BIOS task objects are threads that are managed by the Task module. Tasks have higher priority than the Idle Loop and lower priority than hardware and software interrupts. See the video introducing Tasks for an overview.

The Task module dynamically schedules and preempts tasks based on the task’s priority level and the task’s current execution state. This ensures that the processor is always given to the highest priority thread that is ready to run. There are up to 32 priority levels available for tasks, with the default number of levels being 16. The maximum number of priority levels is 16 for MSP430 and C28x. The lowest priority level (0) is reserved for running the Idle Loop.

The Task module provides a set of functions that manipulate task objects. They access Task objects through handles of type Task_Handle.

The kernel maintains a copy of the processor registers for each task object. Each task has its own runtime stack for storing local variables as well as for further nesting of function calls. See Section 3.6.3 for information about task stack sizes.

All tasks executing within a single program share a common set of global variables, accessed according to the standard rules of scope defined for C functions.
3.6.1 Creating Tasks

You can create Task objects either dynamically with a call to Task_create() or statically in the configuration. Tasks that you create dynamically can also be deleted during program execution.

3.6.1.1 Creating and Deleting Tasks Dynamically

You can spawn SYS/BIOS tasks by calling the function Task_create(), whose parameters include the address of a C function in which the new task begins its execution. The value returned by Task_create() is a handle of type Task_Handle, which you can then pass as an argument to other Task functions.

This C example creates a task:

```c
Task_Params taskParams;
Task_Handle task0;
Error_Block eb;

... 

Error_init(&eb);

/* Create 1 task with priority 15 */
Task_Params_init(&taskParams);
taskParams.stackSize = 512;
taskParams.priority = 15;
task0 = Task_create((Task_FuncPtr)hiPriTask, &taskParams, &eb);
if (task0 == NULL) {
    System_abort("Task create failed");
}
```

If NULL is passed instead of a pointer to an actual Task_Params struct, a default set of parameters is used. The "eb" is an error block that you can use to handle errors that may occur during Task object creation. See Section 3.6.3 for information about task stack sizes.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

A task becomes active when it is created and preempts the currently running task if it has a higher priority.

The memory used by Task objects and stacks can be reclaimed by calling Task_delete(). Task_delete() removes the task from all internal queues and frees the task object and stack.

Any Semaphores or other resources held by the task are not released. Deleting a task that holds such resources is often an application design error, although not necessarily so. In most cases, such resources should be released prior to deleting the task. It is only safe to delete a Task that is either in the Terminated or Inactive State.

```c
Void Task_delete(Task_Handle *task);
```

3.6.1.2 Creating Tasks Statically

You can also create tasks statically within a configuration script. The configuration allows you to set a number of properties for each task and for the Task manager itself.
For a complete description of all Task properties, see the Task module in the "ti.sysbios.knl" package documentation in the online documentation. (For information on running online help, see Section 1.6.1, Using the API Reference Help System, page 25.)

While it is running, a task that was created statically behaves exactly the same as a task created with Task_create(). You cannot use the Task_delete() function to delete statically-created tasks.

The Task module automatically creates the Task_idle task and gives it the lowest task priority (0). It runs the functions defined for the Idle objects when no higher-priority Hwi, Swi, or Task is running.

When you configure tasks to have equal priority, they are scheduled in the order in which they are created in the configuration script. Tasks can have up to 32 priority levels with 16 being the default. The maximum number of priority levels is 16 for MSP430 and C28x. The highest level is the number of priorities defined minus 1, and the lowest is 0. The priority level of 0 is reserved for the system idle task. You cannot sort tasks within a single priority level by setting the order property.

If you want a task to be initially inactive, set its priority to -1. Such tasks are not scheduled to run until their priority is raised at runtime.

3.6.2 Task Execution States and Scheduling

Each Task object is always in one of four possible states of execution:

- **Task_Mode_RUNNING**, which means the task is the one actually executing on the system’s processor.
- **Task_Mode_READY**, which means the task is scheduled for execution subject to processor availability.
- **Task_Mode_BLOCKED**, which means the task cannot execute until a particular event occurs within the system.
- **Task_Mode_TERMINATED**, which means the task is “terminated” and does not execute again.
- **Task_Mode_INACTIVE**, which means the task has a priority equal to -1 and is in a pre-Ready state. This priority can be set when the task is created or by calling the Task_setPri() API at runtime.

Tasks are scheduled for execution according to a priority level assigned by the application. There can be no more than one running task. As a rule, no ready task has a priority level greater than that of the currently running task, since Task preempts the running task in favor of the higher-priority ready task. Unlike many time-sharing operating systems that give each task its “fair share” of the processor, SYS/BIOS *immediately* preempts the current task whenever a task of higher priority becomes ready to run.

The maximum priority level is Task_numPriorities-1 (default=15; maximum=31). The minimum priority is 1. If the priority is less than 0, the task is barred from further execution until its priority is raised at a later time by another task. If the priority equals Task_numPriorities-1, the task cannot be preempted by another task. A highest-priority task can still call Semaphore_pend(), Task_sleep(), or some other blocking call to allow tasks of lower priority to run. A Task’s priority can be changed at runtime with a call to Task_setPri().
During the course of a program, each task’s mode of execution can change for a number of reasons. Figure 3-7 shows how execution modes change.

**Figure 3-7. Execution Mode Variations**

Functions in the Task, Semaphore, Event, and Mailbox modules alter the execution state of task objects: blocking or terminating the currently running task, readying a previously suspended task, re-scheduling the current task, and so forth.

There is one task whose execution mode is Task_Mode_RUNNING. If all program tasks are blocked and no Hwi or Swi is running, Task executes the Task_idle task, whose priority is lower than all other tasks in the system. When a task is preempted by a Hwi or Swi, the task execution mode returned for that task by Task_stat() is still Task_Mode_RUNNING because the task will run when the preemption ends.

**Notes:** Do not make blocking calls, such as Semaphore_pend() or Task_sleep(), from within an Idle function. Doing so causes the application to terminate.

When the Task_Mode_RUNNING task transitions to any of the other three states, control switches to the highest-priority task that is ready to run (that is, whose mode is Task_Mode_READY). A Task_Mode_RUNNING task transitions to one of the other modes in the following ways:

- The running task becomes Task_Mode_TERMINATED by calling Task_exit(), which is automatically called if and when a task returns from its top-level function. After all tasks have returned, the Task manager terminates program execution by calling System_exit() with a status code of 0.
- The running task becomes Task_Mode_BLOCKED when it calls a function (for example, Semaphore_pend() or Task_sleep()) that causes the current task to suspend its execution; tasks can move into this state when they are performing certain I/O operations, awaiting availability of some shared resource, or idling.
- The running task becomes Task_Mode_READY and is preempted whenever some other, higher-priority task becomes ready to run. Task_setPri() can cause this type of transition if the priority of the current task is no longer the highest in the system. A task can also use Task_yield() to yield to other tasks with the same priority. A task that yields becomes ready to run.
A task that is currently Task_Mode_BLOCKED transitions to the ready state in response to a particular event: completion of an I/O operation, availability of a shared resource, the elapse of a specified period of time, and so forth. By virtue of becoming Task_Mode_READY, this task is scheduled for execution according to its priority level; and, of course, this task immediately transitions to the running state if its priority is higher than the currently executing task. Task schedules tasks of equal priority on a first-come, first-served basis.

3.6.3 **Task Stacks**

The kernel maintains a copy of the processor registers for each Task object. Each Task has its own runtime stack for storing local variables as well as for further nesting of function calls.

You can specify the stack size separately for each Task object when you create the Task object statically or dynamically. Each task stack must be large enough to handle both its normal function calls and two full interrupting Hwi contexts.

The “Maximum Stack Consumed” column in the following table shows the amount of stack space required to absorb the worst-case interrupt nesting. These numbers represent two full Hwi interrupt contexts plus the space used by the task scheduler for its local variables. Additional nested interrupt contexts are pushed onto the common system stack.

Note that when the kernel is configured such that there are no Task Hooks configured (with `Task.minimizeLatency = false` and `BIOS.logsEnabled = false`), each task stack must only support a single full Hwi interrupt context in addition to the task scheduler's local variables. This configuration is equal to the “Minimum Stack Consumed” in Table 3–7.

**Table 3–7. Task Stack Use by Target Family**

<table>
<thead>
<tr>
<th>Target</th>
<th>Minimum Stack Consumed</th>
<th>Maximum Stack Consumed</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>IAR_M3</td>
<td>32</td>
<td>32</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>GCC_M3</td>
<td>39</td>
<td>39</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_M3</td>
<td>32</td>
<td>32</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>GCC_M4</td>
<td>39</td>
<td>159</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>IAR_M4</td>
<td>32</td>
<td>152</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_M4</td>
<td>32</td>
<td>112</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>GCC_M4F</td>
<td>111</td>
<td>303</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>IAR_M4F</td>
<td>104</td>
<td>296</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_M4F</td>
<td>104</td>
<td>256</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>MSP430X</td>
<td>32</td>
<td>88</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_C28_float</td>
<td>51</td>
<td>115</td>
<td>16-bit words</td>
</tr>
<tr>
<td>TI_C28_large</td>
<td>39</td>
<td>89</td>
<td>16-bit words</td>
</tr>
<tr>
<td>GCC_A9F</td>
<td>271</td>
<td>591</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_A8Fnv</td>
<td>244</td>
<td>524</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>GCC_A8F</td>
<td>251</td>
<td>571</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>GCC_a15F</td>
<td>271</td>
<td>591</td>
<td>8-bit bytes</td>
</tr>
</tbody>
</table>
When a Task is preempted, a task stack may be required to contain either two interrupting Hwi contexts (if the Task is preempted by a Hwi) or one interrupting Hwi context and one Task preemption context (if the Task is preempted by a higher-priority Task). Since the Hwi context is larger than the Task context, the numbers given are for two Hwi contexts. If a Task blocks, only those registers that a C function must save are saved to the task stack.

Another way to find the correct stack size is to make the stack size large and then use Code Composer Studio software to find the stack size actually used.

For C7000 targets, a special Task stack size constraint applies. The size available for use by the Task is 8 KB smaller than the size allocated. Also, the stack size is truncated using the stack's alignment of 8 KB, and the stack pointer may point only to a location that is a multiple of this alignment. This size reduction occurs because a separate Task Context Save Pointer (TCSP) stack for use in processing interrupts is placed in the top portion of the block allocated for each Task stack. The C7000 hardware requires that the TCSP stack size be 8 KB and have an alignment of 8 KB. Since the Task stack effectively contains 2 stacks, both with a minimum size of 8 KB, the minimum size to allocate for a Task stack is 16 KB.

See Section 3.4.2 for information about system stack use by Hwis and Section 3.5.3 for information about system stack size.

### 3.6.4 Testing for Stack Overflow

When a task uses more memory than its stack has been allocated, it can write into an area of memory used by another task or data. This results in unpredictable and potentially fatal consequences. Therefore, a means of checking for stack overflow is useful.

By default, the Task module checks to see whether a Task stack has overflowed at each Task switch. To improve Task switching latency, you can disable this feature the Task.checkStackFlag property to false.

The function Task_stat() can be used to watch stack size. The structure returned by Task_stat() contains both the size of its stack and the maximum number of MAUs ever used on its stack, so this code segment could be used to warn of a nearly full stack:

```c
Task_Stat statbuf;               /* declare buffer */

Task_stat(Task_self(), &statbuf); /* call func to get status */
if ((statbuf.used > (statbuf.stackSize * 9 / 10)) { 
    System_printf("Over 90% of task's stack is in use.\n")
}
```

See the Task_stat() information in the "ti.sysbios.knl" package documentation in the online documentation.

You can use the Runtime Object View (ROV) to examine runtime Task stack usage.

<table>
<thead>
<tr>
<th>Target</th>
<th>Minimum Stack Consumed</th>
<th>Maximum Stack Consumed</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>TI_ARP32</td>
<td>96</td>
<td>224</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_ARP32_far</td>
<td>96</td>
<td>224</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_C674</td>
<td>300</td>
<td>628</td>
<td>8-bit bytes</td>
</tr>
<tr>
<td>TI_C66</td>
<td>300</td>
<td>628</td>
<td>8-bit bytes</td>
</tr>
</tbody>
</table>
Tasks

3.6.5 Task Hooks

The Task module supports the following set of Hook functions:

- **Register.** A function called before any statically created Tasks are initialized at runtime. The register hook is called at boot time before main() and before interrupts are enabled.
- **Create.** A function called when a Task is created. This includes Tasks that are created statically and those created dynamically using Task_create() or Task_construct(). The Create hook is called outside of a Task_disable/enable block and before the task has been added to the ready list.
- **Ready.** A function called when a Task becomes ready to run. The ready hook is called from within a Task_disable/enable block with interrupts enabled.
- **Switch.** A function called just before a task switch occurs. The 'prev' and 'next' task handles are passed to the Switch hook. 'prev' is set to NULL for the initial task switch that occurs during SYS/BIOS startup. The Switch hook is called from within a Task_disable/enable block with interrupts enabled.
- **Exit.** A function called when a task exits using Task_exit(). The exit hook is passed the handle of the exiting task. The exit hook is called outside of a Task_disable/enable block and before the task has been removed from the kernel lists.
- **Delete.** A function called when a task is deleted at runtime with Task_delete().

The following HookSet structure type definition encapsulates the hook functions supported by the Task module:

```c
typedef struct Task_HookSet {
    Void (*registerFxn)(Int);                 /* Register Hook */
    Void (*createFxn)(Handle, Error.Block *); /* Create Hook */
    Void (*readyFxn)(Handle);                 /* Ready Hook */
    Void (*switchFxn)(Handle, Handle);        /* Switch Hook */
    Void (*exitFxn)(Handle);                  /* Exit Hook */
    Void (*deleteFxn)(Handle);                /* Delete Hook */
};
```

When more than one hook set is defined, the individual hook functions of a common type are invoked in hook ID order.

Task hook functions can only be configured statically.

3.6.5.1 Register Function

The Register function is provided to allow a hook set to store its corresponding hook ID. This ID can be passed to Task_setHookContext() and Task_getHookContext() to set or get hook-specific context. The Register function must be specified if the hook implementation needs to use Task_setHookContext() or Task_getHookContext().

The registerFxn function is called during system initialization before interrupts have been enabled.

The Register function has the following signature:

```c
Void registerFxn(Int id);
```
3.6.5.2 Create and Delete Functions

The Create and Delete functions are called whenever a Task is created or deleted. The Create function is passed an Error_Block that is to be passed to Memory_alloc() for applications that require additional context storage space.

The createFxn and deleteFxn functions are called with interrupts enabled (unless called at boot time or from main()).

These functions have the following signatures.

```c
Void createFxn(Task_Handle task, Error_Block *eb);
Void deleteFxn(Task_Handle task);
```

3.6.5.3 Switch Function

If a Switch function is specified, it is invoked just before the new task is switched to. The switch function is called with interrupts enabled.

This function can be used for purposes such as saving/restoring additional task context (for example, external hardware registers), checking for task stack overflow, and monitoring the time used by each task.

The switchFxn has the following signature:

```c
Void switchFxn(Task_Handle prev, Task_Handle next);
```

3.6.5.4 Ready Function

If a Ready Function is specified, it is invoked whenever a task is made ready to run. The Ready Function is called with interrupts enabled (unless called at boot time or from main()).

The readyFxn has the following signature:

```c
Void readyFxn(Task_Handle task);
```

3.6.5.5 Exit Function

If an Exit Function is specified, it is invoked when a task exits (via call to Task_exit() or when a task returns from its' main function). The exitFxn is called with interrupts enabled.

The exitFxn has the following signature:

```c
Void exitFxn(Task_Handle task);
```

3.6.5.6 Task Hooks Example

The following example application uses a single Task hook set. This example demonstrates how to read and write the Hook Context Pointer associated with each hook set.

The configuration script and program output are shown after the C code listing.

This is the C code for the example:

```c
/* ======== TaskHookExample.c ========
* This example demonstrates basic task hook processing
* operation for dynamically created tasks. */
```
```c
#include <xdc/std.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/Memory.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Types.h>

#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Task.h>

Task_Handle myTsk0, myTsk1, myTsk2;
Int myHookSetId, myHookSetId2;

/* HookSet functions */

/* ======== myRegister ========
 * invoked during Swi module startup before main()
 * for each HookSet */
Void myRegister(Int hookSetId)
{
    System_printf("myRegister: assigned HookSet Id = %d\n", hookSetId);
    myHookSetId = hookSetId;
}

/* ======== myCreate ========
 * invoked during Task_create for dynamically
 * created Tasks */
Void myCreate(Task_Handle task, Error_Block *eb)
{
    String name;
    Ptr pEnv;

    name = Task_Handle_name(task);
    pEnv = Task_getHookContext(task, myHookSetId);

    System_printf("myCreate: task name = '%s', pEnv = 0x%x\n", name, pEnv);
    Task_setHookContext(task, myHookSetId, (Ptr)0xdead);
}

/* ======== myReady ========
 * invoked when Task is made ready to run */
Void myReady(Task_Handle task)
{
    String name;
    Ptr pEnv;
```
name = Task_Handle_name(task);
pEnv = Task_getHookContext(task, myHookSetId);

System_printf("myReady: task name = '%s', pEnv = 0x%\n", name, pEnv);
Task_setHookContext(task, myHookSetId, (Ptr)0xc0de);
}

/* ======== mySwitch ========
 * invoked whenever a Task switch occurs/is made ready to run */
Void mySwitch(Task_Handle prev, Task_Handle next)
{
    String prevName;
    String nextName;
    Ptr pPrevEnv;
    Ptr pNextEnv;

    if (prev == NULL) {
        System_printf("mySwitch: ignoring dummy 1st prev Task\n");
    }
    else {
        prevName = Task_Handle_name(prev);
pPrevEnv = Task_getHookContext(prev, myHookSetId);

        System_printf("mySwitch: prev name = '%s', pPrevEnv = 0x%\n", prevName, pPrevEnv);
        Task_setHookContext(prev, myHookSetId, (Ptr)0xcafeC0de);
    }
    nextName = Task_Handle_name(next);
pNextEnv = Task_getHookContext(next, myHookSetId);

    System_printf("next name = '%s', pNextEnv = 0x%\n", nextName, pNextEnv);
    Task_setHookContext(next, myHookSetId, (Ptr)0xc001C0de);
}

/* ======== myExit ========
 * invoked whenever a Task calls Task_exit() or falls through
 * the bottom of its task function. */
Void myExit(Task_Handle task)
{
    Task_Handle curTask = task;
    String name;
    Ptr pEnv;

    name = Task_Handle_name(curTask);
pEnv = Task_getHookContext(curTask, myHookSetId);
/* Define 3 identical tasks */
Void myTsk0Func(UArg arg0, UArg arg1)
{
    System_printf("myTsk0 Entering\n");
    System_printf("myTsk0 Calling Task_yield\n");
    Task_yield();
    System_printf("myTsk0 Exiting\n");
}

Void myTsk1Func(UArg arg0, UArg arg1)
{
    System_printf("myTsk1 Entering\n");
    System_printf("myTsk1 Calling Task_yield\n");
    Task_yield();
    System_printf("myTsk1 Exiting\n");
}

Void myTsk2Func(UArg arg0, UArg arg1)
{
    System_printf("myTsk2 Entering\n");
    System_printf("myTsk2 Calling Task_yield\n");
    Task_yield();
    System_printf("myTsk2 Exiting\n");
}

/* ======== main ======== */
Int main(Int argc, Char* argv[])
{
    Task_Params params;
    Error_Block eb;
Error_init(&eb);
Task_Params_init(&params);

params.instance->name = "myTsk0";
myTsk0 = Task_create(myTsk0Func, &params, &eb);
if (myTsk0 == NULL) {
    System_abort("myTsk0 create failed");
}

params.instance->name = "myTsk1";
myTsk1 = Task_create(myTsk1Func, &params, &eb);
if (myTsk1 == NULL) {
    System_abort("myTsk1 create failed");
}

params.instance->name = "myTsk2";
myTsk2 = Task_create(myTsk2Func, &params, &eb);
if (myTsk2 == NULL) {
    System_abort("myTsk2 create failed");
}

BIOS_start();
return (0);

/* ======== myIdleFunc ======== */
Void myIdleFunc()
{
    System_printf("Entering idleFunc().\n");

    Task_delete(&myTsk0);
    Task_delete(&myTsk1);
    Task_delete(&myTsk2);

    System_exit(0);
}

The configuration script is as follows:

/* Lots of System_printf() output requires a bigger bufSize */
SysMin = xdc.useModule('xdc.runtime.SysMin');
SysMin.bufSize = 4096;

var Idle = xdc.useModule('ti.sysbios.knl.Idle');
Idle.addFunc('&myIdleFunc');

var Task = xdc.useModule('ti.sysbios.knl.Task');
/* Enable instance names */
Task.common$.namedInstance = true;

/* Define and add one Task Hook Set */
Task.addHookSet({
    registerFxn: '&myRegister',
    createFxn: '&myCreate',
    readyFxn: '&myReady',
    switchFxn: '&mySwitch',
    exitFxn: '&myExit',
    deleteFxn: '&myDelete',
});

The program output is as follows:

myRegister: assigned HookSet Id = 0
myCreate: task name = 'ti.sysbios.knl.Task.IdleTask', pEnv = 0x0
myReady: task name = 'ti.sysbios.knl.Task.IdleTask', pEnv = 0xd
myCreate: task name = 'myTsk0', pEnv = 0x0
myReady: task name = 'myTsk0', pEnv = 0xd
myCreate: task name = 'myTsk1', pEnv = 0x0
myReady: task name = 'myTsk1', pEnv = 0xd
myCreate: task name = 'myTsk2', pEnv = 0x0
myReady: task name = 'myTsk2', pEnv = 0xd
mySwitch: ignoring dummy 1st prev Task
    next name = 'myTsk0', pNextEnv = 0xc0de
myTsk0 Entering
myTsk0 Calling Task_yield
mySwitch: prev name = 'myTsk0', pPrevEnv = 0xc001c0de
    next name = 'myTsk1', pNextEnv = 0xc0de
myTsk1 Entering
myTsk1 Calling Task_yield
mySwitch: prev name = 'myTsk1', pPrevEnv = 0xc001c0de
    next name = 'myTsk2', pNextEnv = 0xc0de
myTsk2 Entering
myTsk2 Calling Task_yield
mySwitch: prev name = 'myTsk2', pPrevEnv = 0xc001c0de
    next name = 'myTsk0', pNextEnv = 0xcafe0de
myTsk0 Exiting
myExit: curTask name = 'myTsk0', pEnv = 0xc001c0de
mySwitch: prev name = 'myTsk0', pPrevEnv = 0xdeadbeef
    next name = 'myTsk1', pNextEnv = 0xcafe0de
myTsk1 Exiting
myExit: curTask name = 'myTsk1', pEnv = 0xc001c0de
mySwitch: prev name = 'myTsk1', pPrevEnv = 0xdeadbeef
    next name = 'myTsk2', pNextEnv = 0xcafe0de
myTsk2 Exiting
myExit: curTask name = 'myTsk2', pEnv = 0xc001c0de
mySwitch: prev name = 'myTsk2', pPrevEnv = 0xdeadbeef
    next name = 'ti.sysbios.knl.Task.IdleTask', pNextEnv = 0xc0de
3.6.6 Task Yielding for Time-Slice Scheduling

Example 3-1 demonstrates a time-slicing scheduling model that can be managed by a user. This model is preemptive and does not require any cooperation (that is, code) by the tasks. The tasks are programmed as if they were the only thread running. Although SYS/BIOS tasks of differing priorities can exist in any given application, the time-slicing model only applies to tasks of equal priority.

In this example, a periodic Clock object is configured to run a simple function that calls the Task_yield() function every 4 clock ticks. Another periodic Clock object is to run a simple function that calls the Semaphore_post() function every 16 milliseconds.

The output of the example code is shown after the code.

Example 3-1 Time-Slice Scheduling

```
/*
 * ======== slice.c ========
 * This example utilizes time-slice scheduling among three tasks of equal priority. A fourth task of higher priority periodically preempts execution.
 * A periodic Clock object drives the time-slice scheduling.
 * Every 4 milliseconds, the Clock object calls Task_yield() which forces the current task to relinquish access to the CPU.
 * Because a task is always ready to run, this program does not spend time in the idle loop. Calls to Idle_run() are added to give time to the Idle loop functions occasionally. The call to Idle_run() is within a Task_disable(), Task_restore() block because the call to Idle_run() is not reentrant.
 */

#include <xdc/std.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Clock.h>
#include <ti/sysbios/knl/Clock.h>
#include <ti/sysbios/knl/Idle.h>
#include <ti/sysbios/knl/Task.h>

#include <xdc/cfg/global.h>
```
Void hiPriTask(UArg arg0, UArg arg1);
Void task(UArg arg0, UArg arg1);
Void clockHandler1(UArg arg);
Void clockHandler2(UArg arg);
Semaphore_Handle sem;

/* ======== main ======== */
Void main()
{
    Task_Params taskParams;
    Task_Handle myTsk0, myTski;
    Clock_Params clockParams;
    Clock_Handle myClk0, myClk1;
    Error_Block eb;
    UInt i;

    System_printf("Slice example started!\n");

    Error_init(&eb);

    /* Create 1 task with priority 15 */
    Task_Params_init(&taskParams);
    taskParamsStackSize = 512;
    // Note: Larger stack needed for some targets, including 'C6748
    taskParams.priority = 15;
    myTsk0 = Task_create((Task_FuncPtr)hiPriTask, &taskParams, &eb);
    if (myTsk0 == NULL) {
        System_abort("hiPriTask create failed");
    }

    /* Create 3 tasks with priority 1 */
    /* re-uses taskParams */
    taskParams.priority = 1;
    for (i = 0; i < 3; i++) {
        taskParams.arg0 = i;
        myTski = Task_create((Task_FuncPtr)task, &taskParams, &eb);
        if (myTski == NULL) {
            System_abort("LoPri Task %d create failed", i);
        }
    }
}
/* Create clock that calls Task_yield() every 4 Clock ticks */
Clock_Params_init(&clockParams);
clockParams.period = 4; /* every 4 Clock ticks */
clockParams.startFlag = TRUE; /* start immediately */
myClk0 = Clock_create((Clock_FuncPtr)clockHandler1, 4, &clockParams, &eb);
if (myClk0 == NULL) {
    System_abort("Clock0 create failed");
}

/* Create clock that calls Semaphore_post() every 16 Clock ticks */
clockParams.period = 16; /* every 16 Clock ticks */
clockParams.startFlag = TRUE; /* start immediately */
myClk1 = Clock_create((Clock_FuncPtr)clockHandler2, 16, &clockParams, &eb);
if (myClk1 == NULL) {
    System_abort("Clock1 create failed");
}

/* Create semaphore with initial count = 0 and default params */
sem = Semaphore_create(0, NULL, &eb);
if (sem == NULL) {
    System_abort("Semaphore create failed");
}

/* Start SYS/BIOS */
BIOS_start();
}

/* ======== clockHandler1 ======== */
Void clockHandler1(UArg arg)
{
    /* Call Task_yield every 4 ms */
    Task_yield();
}

/* ======== clockHandler2 ======== */
Void clockHandler2(UArg arg)
{
    /* Call Semaphore_post every 16 ms */
    Semaphore_post(sem);
/* ======== task ======== */
Void task(UArg arg0, UArg arg1)
{
    Int time;
    Int prevtime = -1;
    UInt taskKey;

    /* While loop simulates work load of time-sharing tasks */
    while (1) {
        time = Clock_getTicks();

        /* print time once per clock tick */
        if (time >= prevtime + 1) {
            prevtime = time;
            System_printf("Task %d: time is %d\n", (Int)arg0, time);
        }

        /* check for rollover */
        if (prevtime > time) {
            prevtime = time;
        }

        /* Process the Idle Loop functions */
        taskKey = Task_disable();
        Idle_run();
        Task_restore(taskKey);
    }
}

/* ======== hiPriTask ======== */
Void hiPriTask(UArg arg0, UArg arg1)
{
    static Int numTimes = 0;

    while (1) {
        System_printf("hiPriTask here\n");
        if (++numTimes < 3) {
            Semaphore_pend(sem, BIOS_WAIT_FOREVER);
        } else {
            System_printf("Slice example ending.\n");
            System_exit(0);
        }
    }
}
The System_printf() output for this example is as follows:

```
Slice example started!
hiPriTask here
Task 0: time is 0
Task 0: time is 1
Task 0: time is 2
Task 0: time is 3
Task 1: time is 4
Task 1: time is 5
Task 1: time is 6
Task 1: time is 7
Task 2: time is 8
Task 2: time is 9
Task 2: time is 10
Task 2: time is 11
Task 0: time is 12
Task 0: time is 13
Task 0: time is 14
Task 0: time is 15
hiPriTask here
Task 1: time is 16
Task 1: time is 17
Task 1: time is 18
Task 1: time is 19
Task 2: time is 20
Task 2: time is 21
Task 2: time is 22
Task 2: time is 23
Task 0: time is 24
Task 0: time is 25
Task 0: time is 26
Task 0: time is 27
Task 1: time is 28
Task 1: time is 29
Task 1: time is 30
Task 1: time is 31
hiPriTask here
```

Slice example ending.
3.7 The Idle Loop

The Idle Loop is the background thread of SYS/BIOS, which runs continuously when no Hwi, Swi, or Task is running. Any other thread can preempt the Idle Loop at any point.

The Idle manager allows you to insert functions that execute within the Idle Loop. The Idle Loop runs the Idle functions you configured. Idle_loop calls the functions associated with each one of the Idle objects one at a time, and then starts over again in a continuous loop.

Idle threads all run at the same priority, sequentially. The functions are called in the same order in which they were created. An Idle function must run to completion before the next Idle function can start running. When the last idle function has completed, the Idle Loop starts the first Idle function again.

Idle Loop functions are often used to poll non-real-time devices that do not (or cannot) generate interrupts, monitor system status, or perform other background activities.

The Idle Loop is the thread with lowest priority in a SYS/BIOS application. The Idle Loop functions run only when no Hwis, Swis, or Tasks need to run.

The CPU load and thread load are computed in an Idle loop function. (Data transfer for between the target and the host is handled by a low-priority task.)

If you configure Task.enableIdleTask to be false, no Idle task is created and the Idle functions are not run. If you want a function to run when there are no other threads ready to run, you can specify such a function using Task.allBlockedFunc.

If you want the Idle Loop to run without creating a dedicated Idle task, you can disable Task.enableIdleTask and configure Task.allBlockedFunc as follows. These statements cause the Idle functions to be run using the stack of the last Task to pend.

```
Task.enableIdleTask = false;
Task.allBlockedFunc = Idle.run;
```
3.8 Example Using Hwi, Swi, and Task Threads

This example depicts a stylized version of the SYS/BIOS Clock module design. It uses a combination of Hwi, Swi, and Task threads.

A periodic timer interrupt posts a Swi that processes the Clock object list. Each entry in the Clock object list has its own period and Clock function. When an object’s period has expired, the Clock function is invoked and the period restarted.

Since there is no limit to the number of Clock objects that can be placed in the list and no way to determine the overhead of each Clock function, the length of time spent servicing all the Clock objects is non-deterministic. As such, servicing the Clock objects in the timer's Hwi thread is impractical. Using a Swi for this function is a relatively (as compared with using a Task) lightweight solution to this problem.

The configuration script and program output are shown after the C code listing. This is the C code for the example:

```c
/*
 * ======== HwiSwiTaskExample.c ========
 */
#include <xdc/std.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/hal/Timer.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Swi.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/knl/Queue.h>
#include <xdc/cfg/global.h>

typedef struct {
    Queue_Elem elem;
    UInt32 timeout;
    UInt32 period;
    Void (*fxn)(UArg);
    UArg arg;
} Clock_Object;

Clock_Object clk1, clk2;
Timer_Handle timer;
Semaphore_Handle sem;
Swi_Handle swi;
Task_Handle task;
Queue_Handle clockQueue;

/* Here on Timer interrupt */
Void hwiFxn(UArg arg)
{
    Swi_post(swi);
}
```
/* Swi thread to handle Timer interrupt */
Void swiFxn(UArg arg1, UArg arg2)
{
    Queue_Elem *elem;
    Clock_Object *obj;

    /* point to first clock object in the clockQueue */
    elem = Queue_next((Queue_Elem *)clockQueue);

    /* service all the Clock Objects in the clockQueue */
    while (elem != (Queue_Elem *)clockQueue) {
        obj = (Clock_Object *)elem;
        /* decrement the timeout counter */
        obj->timeout -= 1;
        /* if period has expired, refresh the timeout */
        * value and invoke the clock func */
        if (obj->timeout == 0) {
            obj->timeout = obj->period;
            (obj->fxn)(obj->arg);
        }
        /* advance to next clock object in clockQueue */
        elem = Queue_next(elem);
    }
}

/* Task thread pends on Semaphore posted by Clock thread */
Void taskFxn(UArg arg1, UArg arg2)
{
    System_printf("In taskFxn pending on Sempahore.\n");
    Semaphore_pend(sem, BIOS_WAIT_FOREVER);
    System_printf("In taskFxn returned from Sempahore.\n");
    System_exit(0);
}

/* First Clock function, invoked every 5 timer interrupts */
Void clk1Fxn(UArg arg)
{
    System_printf("In clk1Fxn, arg = %d.\n", arg);
    clk1.arg++;
}

/* Second Clock function, invoked every 20 timer interrupts */
Void clk2Fxn(UArg sem)
{
    System_printf("In clk2Fxn, posting Semaphore.\n");
    Semaphore_post((Semaphore_Object *)sem);
}

/* main() */
Int main(Int argc, char* argv[])
{
    Timer_Params timerParams;
    Task_Params taskParams;
    Error_Block eb;
Example Using Hwi, Swi, and Task Threads

```c
System_printf("Starting HwiSwiTask example.\n");
Error_init(&eb);
Timer_Params_init(&timerParams);
Task_Params_init(&taskParams);
/* Create a Swi with default priority (15).
 * Swi handler is 'swiFxn' which runs as a Swi thread. */
swi = Swi_create(swiFxn, NULL, &eb);
if (swi == NULL) {
    System_abort("Swi create failed");
}
/* Create a Task with priority 3.
 * Task function is 'taskFxn' which runs as a Task thread. */
taskParams.priority = 3;
task = Task_create(taskFxn, &taskParams, &eb);
if (task == NULL) {
    System_abort("Task create failed");
}
/* Create a binary Semaphore for example task to pend on */
sem = Semaphore_create(0, NULL, &eb);
if (sem == NULL) {
    System_abort("Semaphore create failed");
}
/* Create a Queue to hold the Clock Objects on */
clockQueue = Queue_create(NULL, &eb);
if (clockQueue == NULL) {
    System_abort("Queue create failed");
}
/* setup clk1 to go off every 5 timer interrupts. */
clk1.fxn = clk1Fxn;
clk1.period = 5;
clk1.timeout = 5;
clk1.arg = 1;
/* add the Clock object to the clockQueue */
Queue_put(clockQueue, &clk1.elem);
/* setup clk2 to go off every 20 timer interrupts. */
clk2.fxn = clk2Fxn;
clk2.period = 20;
clk2.timeout = 20;
clk2.arg = (UArg)sem;
/* add the Clock object to the clockQueue */
Queue_put(clockQueue, &clk2.elem);
```
The configuration script is as follows:

```c
/* Configure a periodic interrupt using any available Timer
 * with a 1000 microsecond (1ms) interrupt period.
 * The Timer interrupt will be handled by 'hwiFxn' which
 * will run as a Hwi thread.
 */
timerParams.period = 1000;
timer = Timer_create(Timer_ANY, hwiFxn, &timerParams, &eb);
if (timer == NULL) {
    System_abort("Timer create failed");
}

BIOS_start();

return(0);
```

```c
/* ======== HwiSwiTaskExample.cfg ======== */
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Diags = xdc.useModule('xdc.runtime.Diags');
var Error = xdc.useModule('xdc.runtime.Error');
var Log = xdc.useModule('xdc.runtime.Log');
var LoggerBuf = xdc.useModule('xdc.runtime.LoggerBuf');
var Main = xdc.useModule('xdc.runtime.Main');
var Memory = xdc.useModule('xdc.runtime.Memory');
var SysMin = xdc.useModule('xdc.runtime.SysMin');
var System = xdc.useModule('xdc.runtime.System');
var Text = xdc.useModule('xdc.runtime.Text');
var Timer = xdc.useModule('ti.sysbios.hal.Timer');

var BIOS = xdc.useModule('ti.sysbios.BIOS');
var Clock = xdc.useModule('ti.sysbios.knl.Clock');
var Task = xdc.useModule('ti.sysbios.knl.Task');
var Semaphore = xdc.useModule('ti.sysbios.knl.Semaphore');
var Queue = xdc.useModule('ti.sysbios.knl.Queue');
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');

Program.argSize = 0x0;
System.maxAtexitHandlers = 4;
BIOS.heapSize = 0x2000;

/* System stack size (used by ISRs and Swis) */
Program.stack = 0x1000;

/* Circular buffer size for System_printf() */
SysMin.bufSize = 0x400;
```
The program output is as follows:

```
Starting HwiSwiTask example.
In taskFxn pending on Semaphore.
In clk1Fxn, arg = 1.
In clk1Fxn, arg = 2.
In clk1Fxn, arg = 3.
In clk1Fxn, arg = 4.
In clk2Fxn, posting Semaphore.
In taskFxn returned from Semaphore
```
This chapter describes modules that can be used to synchronize access to shared resources.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.1 Semaphores</td>
<td>107</td>
</tr>
<tr>
<td>4.2 Event Module</td>
<td>113</td>
</tr>
<tr>
<td>4.3 Gates</td>
<td>119</td>
</tr>
<tr>
<td>4.4 Mailboxes</td>
<td>122</td>
</tr>
<tr>
<td>4.5 Queues</td>
<td>124</td>
</tr>
</tbody>
</table>
4.1 Semaphores

SYS/BIOS provides a fundamental set of functions for inter-task synchronization and communication based upon semaphores. Semaphores are often used to coordinate access to a shared resource among a set of competing tasks. The Semaphore module provides functions that manipulate semaphore objects accessed through handles of type Semaphore_Handle. See the video introducing Semaphores for an overview.

Semaphore objects can be declared as either counting or binary semaphores and as either simple (FIFO) or priority-aware semaphores. Semaphores can be used for task synchronization and mutual exclusion. The same APIs are used for both counting and binary semaphores.

By default, semaphores are simple counting semaphores.

Counting semaphores keep an internal count of the number of corresponding resources available. When count is greater than 0, tasks do not block when acquiring a semaphore. The count value for a semaphore is limited only by the size of the 16-bit counter. If Asserts are disabled, the count rolls over with no notification if the count is incremented from the maximum 16 bit value.

To configure a counting semaphore, set the mode parameter as follows:

```
semParams.mode = Semaphore_Mode_COUNTING;
```

Binary semaphores are either available or unavailable. Their value cannot be incremented beyond 1. Thus, they should be used for coordinating access to a shared resource by a maximum of two tasks. Binary semaphores provide better performance than counting semaphores.

To configure a binary semaphore, set the mode parameter as follows:

```
semParams.mode = Semaphore_Mode_BINARY;
```

Tasks wait for simple counting and binary semaphores in FIFO order without regard to the priority of the tasks. Optionally, you can create "priority" semaphores that insert pending tasks into the waiting list before the first task that has a lower priority. As a result, tasks of equal priority pend in FIFO order, but tasks of higher priority are readied before tasks of lower priority.

To configure a counting or binary priority semaphore, set the mode parameter using one of the following constants:

```
semParams.mode = Semaphore_Mode_COUNTING_PRIORITY;
semParams.mode = Semaphore_Mode_BINARY_PRIORITY;
```

Note that using priority semaphores can increase the interrupt latency in the system, since interrupts are disabled while the list of tasks waiting on the semaphore is scanned for the proper insertion point. This is typically about a dozen instructions per waiting task. For example, if you have 10 tasks of higher priority waiting, then all 10 will be checked with interrupts disabled before the new task is entered onto the list.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.
The functions Semaphore_create() and Semaphore_delete() are used to create and delete semaphore objects, respectively, as shown in Example 4-1. You can also create semaphore objects statically.

**Example 4-1 Creating and Deleting a Semaphore**

```c
Semaphore_Handle Semaphore_create(
    Int                count,
    Semaphore_Params   *attrs
    Error_Block        *eb );

Void Semaphore_delete(Semaphore_Handle *sem);
```

The semaphore count is initialized to count when it is created. In general, count is set to the number of resources that the semaphore is synchronizing.

Semaphore_pend() waits for a semaphore. If the semaphore count is greater than 0, Semaphore_pend() simply decrements the count and returns. Otherwise, Semaphore_pend() waits for the semaphore to be posted by Semaphore_post().

The timeout parameter to Semaphore_pend(), as shown in Example 4-2, allows the task to wait until a timeout, to wait indefinitely (BIOS_WAIT_FOREVER), or to not wait at all (BIOS_NO_WAIT). Semaphore_pend()’s return value is used to indicate if the semaphore was acquired successfully.

**Example 4-2 Setting a Timeout with Semaphore_pend()**

```c
Bool Semaphore_pend(
    Semaphore_Handle   sem,
    UInt               timeout);
```

Example 4-3 shows Semaphore_post(), which is used to signal a semaphore. If a task is waiting for the semaphore, Semaphore_post() removes the first task from the semaphore queue and puts it on the ready queue. If no tasks are waiting, Semaphore_post() simply increments the semaphore count and returns.

**Example 4-3 Signaling a Semaphore with Semaphore_post()**

```c
Void Semaphore_post(Semaphore_Handle sem);
```

Calling Semaphore_post() may result in hardware interrupts being re-enabled in specific circumstances. See Section 8.2.4, *Enabling and Disabling Interrupts* for details.
4.1.1 Semaphore Example

Example 4-4 provides sample code for three writer tasks that create unique messages and place them on a list for one reader task. The writer tasks call Semaphore_post() to indicate that another message has been put on the list. The reader task calls Semaphore_pend() to wait for messages. Semaphore_pend() returns only when a message is available on the list. The reader task prints the message using the System_printf() function.

The three writer tasks, a reader task, a semaphore, and a queue in this example program were created statically as follows:

```javascript
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Diags = xdc.useModule('xdc.runtime.Diags');
var Error = xdc.useModule('xdc.runtime.Error');
var Log = xdc.useModule('xdc.runtime.Log');
var LoggerBuf = xdc.useModule('xdc.runtime.LoggerBuf');
var Main = xdc.useModule('xdc.runtime.Main');
var Memory = xdc.useModule('xdc.runtime.Memory');
var SysMin = xdc.useModule('xdc.runtime.SysMin');
var System = xdc.useModule('xdc.runtime.System');
var Text = xdc.useModule('xdc.runtime.Text');

var BIOS = xdc.useModule('ti.sysbios.BIOS');
var Clock = xdc.useModule('ti.sysbios.knl.Clock');
var Task = xdc.useModule('ti.sysbios.knl.Task');
var Semaphore = xdc.useModule('ti.sysbios.knl.Semaphore');
var Hwi = xdc.useModule('ti.sysbios.hl.Hwi');
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');

/* set heap and stack sizes */
BIOS.heapSize = 0x2000;
Program.stack = 0x1000;
SysMin.bufSize = 0x400;

/* set library type */
BIOS.libType = BIOS.LibType_Custom;

/* Set logger for the whole system */
var loggerBufParams = new LoggerBuf.Params();
loggerBufParams.numEntries = 32;
var logger0 = LoggerBuf.create(loggerBufParams);
Defaults.common$.logger = logger0;
Main.common$.diags_INFO = Diags.ALWAYS_ON;

/* Use Semaphore, and Task modules and set global properties */
var Semaphore = xdc.useModule('ti.sysbios.knl.Semaphore');
Program.global.sem = Semaphore.create(0);
var Task = xdc.useModule('ti.sysbios.knl.Task');
Task.idleTaskVitalTaskFlag = false;
```
Since this program employs multiple tasks, a counting semaphore is used to synchronize access to the list. Figure 4-1 provides a view of the results from Example 4-3. Though the three writer tasks are scheduled first, the messages are read as soon as they have been put on the queue, because the reader's task priority is higher than that of the writer.

**Example 4-4 Semaphore Example Using Three Writer Tasks**

```c
/* Statically create reader and writer Tasks */
var reader = Task.create('&reader');
reader.priority = 5;

var writer0 = Task.create('&writer');
writer0.priority = 3;
writer0.arg0 = 0;

var writer1 = Task.create('&writer');
writer1.priority = 3;
writer1.arg0 = 1;

var writer2 = Task.create('&writer');
writer2.priority = 3;
writer2.arg0 = 2;

/* uses Queue module and create two instances statically */
var Queue = xdc.useModule('ti.sysbios.knl.Queue');
Program.global.msgQueue = Queue.create();
Program.global.freeQueue = Queue.create();
```

```c
/* ======== semtest.c ======== */
#include <xdc/std.h>
#include <xdc/runtime/Memory.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/knl/Task.h>
#include <ti/sysbios/knl/Queue.h>

#define NUMMSGS 3    /* number of messages */
#define NUMWRITERS 3 /* number of writer tasks created with */

/* Config Tool */
typedef struct MsgObj {
    Queue_ELEM elem;    /* first field for Queue */
    Int id;             /* writer task id */
    Char val;           /* message value */
} MsgObj, *Msg;

Void reader();
Void writer();
```
/* The following objects are created statically. */
extern Semaphore_Handle sem;
extern Queue_Handle msgQueue;
extern Queue_Handle freeQueue;

/* ======== main ======== */
Int main(Int argc, Char* argv[])
{
    Int i;
    MsgObj *msg;
    Error_Block eb;
    Error_init(&eb);

    msg = (MsgObj *) Memory_alloc(NULL, NUMMSGS * sizeof(MsgObj), 0, &eb);
    if (msg == NULL) {
        System_abort("Memory allocation failed");
    }

    /* Put all messages on freeQueue */
    for (i = 0; i < NUMMSGS; msg++, i++) {
        Queue_put(freeQueue, (Queue_Elem *) msg);
    }
    BIOS_start();
    return(0);
}

/* ======== reader ======== */
Void reader()
{
    Msg msg;
    Int i;
    for (i = 0; i < NUMMSGS * NUMWRITERS; i++) {
        /* Wait for semaphore to be posted by writer(). */
        Semaphore_pend(sem, BIOS_WAIT_FOREVER);

        /* get message */
        msg = Queue_get(msgQueue);
        /* print value */
        System_printf("read '%c' from (%d).\n", msg->val, msg->id);
        /* free msg */
        Queue_put(freeQueue, (Queue_Elem *) msg);
    }
    System_printf("reader done.\n");
}
/ * ======== writer ======== */
Void writer(Int id)
{
    Msg msg;
    Int i;

    for (i = 0; i < NUMMSGS; i++) {
        /* Get msg from the free list. Since reader is higher
           * priority and only blocks on sem, list is never
           * empty. */
        msg = Queue_get(freeQueue);

        /* fill in value */
        msg->id = id;
        msg->val = (i & 0xf) + 'a';
        System_printf("(%d) writing '%c' ...
" , id, msg->val);

        /* put message */
        Queue_put(msgQueue, (Queue_Elem *) msg);

        /* post semaphore */
        Semaphore_post(sem);
    }

    System_printf("writer (%d) done.\n", id);
}
4.2 Event Module

Events provide a means for communicating between and synchronizing threads. They are similar to Semaphores (see Section 4.1), except that they allow you to specify multiple conditions ("events") that must occur before the waiting thread returns.

An Event instance is used with calls to "pend" and "post", just as for a Semaphore. However, calls to Event_pend() additionally specify which events to wait for, and calls to Event_post() specify which events are being posted.

Note: Only a single Task can pend on an Event object at a time.

A single Event instance can manage up to 32 events, each represented by an event ID. Event IDs are simply bit masks that correspond to a unique event managed by the Event object.

Each Event behaves like a binary semaphore. However, see Section 4.2.1 for details about events that are implicitly posted from a Semaphore or Mailbox.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

A call to Event_pend() takes an "andMask" and an "orMask". The andMask consists of the event IDs of all the events that must occur, and the orMask consists of the event IDs of any events of which only one must occur.
As with Semaphores, a call to Event_pend() takes a timeout value and returns 0 if the call times out. If a call to Event_pend() is successful, it returns a mask of the "consumed" events—that is, the events that occurred to satisfy the call to Event_pend(). The task is then responsible for handling ALL of the consumed events.

Only Tasks can call Event_pend(), whereas Hwis, Swis, and other Tasks can all call Event_post().

The Event_pend() prototype is as follows:

```c
UInt Event_pend(Event_Handle event,
                UInt         andMask,
                UInt         orMask,
                UInt         timeout);
```

The Event_post() prototype is as follows:

```c
Void Event_post(Event_Handle event,
                 UInt         eventIds);
```

Calling Event_post() may result in hardware interrupts being re-enabled in specific circumstances. See Section 8.2.4, Enabling and Disabling Interrupts for details.

**Configuration example:** These configuration statements create an event statically. The Event object has an Event_Handle named "myEvent".

```c
var Event = xdc.useModule("ti.sysbios.knl.Event");
Program.global.myEvent = Event.create();
```

**Runtime example:** The following C code creates an Event object with an Event_Handle named "myEvent".

```c
Event_Handle myEvent;
Error_Block eb;
...
Error_init(&eb);

/* Default instance configuration params */
myEvent = Event_create(NULL, &eb);
if (myEvent == NULL) {
    System_abort("Event create failed");
}
```

**Runtime example:** The following C code blocks on an event. It wakes the task only when both events 0 and 6 have occurred. It sets the andMask to enable both Event_Id_00 and Event_Id_06. It sets the orMask to Event_Id_NONE.

```c
Event_pend(myEvent, (Event_Id_00 + Event_Id_06), Event_Id_NONE,
          BIOS_WAIT_FOREVER);
```

**Runtime example:** The following C code has a call to Event_post() to signal which events have occurred. The eventMask should contain the IDs of the events that are being posted.

```c
Event_post(myEvent, Event_Id_00);
```
**Runtime Example:** The following C code example shows a task that provides the background processing required for three Interrupt Service Routines:

```c
Event_Handle myEvent;

main()
{
    ...

    /* create an Event object. All events are binary */
    myEvent = Event_create(NULL, &eb);
    if (myEvent == NULL) {
        System_abort("Event create failed");
    }
}

isr0()
{
    ...
    Event_post(myEvent, Event_Id_00);
    ...
}

isr1()
{
    ...
    Event_post(myEvent, Event_Id_01);
    ...
}

isr2()
{
    ...
    Event_post(myEvent, Event_Id_02);
    ...
}
```
4.2.1 Implicitly Posted Events

In addition to supporting the explicit posting of events through the Event_post() API, some SYS/BIOS objects support implicit posting of events associated with their objects. For example, a Mailbox can be configured to post an associated event whenever a message is available (that is, whenever Mailbox_post() is called) thus allowing a task to block while waiting for a Mailbox message and/or some other event to occur.

Mailbox and Semaphore objects currently support the posting of events associated with their resources becoming available. To enable the ability to post events from Mailbox and Semaphore objects, you must set the supportsEvents property of the Semaphore module to True. For example, the configuration could include the following statement:

```c
Semaphore.supportsEvents = true;
```

SYS/BIOS objects that support implicit event posting must be configured with an event object and event ID when created. You can decide which event ID to associate with the specific resource availability signal (that is, a message available in Mailbox, room available in Mailbox, or Semaphore available).

**Note:** As mentioned earlier, only one Task can pend on an Event object at a time. Consequently, SYS/BIOS objects that are configured for implicit event posting should only be waited on by a single Task at a time.

When Event_pend() is used to acquire a resource from implicitly posting objects, the BIOS_NO_WAIT timeout parameter should be used to subsequently retrieve the resource from the object.

Recall that Events are binary, so upon returning from an Event_pend() all matching events are consumed. If a task is blocked on an Event that is implicitly posted from a Semaphore, the corresponding Event ID is consumed. An immediate call to Event_pend() again on that same Event ID would block.
However, if `Semaphore_pend(sem, BIOS_NO_WAIT)` is called on the corresponding Semaphore prior to calling `Event_pend()`, the matching Event ID in the Event object is internally refreshed prior to returning from `Semaphore_pend()` to match the newly updated Semaphore "count". If the Semaphore count is non-zero after calling `Semaphore_pend()`, the corresponding Event ID is set within the Event object. If the count is zero after calling `Semaphore_pend()`, the corresponding Event ID in the event object is cleared. In this way, the Event object is kept in sync with the Semaphore object that implicitly calls `Event_post()`. But the application must call `Semaphore_pend(sem, BIOS_NO_WAIT)` in order for this synchronization to take place.

Since Mailboxes are built upon Semaphores, the same logic applies to Event objects that register with Mailboxes. Following the return from `Event_pend()` of an implicitly posted Event by either `Mailbox_post()` or `Mailbox_pend()`, the corresponding Event object is refreshed by an intervening call to `Mailbox_pend(mbx, mbuf, BIOS_NO_WAIT)` or `Mailbox_post(mbx, mbuf, BIOS_NO_WAIT)`.

**Runtime example:** The following C code example shows a task processing the messages posted to a Mailbox message as well as performing an ISR’s post-processing requirements.

```c
Event_Handle myEvent;
Mailbox_Handle mbox;

typedef struct msg {
    UInt id;
    Char buf[10];
} msg;

main()
{
    Mailbox_Params mboxParams;
    Error_Block eb;
    Error_init(&eb);

    myEvent = Event_create(NULL, &eb);
    if (myEvent == NULL) {
        System_abort("Event create failed");
    }
```

```c
```
Mailbox_Params_init(&mboxParams);
    mboxParams.readerEvent = myEvent;

    /* Assign Event_Id_00 to Mailbox "not empty" event */
    mboxParams.readerEventId = Event_Id_00;
    mbox = Mailbox_create(sizeof(msg), 50, &mboxParams, &eb);
    if (mbox == NULL) {
        System_abort("Mailbox create failed");
    }

    /* Mailbox_create() sets Mailbox's readerEvent to 
    * counting mode and initial count = 50 */
}

writerTask()
{
    ...
    Mailbox_post(mbox, &msgA, BIOS_WAIT_FOREVER);
    /* implicitly posts Event_Id_00 to myEvent */
    ...
}

isr()
{
    Event_post(myEvent, Event_Id_01);
}

readerTask()
{
    while (TRUE) {/* Wait for either ISR or Mailbox message */
        events = Event_pend(myEvent,
            Event_Id.NONE,      /* andMask = 0 */
            Event_Id_00 + Event_Id_01, /* orMask */
            BIOS_WAIT_FOREVER); /* timeout */
        if (events & Event_Id_00) {
            /* Get the posted message.
            * Mailbox_pend() will not block since Event_pend()
            * has guaranteed that a message is available.
            * Notice that the special BIOS_NO_WAIT
            * parameter tells Mailbox that Event_pend()
            * was used to acquire the available message.
            */
            Mailbox_pend(mbox, &msgB, BIOS_NO_WAIT);
            processMsg(&msgB);
        }
        if (events & Event_Id_01) {
            processISR();
        }
    }
}
4.3 Gates

Gates are devices for preventing concurrent accesses to critical regions of code. The various Gate implementations differ in how they attempt to lock the critical regions.

Threads can be preempted by other threads of higher priority, and some sections of code need to be completed by one thread before they can be executed by another thread. Code that modifies a global variable is a common example of a critical region that may need to be protected by a Gate.

Gates generally work by either disabling some level of preemption such as disabling task switching or even hardware interrupts, or by using a binary semaphore.

All Gate implementations support nesting through the use of a "key".

For Gates that function by disabling preemption, it is possible that multiple threads would call Gate_enter(), but preemption should not be restored until all of the threads have called Gate_leave(). This functionality is provided through the use of a key. A call to Gate_enter() returns a key that must then be passed back to Gate_leave(). Only the outermost call to Gate_enter() returns the correct key for restoring preemption.

As shown in the examples that follow, the actual module name for the implementation is used instead of "Gate" in the function name.

**Runtime example:** The following C code protects a critical region with a Gate. This example uses a GateHwi, which disables and enables interrupts as the locking mechanism.

```c
UInt gateKey;
GateHwi_Handle gateHwi;
GateHwi_Params prms;
Error_Block eb;
...
Error_init(&eb);
GateHwi_Params_init(&prms);

gateHwi = GateHwi_create(&prms, &eb);
if (gateHwi == NULL) {
    System_abort("Gate create failed");
}

/* Simultaneous operations on a global variable by multiple
 * threads could cause problems, so modifications to the global
 * variable are protected with a Gate. */
gateKey = GateHwi_enter(gateHwi);
myGlobalVar = 7;
GateHwi_leave(gateHwi, gateKey);
```

For information about the parameter structure and individual parameters for instances of these modules, see the API Reference Help System described in Section 1.6.1.
4.3.1 Preemption-Based Gate Implementations

The following implementations of gates use some form of preemption disabling:

- `ti.sysbios.gates.GateHwi`
- `ti.sysbios.gates.GateSwi`
- `ti.sysbios.gates.GateTask`

4.3.1.1 GateHwi

GateHwi disables and enables interrupts as the locking mechanism. Such a gate guarantees exclusive access to the CPU. This gate can be used when the critical region is shared by Task, Swi, or Hwi threads.

The duration between the enter and leave should be as short as possible to minimize Hwi latency.

4.3.1.2 GateSwi

GateSwi disables and enables software interrupts as the locking mechanism. This gate can be used when the critical region is shared by Swi or Task threads. This gate cannot be used by a Hwi thread.

The duration between the enter and leave should be as short as possible to minimize Swi latency.

4.3.1.3 GateTask

GateTask disables and enables tasks as the locking mechanism. This gate can be used when the critical region is shared by Task threads. This gate cannot be used by a Hwi or Swi thread.

The duration between the enter and leave should be as short as possible to minimize Task latency.

4.3.2 Semaphore-Based Gate Implementations

The following implementations of gates use a semaphore:

- `ti.sysbios.gates.GateMutex`
- `ti.sysbios.gates.GateMutexPri`

4.3.2.1 GateMutex

GateMutex uses a binary Semaphore as the locking mechanism. Each GateMutex instance has its own unique Semaphore. Because this gate can potentially block, it should not be used a Swi or Hwi thread, and should only be used by Task threads.

4.3.2.2 GateMutexPri

GateMutexPri is a mutex Gate (it can only be held by one thread at a time) that implements "priority inheritance" in order to prevent priority inversion. Priority inversion occurs when a high-priority Task has its priority effectively "inverted" because it is waiting on a Gate held by a lower-priority Task. Issues and solutions for priority inversion are described in Section 4.3.3.

Configuration example: The following example specifies a GateType to be used by HeapMem. (See Section 7.8.2, HeapMem for further discussion.)

```javascript
var GateMutexPri = xdc.useModule('ti.sysbios.gates.GateMutexPri');
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');

HeapMem.common$.gate = GateMutexPri.create();
```
4.3.3 Priority Inversion

The following example shows the problem of priority inversion. A system has three tasks—Low, Med, and High—each with the priority suggested by its name. Task Low runs first and acquires the gate. Task High is scheduled and preempts Low. Task High tries to acquire the gate, and waits on it. Next, task Med is scheduled and preempts task Low. Now task High must wait for both task Med and task Low to finish before it can continue. In this situation, task Low has, in effect, lowered task High's priority to that of Low.

Solution: Priority Inheritance

To guard against priority inversion, GateMutexPri implements priority inheritance. When task High tries to acquire a gate that is owned by task Low, task Low's priority is temporarily raised to that of High, as long as High is waiting on the gate. So, task High "donates" its priority to task Low.

When multiple tasks wait on the gate, the gate owner receives the highest priority of any of the tasks waiting on the gate.

Caveats

Priority inheritance is not a complete guard against priority inversion. Tasks only donate their priority on the call to enter a gate, so if a task has its priority raised while waiting on a gate, that priority is not carried through to the gate owner.

This can occur in situations involving multiple gates. For example, a system has four tasks: VeryLow, Low, Med, and High, each with the priority suggested by its name. Task VeryLow runs first and acquires gate A. Task Low runs next and acquires gate B, then waits on gate A. Task High runs and waits on gate B. Task High has donated its priority to task Low, but Low is blocked on VeryLow, so priority inversion occurs despite the use of the gate. The solution to this problem is to design around it. If gate A may be needed by a high-priority, time-critical task, then it should be a design rule that no task holds this gate for a long time or blocks while holding this gate.

When multiple tasks wait on this gate, they receive the gate in order of priority (higher-priority tasks receive the gate first). This is because the list of tasks waiting on a GateMutexPri is sorted by priority, not FIFO.

Calls to GateMutexPri_enter() may block, so this gate can only be used in the task context.

GateMutexPri has non-deterministic calls because it keeps the list of waiting tasks sorted by priority.

4.3.4 Configuring the SYS/BIOS Gate Type

The application sets the type of gate to be used by calls in the TI RTS library (Chapter 6). The type of gate selected is used to guarantee re-entrancy of the RTS APIs. The BIOS.rtsGateType configuration property controls this behavior. In XGCONF, this property is labeled "C Standard Library Lock".

The type of gate depends on the type of threads that are going to be calling RTS library functions. For example, if both Swi and Task threads are going to be calling the RTS library's System_printf() function, GateSwi should be used. In this case, Hwi threads are not disabled during System_printf() calls from the Swi or Task threads.

If NoLocking is used, the RTS lock is not plugged, and re-entrancy for the TI RTS library calls is not guaranteed. The application can plug the RTS locks directly if it wants.
The types of gates available are listed in Section 4.3.1 and Section 4.3.2. Note that GateTask is not supported as a SYS/BIOS RTS gate type.

- **GateHwi**: Interrupts are disabled and restored to maintain re-entrancy. Use if making any RTS calls from a Hwi.
- **GateSwi**: Swis are disabled and restored to maintain re-entrancy. Use if not making RTS calls from any Hwis but making such calls from Swis.
- **GateMutex**: A single mutex is used to maintain re-entrancy. Use if only making RTS calls from Tasks. Blocks only Tasks that are also trying to execute critical regions of the RTS library.
- **GateMutexPri**: A priority-inheriting mutex is used to maintain re-entrancy. Blocks only Tasks that are also trying to execute critical regions of the RTS library. Raises the priority of the Task that is executing the critical region in the RTS library to the level of the highest priority Task that is blocked by the mutex.

The default RTS Gate Type depends on the type of threading model enabled by other configuration parameters. If BIOS.taskEnabled is true, GateMutex is used. If BIOS.swiEnabled is true and BIOS.taskEnabled is false, GateSwi is used. If both BIOS.swiEnabled and BIOS.taskEnabled are false, xdc.runtime.GateNull is used.

If BIOS.taskEnabled is false, the user should not select GateMutex (or other Task level gates). Similarly, if BIOS.taskEnabled and BIOS.swiEnabled are false, the user should not select GateSwi or the Task level gates.

### 4.4 Mailboxes

The ti.sysbios.knl.Mailbox module provides a set of functions to manage mailboxes. Mailboxes can be used to pass buffers from one task to another on the same processor.

A Mailbox instance can be used by multiple readers and writers.

The Mailbox module copies the buffer to fixed-size internal buffers. The size and number of these buffers are specified when a Mailbox instance is created (or constructed). A copy is done when a buffer is sent via Mailbox_post(). Another copy occurs when the buffer is retrieved via a Mailbox_pend().

Mailbox_create() and Mailbox_delete() are used to create and delete mailboxes, respectively. You can also create mailbox objects statically.

Mailboxes can be used to ensure that the flow of incoming buffers does not exceed the ability of the system to process those buffers. The examples given later in this section illustrate just such a scheme.

You specify the number of internal mailbox buffers and size of each of these buffers when you create a mailbox. Since the size is specified when you create the Mailbox, all buffers sent and received with the Mailbox instance must be of this same size.

```c
Mailbox_Handle Mailbox_create(SizeT bufsize, 
                               UInt numBufs, 
                               Mailbox_Params *params, 
                               Error_Block *eb)

Void Mailbox_delete(Mailbox_Handle *handle);
```

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.
Mailbox_pend() is used to read a buffer from a mailbox. If no buffer is available (that is, the mailbox is empty), Mailbox_pend() blocks. The timeout parameter allows the task to wait until a timeout, to wait indefinitely (BIOS_WAIT_FOREVER), or to not wait at all (BIOS_NO_WAIT). The unit of time is system clock ticks.

```c
Bool Mailbox_pend(Mailbox_Handle handle,
                  Ptr            buf,
                  UInt           timeout);
```

Mailbox_post() is used to post a buffer to the mailbox. If no buffer slots are available (that is, the mailbox is full), Mailbox_post() blocks. The timeout parameter allows the task to wait until a timeout, to wait indefinitely (BIOS_WAIT_FOREVER), or to not wait at all (BIOS_NO_WAIT).

```c
Bool Mailbox_post(Mailbox_Handle handle,
                  Ptr            buf,
                  UInt           timeout);
```

Mailbox provides configuration parameters to allow you to associate events with a mailbox. This allows you to wait on a mailbox message and another event at the same time. Mailbox provides two configuration parameters to support events for the reader(s) of the mailbox—readerEvent and readerEventId. These allow a mailbox reader to use an event object to wait for the mailbox message. Mailbox also provides two configuration parameters for the mailbox writer(s)—writerEvent and writerEventId. These allow mailbox writers to use an event object to wait for room in the mailbox.

Note that the names of these event handles can be misleading. The readerEvent is the Event that a Mailbox reader should pend on, but it is posted by the Mailbox writer within the Mailbox_post() call. The writerEvent is the Event that the Mailbox writer should pend on waiting for the Mailbox to become not full so that it can successfully perform a Mailbox_post() without pending because the Mailbox is full. However, the writerEvent is posted by the Mailbox reader whenever the Mailbox is successful read from (that is, Mailbox_pend() returns TRUE).

When using events, a thread calls Event_pend() and waits on several events. Upon returning from Event_pend(), the thread must call Mailbox_pend() or Mailbox_post()—depending on whether it is a reader or a writer—with a timeout value of BIOS_NO_WAIT. See Section 4.2.1 for a code example that obtains the corresponding Mailbox resource after returning from Event_pend().

Calling Mailbox_post() may result in hardware interrupts being re-enabled in specific circumstances. See Section 8.2.4, Enabling and Disabling Interrupts for details.
4.5 Queues

The ti.sysbios.knl.Queue module provides support for creating lists of objects. A Queue is implemented as a doubly-linked list, so that elements can be inserted or removed from anywhere in the list, and so that Queues do not have a maximum size.

4.5.1 Basic FIFO Operation of a Queue

To add a structure to a Queue, its first field needs to be of type Queue_Elem. The following example shows a structure that can be added to a Queue.

A Queue has a "head", which is the front of the list. Queue_enqueue() adds elements to the back of the list, and Queue_dequeue() removes and returns the element at the head of the list. Together, these functions support a natural FIFO queue.

Run-time example: The following example demonstrates the basic Queue operations—Queue_enqueue() and Queue_dequeue(). It also uses the Queue_empty() function, which returns true when there are no more elements in the Queue.

```c
/* This structure can be added to a Queue because the first field is a Queue_Elem. */
typedef struct Rec {
  Queue_Elem elem;
  Int data;
} Rec;

Queue.Handle myQ;
Rec r1, r2;
Rec* rp;

r1.data = 100;
r2.data = 200;

// No parameters or Error block are needed to create a Queue.
myQ = Queue_create(NULL, NULL);

// Add r1 and r2 to the back of myQ.
Queue_enqueue(myQ, &(r1.elem));
Queue_enqueue(myQ, &(r2.elem));

// Dequeue the records and print their data
while (!Queue_empty(myQ)) {
  // Implicit cast from (Queue_Elem *) to (Rec *)
  rp = Queue_dequeue(myQ);
  System_printf("rec: %d\n", rp->data);
}
```

The example prints:

```
rec: 100
rec: 200
```
4.5.2 Iterating Over a Queue

The Queue module also provides several APIs for looping over a Queue. Queue_head() returns the element at the front of the Queue (without removing it) and Queue_next() and Queue_prev() return the next and previous elements in a Queue, respectively.

Run-time example: The following example demonstrates one way to iterate over a Queue once from beginning to end. In this example, "myQ" is a Queue_Handle.

```c
Queue_Elem *elem;
for (elem = Queue_head(myQ); elem != (Queue_Elem *)myQ;
    elem = Queue_next(elem)) {
    ...
}
```

4.5.3 Inserting and Removing Queue Elements

Elements can also be inserted or removed from anywhere in the middle of a Queue using Queue_insert() and Queue_remove(). Queue_insert() inserts an element in front of the specified element, and Queue_remove() removes the specified element from whatever Queue it is in. Note that Queue does not provide any APIs for inserting or removing elements at a given index in the Queue.

Run-time example: The following example demonstrates Queue_insert() and Queue_remove().

```c
Queue_enqueue(myQ, &(r1.elem));
/* Insert r2 in front of r1 in the Queue. */
Queue_insert(&(r1.elem), &(r2.elem));

/* Remove r1 from the Queue. Note that Queue_remove() does not * require a handle to myQ. */
Queue_remove(&(r1.elem));
```

4.5.4 Atomic Queue Operations

Queues are commonly shared across multiple threads in the system, which might lead to concurrent modifications of the Queue by different threads, which would corrupt the Queue. The Queue APIs discussed above do not protect against this. However, Queue provides two "atomic" APIs, which disable interrupts before operating on the Queue. These APIs are Queue_get(), which is the atomic version of Queue_dequeue(), and Queue_put(), which is the atomic version of Queue_enqueue().
This chapter describes modules that can be used for timing purposes.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.1 Overview of Timing Services</td>
<td>127</td>
</tr>
<tr>
<td>5.2 Clock</td>
<td>127</td>
</tr>
<tr>
<td>5.3 Timer Module</td>
<td>130</td>
</tr>
<tr>
<td>5.4 Seconds Module</td>
<td>131</td>
</tr>
<tr>
<td>5.5 Timestamp Module</td>
<td>132</td>
</tr>
</tbody>
</table>
5.1 Overview of Timing Services

Several modules are involved in timekeeping and clock-related services within SYS/BIOS and XDCtools:

- **The ti.sysbios.knl.Clock module** is responsible for the periodic system tick that the kernel uses to keep track of time. All SYS/BIOS APIs that expect a timeout parameter interpret the timeout in terms of Clock ticks. The Clock module is used to schedule functions that run at intervals specified in clock ticks. By default, the Clock module uses the hal.Timer module to get a hardware-based tick. Alternately, the Clock module can be configured to use an application-provided tick source. See Section 5.2 for details. (The Clock module replaces both the CLK and PRD modules in earlier versions of DSP/BIOS.)

- **The ti.sysbios.hal.Timer module** provides a standard interface for using timer peripherals. It hides any target/device-specific characteristics of the timer peripherals. Target/device-specific properties for timers are supported by the ti.sysbios.family.xxx.Timer modules (for example, ti.sysbios.family.c64.Timer). You can use the Timer module to select a timer that calls a tickFxn when the timer expires. See Section 5.3 and Section 8.3 for details.

- **The ti.sysbios.hal.Seconds module** provides a means for maintaining the current time and date, as defined by the number of seconds since 1970 (the Unix epoch). This module generates a custom time() function that calls Seconds_get(), overriding the C standard library’s time() function. See Section 5.4 for details.

- **The xdc.runtime.Timestamp module** provides simple timestamping services for benchmarking code and adding timestamps to logs. This module uses a target/device-specific TimestampProvider in SYS/BIOS to control how timestamping is implemented. See Section 5.5 for details.

See the video introducing Timers and Clocks for an overview.

5.2 Clock

The ti.sysbios.knl.Clock module is responsible for the periodic system tick that the kernel uses to keep track of time. All SYS/BIOS APIs that expect a timeout parameter interpret the timeout in terms of Clock ticks.

The Clock module, by default, uses the ti.sysbios.hal.Timer module to create a timer to generate the system tick, which is basically a periodic call to Clock_tick(). See Section 5.3 for more about the Timer module.

The Clock module can be configured not to use the timer with either of the following configuration statements:

```
    ti.sysbios.knl.Clock.tickSource = Clock.TickSource_USER
    or
    ti.sysbios.knl.Clock.tickSource = Clock.TickSource_NULL
```

The period for the system tick is set by the configuration parameter Clock.tickPeriod. This is set in microseconds.

The Clock_tick() and the tick period are used as follows:

- **If the tickSource is Clock.TickSource_TIMER** (the default), Clock uses ti.sysbios.hal.Timer to create a timer to generate the system tick, which is basically a periodic call to Clock_tick(). Clock uses Clock.tickPeriod to create the timer. Clock.timerId can be changed to make Clock use a different timer.
• If the tickSource is Clock.TickSource_USER, then your application must call Clock_tick() from a user interrupt and set the tickPeriod to the approximate frequency of the user interrupt in microseconds.

• If the tickSource is Clock.TickSource_NULL, you cannot call any SYS/BIOS APIs with a timeout value and cannot call any Clock APIs. You can still use the Task module but you cannot call APIs that require a timeout, for example, Task_sleep(). Clock.tickPeriod values is not valid in this configuration.

Clock_getTicks() gets the number of Clock ticks that have occurred since startup. The value returned wraps back to zero after it reaches the maximum value that can be stored in 32 bits.

The Clock module provides APIs to start, stop and reconfigure the tick. These APIs allow you to make frequency changes at runtime. These three APIs are not reentrant and gates need to be used to protect them.

• Clock_tickStop() stops the timer used to generate the Clock tick by calling Timer_stop().

• Clock_tickReconfig() calls Timer_setPeriodMicroseconds() internally to reconfigure the timer. Clock_tickReconfig() fails if the timer cannot support Clock.tickPeriod at the current CPU frequency.

• Clock_tickStart() restarts the timer used to generate the Clock tick by calling Timer_start().

The Clock module lets you create Clock object instances, which reference functions that run when a timeout value specified in Clock ticks expires.

All Clock functions run in the context of a Swi. That is, the Clock module automatically creates a Swi for its use and runs the Clock functions within that Swi. The priority of the Swi used by Clock can be changed by configuring Clock.swiPriority.

You can dynamically create clock instances using Clock_create(). Clock instances can be "one-shot" or continuous. You can start a clock instance when it is created or start it later by calling Clock_start(). This is controlled by the startFlag configuration parameter. Clock_create() can be called only from the context of a Task or the main() function.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

A function and a non-zero timeout value are required arguments to Clock_create(). The function is called when the timeout expires. The timeout value is used to compute the first expiration time. For one-shot Clock instances, the timeout value used to compute the single expiration time, and the period is zero. For periodic Clock instances, the timeout value is used to compute the first expiration time; the period value (part of the params) is used after the first expiration.
Clock instances (both one-shot and periodic) can be stopped and restarted by calling `Clock_start()` and `Clock_stop()`. Notice that while `Clock_tickStop()` stops the timer used to generate the Clock tick, `Clock_stop()` stops only one instance of a clock object. The expiration value is recomputed when you call `Clock_start()`. APIs that start or stop a Clock instance—`Clock_start()` and `Clock_stop()`—can be called in any context except program startup before `main()` begins.

The Clock module provides the `Clock_setPeriod()`, `Clock_setTimeout()`, and `Clock_setFunc()` APIs to modify Clock instance properties for Clock instances that have been stopped.

**Runtime example:** This C example shows how to create a Clock instance. This instance is dynamic (runs repeatedly) and starts automatically. It runs the `myHandler` function every 5 ticks. A user argument (UArg) is passed to the function.

```c
Clock_Params clockParams;
Clock_Handle myClock;
Error_Block eb;
...

Error_init(&eb);
Clock_Params_init(&clockParams);
clockParams.period = 5;
clockParams.startFlag = TRUE;
clockParams.arg = (UArg)0x5555;
myClock = Clock_create(myHandler1, 5, &clockParams, &eb);
if (myClock == NULL) {
    System_abort("Clock create failed");
}
```

**Table 5–1. Timeline for One-shot and Continuous Clocks**

<table>
<thead>
<tr>
<th>Time</th>
<th>One-shot Clock</th>
<th>Continuous Clock</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Func runs</td>
<td>Func runs</td>
</tr>
<tr>
<td></td>
<td>timeout</td>
<td>period</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Clock created and started with `startFlag` or `Clock_start()`
Configuration example: This example configuration script creates a Clock instance with the same properties as the previous example.

```javascript
var Clock = xdc.useModule('ti.sysbios.knl.Clock');
var clockParams = new Clock.Params();
clockParams.period = 5;
clockParams.startFlag = true;
clockParams.arg = (UArg)0x5555;
Program.global.clockInst1 = Clock.create("&myHandler1", 5, clockParams);
```

Runtime example: This C example uses some of the Clock APIs to print messages about how long a Task sleeps.

```c
Uint32 time1, time2;
...
System_printf("task going to sleep for 10 ticks... \n");
time1 = Clock_getTicks();
Task_sleep(10);

time2 = Clock_getTicks();
System_printf("...awake! Delta time is: %lu\n", (ULong) (time2 - time1));
```

Runtime example: This C example uses some of the Clock APIs to lower the Clock module frequency.

```c
BIOS_getCpuFreq(&cpuFreq);
cpuFreq.lo = cpuFreq.lo / 2;
BIOS_setCpuFreq(&cpuFreq);

key = Hwi_disable();
Clock_tickStop();
Clock_tickReconfig();
Clock_tickStart();
Hwi_restore(key);
```

### 5.3 Timer Module

The ti.sysbios.hal.Timer module presents a standard interface for using the timer peripherals. This module is described in detail in [Section 8.3](#) because it is part of the Hardware Abstraction Layer (HAL) package.

You can use this module to create a timer (that is, to mark a timer for use) and configure it to call a tickFxn when the timer expires. Use this module only if you do not need to do any custom configuration of the timer peripheral.

The timer can be configured as a one-shot or a continuous mode timer. The period can be specified in timer counts or microseconds.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in [Section 1.6.1](#).
5.4 Seconds Module

The ti.sysbios.hal.Seconds module provides a way to set and get the number of seconds elapsed since Jan 1 00:00:00 GMT 1970 (the Unix epoch). The Seconds module maintains the time through a device-specific Seconds delegate, if available.

If a device-specific Seconds module is not available, the ti.sysbios.hal.SecondsClock module is used as the Seconds delegate. SecondsClock internally uses the Clock module to periodically increment the seconds count. For example, these configuration commands cause the Seconds module to use the ti.sysbios.hal.SecondsClock module:

```c
Seconds = xdc.useModule('ti.sysbios.hal.Seconds');
var SecondsProxy = xdc.useModule('ti.sysbios.hal.SecondsClock');
```

The Seconds module APIs are:

```c
Void Seconds_set(UInt32 seconds);
UInt32 Seconds_get(Void);
```

An application should call Seconds_set() to initialize the seconds count. Seconds_set() can be called again, if needed, to update or reset the seconds count. An application must call Seconds_set() at least once before any calls to Seconds_get(). Otherwise, the result returned by Seconds_get() is meaningless. The Seconds_set() function is non-reentrant.

The Seconds module includes a time() function that calls Seconds_get(). This overrides the C Standard Library time() function. You can use this time() function in conjunction with other time functions in the C Standard header file, time.h, to view the current date and time in a readable format.

**Examples**

This example initializes the Seconds module, sets the date, gets the current date, and displays the current time and date in human readable format:

```c
#include <time.h>
#include <ti/sysbios/hal/Seconds.h>

UInt32 t;
time_t tl;
struct tm *ltm;
char *curTime;
/* set to today’s date in seconds since Jan 1, 1970 */
Seconds_set(1412800000); /* Wed, 08 Oct 2014 20:26:40 GMT */
/* retrieve current time relative to Jan 1, 1970 */
t = Seconds_get();
/* Use overridden time() function to get the current time.
* Use standard C RTS library functions with return from time().
* Assumes Seconds_set() has been called as above */
t1 = time(NULL);
ltm = localtime(&t1);
curTime = asctime(ltm);
System_printf("Time(GMT): %s\n", curTime);
```
Note: For some compiler run-time libraries, including TI's, the time() function returns the number of seconds elapsed since Jan 1, 1900. In this case, the Seconds module's time() function adds an offset to the value returned by Seconds_get(), in order to be consistent with other APIs in the run-time support library.

5.5 Timestamp Module

The xdc.runtime.Timestamp module, as the name suggests, provides timestamping services. The Timestamp module can be used for benchmarking code and adding timestamps to logs. Calls to the Timestamp module function are forwarded to a platform-specific TimestampProvider implementation.

The package path to the Timestamp module is xdc.runtime.Timestamp, so SYS/BIOS applications should contain the following #include statement:

```
#include <xdc/runtime/Timestamp.h>
```

Configuration files (*.cfg) should enable this module as follows:

```markdown
var Timestamp = xdc.useModule('xdc.runtime.Timestamp');
```

The following Timestamp module APIs are useful in SYS/BIOS applications:

- `Timestamp_get32()` — Return a 32-bit timestamp.
- `Timestamp_get64()` — Return a 64-bit timestamp if 64-bit resolution is supported by the target.
- `Timestamp_getFreq()` — Get the timestamp timer’s frequency in Hz. You can use this function to convert a timestamp value into units of real time.

If you want a platform-independent version of Timestamp, you can use the TimestampStd modules, which uses the ANSI C clock() function.

Platform-specific TimestampProvider modules are located in the ti.sysbios.family package. For example, ti.sysbios.family.msp430.TimestampProvider and ti.sysbios.family.arm.m3.TimestampProvider. Most TimestampProvider modules have configuration parameters that you can use to control the hardware clock source and the behavior if the timestamp counter overflows.

This example calculates the factor needed to correlate Timestamp delta to the CPU cycles:

```c
Types_FreqHz freq1;   /* Timestamp frequency */
Types_FreqHz freq2;   /* BIOS frequency */
Float factor;  /* Clock ratio cpu/timestamp */

Timestamp_getFreq(&freq1);
BIOS_getCpuFreq(&freq2);
factor = (Float)freq2.lo / freq1.lo;

System_printf("%lu\t%lu\t%lu\t Timestamp Freq, BIOS Freq, Factor\n",
    freq1.lo, freq2.lo, (UInt32) factor);
```
This chapter describes modules that provide APIs for some basic support features and configuration parameters that manage overall application behavior.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>6.1 Modules for Application Support and Management</td>
<td>134</td>
</tr>
<tr>
<td>6.2 BIOS Module</td>
<td>134</td>
</tr>
<tr>
<td>6.3 System Module</td>
<td>135</td>
</tr>
<tr>
<td>6.4 Program Module</td>
<td>137</td>
</tr>
<tr>
<td>6.5 Startup Module</td>
<td>138</td>
</tr>
<tr>
<td>6.6 Reset Module</td>
<td>138</td>
</tr>
<tr>
<td>6.7 Error Module</td>
<td>139</td>
</tr>
<tr>
<td>6.8 Text Module</td>
<td>140</td>
</tr>
</tbody>
</table>
6.1 Modules for Application Support and Management

SYS/BIOS and XDCtools provide a number of modules with functions intended to be used to support and manage overall application behavior. The following modules provide APIs and configuration settings that fall into this category:

- **BIOS Module (ti.sysbios.BIOS):** Responsible for SYS/BIOS startup and global parameter maintenance. See Section 6.2.
- **System (xdc.runtime.System):** Provides low-level "system" services. See Section 6.3.
- **SysMin (xdc.runtime.SysMin):** Recommended proxy for System module. See Section 6.3.1.
- **SysCallback (xdc.runtime.SysCallback):** Proxy for custom System behavior. See Section 6.3.2.
- **Program (xdc.cfg.Program):** Acts as the root of the configuration namespace. See Section 6.4.
- **Startup (xdc.runtime.Startup):** Manages application startup and initialization. See Section 6.5.
- **Reset (xdc.runtime.Reset):** Allows target-specific reset functions to be run immediately after system reset. See Section 6.6.
- **Error (xdc.runtime.Error):** Provides mechanisms for handling errors. See Section 6.7.
- **Memory (xdc.runtime.Memory):** Manages memory use. See Chapter 7.

6.2 BIOS Module

The BIOS module is responsible for setting up global parameters for use by SYS/BIOS and for performing the SYS/BIOS startup sequence. See Section 3.1 for details about the startup sequence.

The package path to the BIOS module is ti.sysbios.BIOS, so SYS/BIOS applications should contain the following #include statement:

```c
#include <ti/sysbios/BIOS.h>
```

Configuration files (*.cfg) should enable this module as follows:

```c
var BIOS = xdc.useModule('ti.sysbios.BIOS');
```

The following BIOS module APIs are useful in SYS/BIOS applications:

- **BIOS_start()** — An application's main() function must call this function as the final statement to be executed before giving control to the SYS/BIOS thread scheduler. This call should be performed after all initialization required by the application has been performed. This function performs any remaining SYS/BIOS initialization and then transfers control to the highest priority Task that is ready to run. If Tasks are not enabled, control is transferred directly to the Idle Loop. The BIOS_start() function does not return.

- **BIOS_getCpuFreq()** — This function returns the CPU frequency in Hz. See Chapter 5 for information about timing. An example that uses this function is provided in Section 5.2.

- **BIOS_setCpuFreq()** — This function sets the CPU frequency in Hz. See Chapter 5 for information about timing. An example that uses this function is provided in Section 5.2.

- **BIOS_exit()** — Call this function when you want a SYS/BIOS application to terminate. BIOS_exit() does not return to the function that called BIOS_start(). Instead, BIOS_exit() calls System_exit() and aborts. All functions bound via System_atexit() or the ANSI C Standard Library atexit() function are
then executed. Calling BIOS_exit() is recommended over calling System_exit() for SYS/BIOS applications, because BIOS_exit() performs internal cleanup before calling System_exit(). If an application needs to terminate with an error condition, it should call System_abort() or System_abortSpin().

- BIOS_getThreadType() — This function returns the type of thread from which this function call is made. The available thread types are BIOS_ThreadType_Hwi, BIOS_ThreadType_Swi, BIOS_ThreadType_Task, and BIOS_ThreadType_Main.

The BIOS module defines the BIOS_WAIT_FOREVER and BIOS_NO_WAIT constants, which can be used with APIs that have a timeout argument.

The BIOS module provides several configuration parameters that control global SYS/BIOS behavior. For example, BIOS.heapSize and BIOS.heapSection are described in Section 7.7.2. The BIOS.libType configuration parameter selected which version of the SYS/BIOS libraries to use in the build, and is described in Section 2.4.5. Various ways to reduce the size of a SYS/BIOS application by setting BIOS module configuration parameters are shown in Appendix C and Appendix D.

See the API Reference help (described in Section 1.6.1) for the ti.sysbios.BIOS module for details about the APIs and configuration parameters.

6.3 System Module

The System module provides basic low-level "system" services, such as character output, printf-like output, and exit handling.

The package path to the System module is xdc.runtime.System, so SYS/BIOS applications should contain the following #include statement:

```
#include <xdc/runtime/System.h>
```

Configuration files (*.cfg) should enable this module as follows:

```
var System = xdc.useModule('xdc.runtime.System');
```

The following System module APIs are useful in SYS/BIOS applications:

- System_printf() — Several functions similar to printf are provided. These include System_putstr(), System_sprintf(), System_vprintf(), System_aprintf() (arguments of type IArg), and System_vsnprintf() (print a specified number of characters to a character buffer using a varargs list to pass the arguments).

We strongly recommend that SYS/BIOS applications call System_printf() and its related functions in place of the standard printf() function. The System module provides familiar printf-like functionality but with fewer options. The memory footprint is much smaller than traditional printf(). System_printf() allows users to specify to handling of the character output using a System provider (see Section 6.3.1 and Section 6.3.2).

The System module print functions are also recommended over Log module print functions, because the System module functions are easier to use in many cases. The Log module is intended for use with host instrumentation tools, and it has more options to disable/enable logging. See information about UIA, which uses the Log module, in the System Analyzer User's Guide (SPRUH43) for details.
• **System_abort()** — Your application can call this function when it needs to abort abnormally and return an error message. This function allows you to return a string describing the error that occurred. When this function is called, the System gate is entered, the SupportProxy’s abort function is called, and then System.abortFxnx is called. No exit functions bound via System_atexit() or the ANSI C Standard Library atexit() functions are called.

• **System_flush()** — This function sends any buffered output characters to the output device. It also issues a break point to the IDE. Because this call halts the target, calling it can affect the details of real-time execution. The destination for the output characters is determined by the SupportProxy module.

• **System_exit()** — It is recommended that SYS/BIOS applications call BIOS_exit() instead of the System_exit() function. The BIOS_exit() function performs internal cleanup before calling System_exit(). The System_exit() may be called from a Task, but cannot be called from a Swi or Hwi.

• **System_atexit()** — Call this function to add an exit handler to the internal stack of functions to be executed when System_exit() is called. The System.maxAtexitHandlers configuration property controls how many exit handlers can be stacked. The default is 8.

The System module uses a support proxy module to implement the low-level services needed. The implementation to use is specified by the System.SupportProxy configuration parameter. The default is the SysMin module (Section 6.3.1). Another option is the SysCallback module (Section 6.3.2).

The System module is gated. Other modules can use its gate via the Gate_enterSystem() and Gate_leaveSystem() functions.

**Exit Functions**

By default, when an application exits with no error condition, the System module calls its standard exit function, which in turn calls the ANSI C Standard exit() function. If you would like your application to spin indefinitely, so that you can debug the application’s state, add the following statement to your configuration:

```c
System.exitFxnx = System.exitSpin;
```

Configuring this "spin" function also reduces the code size of your application. The System_exitSpin() API should not be called directly by applications.

You can also use the System_atexit() function in C code to specify multiple exit handlers at runtime. The prototype for a custom exit handler is:

```c
typedef Void (*System_AtexitHandler)(Int);
```

**Abort Functions**

By default, when an application aborts due to an error condition, the System module calls its standard abort function, which in turn calls the ANSI C Standard abort() function. If you would like your application to spin indefinitely, so that you can debug the application’s state, add the following statement to your configuration:

```c
System.abortFxnx = System.abortSpin;
```

Configuring this "spin" function also reduces the code size of your application. The System_abortSpin() API should not be called directly by applications.

The prototype for a custom abort handler is:

```c
typedef Void (*System_AbortFxnx)();
```
6.3.1  **SysMin Module**

The SysMin module is the SupportProxy module used by most SYS/BIOS examples and templates. This module provides implementations of the functions required for a System SupportProxy. This includes functions to flush buffered characters, output a single character, and perform exit and abort actions.

The SysMin module is recommended for most applications, because it places the characters into a circular buffer that the Runtime Object View (ROV) tool knows how to find and present.

The module maintains an internal circular buffer on the target to store the "output" characters. When the buffer is full, data is over-written. When a function is called that flushes the buffer, characters in the internal circular buffer are "output" using the configured outputFxn. Unless you provide a custom outputFxn, on TI targets the HOSTwrite() function in the TI C Run Time Support library is used to output the character buffer. On non-TI targets, the ANSI C Standard Library function fwrite() is used.

An application should not call SysMin functions directly, but the SysMin module does provide configuration parameters to control the buffer site used to store output internally and the memory section that contains this buffer. Configuration files (*.cfg) can enable this module as follows:

```javascript
var SysMin = xdc.useModule('xdc.runtime.SysMin');
```

6.3.2  **SysCallback Module**

The SysCallback module is an alternative to the SysMin proxy module. It requires you to provide custom functions to handle abort, exit, flush, putch, and ready actions. Use this module if you need to customize the output behavior of your application.

6.4  **Program Module**

The Program module acts as the "root" for the configuration namespace. It is used in configurations, but does not provide any C APIs. See Section 2.3.10 for more about the configuration namespace.

Your configuration file does not need a useModule() statement to enable this module.

The Program module provides configuration parameters that control the run-time memory sizes. These include Program.stack, which controls the size of the application's stack, which is separate from the stacks used by individual Tasks.

The Program module also provides configuration parameters that control the contents of the auto-generated linker command file. These include the Program.sectMap[] array (see Section 7.3.2) and the Program.sectionsExclude parameter (see Section 7.3.4).

See the API Reference help (described in Section 1.6.1) for the xdc.cfg.Program module for details about the APIs and configuration parameters.
6.5 Startup Module

The Startup module manages the very early startup initialization that occurs before C's main() function is invoked. This initialization typically consists of setting hardware specific registers that control watchdog timers, access to memory, cache settings, clock speeds, etc.

In addition to configuration parameters that allow the user to add custom startup functions, this module also provides services that allow modules to automatically add initialization functions to the startup sequence.

Configuration files (*.cfg) that configure the Startup module should enable this module as follows:

```javascript
var Startup = xdc.useModule('xdc.runtime.Startup');
```

See Section 3.1 for an overview of the startup process, including the sequence in which functions configured for the Startup module are run and an example configuration script excerpt that installs a user-supplied startup function at every possible control point in the startup sequence.

See the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html) for target-specific details about the startup sequence.

6.6 Reset Module

The Reset module allows you to define an initial reset function by configuring the Reset.fxns[] array property. Reset functions are called as early as possible in the application startup and are intended for platform-specific hardware initialization.

Note that only certain target families perform a device reset before running a program. As a result, the reset function is not supported on all platforms. Do not place code that you intend to be portable in this function. Instead, use the Startup module to define cross-platform application startup functions.

Configuration files (*.cfg) that configure the Reset module should enable this module as follows:

```javascript
var Reset = xdc.useModule('xdc.runtime.Reset');
```

The Reset module allows you to configure an array of startup functions, which allows you to provide separate functions for the startup activities of various peripherals.

See Section 3.1 for an overview of the startup process, including the sequence in which functions configured for the Reset module are run and an example configuration script excerpt that installs a user-supplied Reset function.
6.7 Error Module

The Error module provides mechanisms for raising, checking, and handling errors in a program.

The package path to the Error module is xdc.runtime.Error, so SYS/BIOS applications that use Error APIs should contain the following #include statement:

```c
#include <xdc/runtime/Error.h>
```

Configuration files (*.cfg) that configure the Error module should enable this module as follows:

```c
var Error = xdc.useModule('xdc.runtime.Error');
```

A number of SYS/BIOS APIs—particularly those that create objects and allocate memory—have an argument that expects an Error_Block. Section 9.3 describes error blocks and several APIs that manage them, including Error_init() and Error_check().

The following Error module APIs may be useful in your SYS/BIOS applications:

- **Error_init()** — Puts an error block into its initial state.
- **Error_check()** — Returns true if an error was raised.
- **Error_getData()** — Gets an error’s argument list.
- **Error_getMsg()** — Gets an error’s "printf" format string.
- **Error_print()** — Prints an error using System_printf().
- **Error_raise()** — Raises an error.

This example uses Error APIs in a call to System_printf():

```c
System_printf(Error_getMsg(eb),
              Error_getData(eb)->arg[0],
              Error_getData(eb)->arg[1]);
```

This example raises an error in response to an error condition:

```c
if (val % 2) {
    Error_raise(eb, Error_E_generic, "Value is not a multiple of 2", 0);
}
```

You can configure how SYS/BIOS applications respond to raised errors with the Error.policy, Error.policyFxnn, and Error.raiseHook configuration parameters.

By default, the Error.policy is to return errors to the calling function (Error_UNWIND). If the Error.policy is Error_TERMINATE, all raised errors are fatal and calls to Error_raise() do not return to the caller.

By default, the Error.policyFxnn is Error_policyDefault(), which processes the error and logs it before returning to the caller or aborting, depending on the Error.policy. Alternately, you can use Error_policySpin(), which simply loops infinitely, to minimize the target footprint.

You can specify an Error.raiseHook function to be called whenever an error is raised, even if the Error.policy is TERMINATE. By default, this function is set to Error_print(), which causes the error to be formatted and output by System_printf(). Setting this configuration parameter to null indicates that no function hook should be called.
6.8   Text Module

The Text module efficiently manages a collection of strings with common substrings. Collections with a high degree of commonality are stored in much less space than as ordinary table of independent strings. The total space available for the compressed representation of text strings is limited to 64K characters.

To further save space, the "compressed" representation need not even be loaded in the target's memory. By default, text strings are loaded into target memory. You can prevent this by setting the Text.isLoaded configuration property to false as shown in Appendix D.

You can use the Text.isLoaded Boolean constant at run-time to control the format string passed to System_printf() functions. For example:

```c
if (Text_isLoaded) {
    System_asprintf(tempStr, fmt, a1, a2, a3, a4, a5, a6, a7, a8);
} else {
    System_asprintf(tempStr, "{evt: fmt=%p, args=[0x%x, 0x%x ...]}", fmt, a1, a2);
}
```

The package path to the Text module is xdc.runtime.Text, so SYS/BIOS applications that use Text APIs and constants should contain the following #include statement:

```c
#include <xdc/runtime/Text.h>
```

Configuration files (*.cfg) that configure the Text module should enable this module as follows:

```c
var Text = xdc.useModule('xdc.runtime.Text');
```
This chapter describes issues related to memory use in SYS/BIOS.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>7.1 Background</td>
<td>142</td>
</tr>
<tr>
<td>7.2 Memory Map</td>
<td>143</td>
</tr>
<tr>
<td>7.3 Placing Sections into Memory Segments</td>
<td>148</td>
</tr>
<tr>
<td>7.4 Sections and Memory Mapping for MSP430, Stellaris M3, and C28x</td>
<td>152</td>
</tr>
<tr>
<td>7.5 Stacks</td>
<td>152</td>
</tr>
<tr>
<td>7.6 Cache Configuration</td>
<td>154</td>
</tr>
<tr>
<td>7.7 Dynamic Memory Allocation</td>
<td>155</td>
</tr>
<tr>
<td>7.8 Heap Implementations</td>
<td>158</td>
</tr>
</tbody>
</table>
7.1 Background

This chapter deals with the configuration of static memory (that is, memory mapping and section placement), caches, and stacks. It also provides information on dynamic memory allocation (allocating and freeing memory at runtime).

Static memory configuration relates to the "memory map" available to the executable and the placement of code and data into the memory map. The memory map is made up of internal memory regions that exist within the CPU and external memory regions located on the hardware board. See Section 7.2 for details about the memory map.

Code and data are placed in memory regions by the linker using the linker command file. The linker command file specifies a memory map. For each memory region, the linker command file specifies the origin or base address, length and attributes (read, write, and execute). A memory region specified in the linker command file is also called a "memory segment".

The following is a memory map specification from a linker command file:

```
MEMORY {
  IRAM (RWX) : org = 0x800000, len = 0x200000
  DDR : org = 0x80000000, len = 0x10000000
}
```

The following example shows section placement, as specified by the SECTIONS portion of the linker command file. The compiler produces some well-known sections for placements of various types of code and data, for example: .text, .switch, .bss, .far, .cinit, and .const. For details, see the appropriate compiler user's guide.

```
SECTIONS {
  .text: load >> DDR
  .switch: load >> DDR
  .stack: load > DDR
  .vecs: load >> DDR
  .args: load > DDR
  .sysmem: load > DDR
  .far: load >> DDR
  .data: load >> DDR
  .cinit: load > DDR
  .bss: load > DDR
  .const: load > DDR
  .pinit: load > DDR
  .cio: load >> DDR
}
```

The linker places "memory sections" (such as .text and .cinit) into "memory segments" (such as IRAM) as specified by SECTIONS portion of the linker command file. See Section 7.3 for details about section placement.

Section 7.2 discusses the memory map for SYS/BIOS applications. (MSP430 users should see Section 7.4 instead.)

Section 7.3 discusses section placement in a SYS/BIOS application. (MSP430 users should see Section 7.4 instead.)

Section 7.5 discusses stacks, including how to configure the system stack and task stacks.
Section 7.6 covers cache configuration specific to the C6000 and cache runtime APIs.

Section 7.7 also discusses dynamic memory allocation. Runtime code can allocate and free memory from a "heap," which is a memory pool that has been set aside and managed for the purpose of dynamic memory allocation.

Various heap implementations are described in Section 7.8.

7.2 Memory Map

Note: If you are using the MSP430, see Section 7.4 instead. This section does not apply to the MSP430.

The memory map for an executable is determined by the device (which has internal memory) and the hardware board (which has external memory).

When you create a CCS project for an application that uses SYS/BIOS, you select a "platform" on the RTSC Configuration Settings page. The memory map for on-device and external memory is determined by this platform. The platform also sets the clock speed and specifies memory section placement.

You select a platform when you create a new project or change the project build properties, not when you create a configuration file.

Executables that need different memory maps must use different platforms even if they run on the same type of board.

The platform is tied to a particular device (CPU) and gets the internal memory map from the device—for example, IRAM and FLASH. The platform also contains the external memory specifications and cache settings. The internal and external memory segments together form the memory map.

7.2.1 Choosing an Available Platform

Before building a SYS/BIOS 6.x executable, you need to select the hardware board you will be using. You do this by selecting a platform either when you create a CCS project or in the RTSC tab of the project’s CCS General properties. The Platform field provides a drop-down list of all available platforms that match your Target; these items represent various evaluation boards available for your chosen device (CPU).
To view the memory map for your platform, you can open the platform wizard by choosing Tools > RTSC Tools > Platform > Edit/View. Select the packages repository in your SYS/BIOS installation (that is, your <bios_install_dir>\packages directory). Then, choose the Package you are using and click Next.

In most cases, you begin application development using one of the evaluation boards, and can select one of the standard platforms from the drop-down list. You should select one of existing platforms if all of the following are true:

- You are in the development phase and are using an evaluation board.
- You do not care about cache sizes and are satisfied with the defaults set by the existing platform.
- You do not want to change the default section placement.
- You want the same clock rate as the evaluation board.

If any of these statements do not apply, see Section 7.2.2.

7.2.2 Creating a Custom Platform

At some point in the application development process, most customers build their own boards, choosing a TI device and adding custom external memory.

You will also need to create your own platform if any of the following items are true:

- You want to customize cache sizes.
- You want to manually override the default section placement.

For such custom boards you will need to create a platform using the platform wizard. The platform wizard is a GUI tool that allows you to easily create a custom platform. Creating a custom platform gives you a lot of flexibility in terms of defining the memory map and selecting default memory segments for section placement.

To run the platform wizard, follow these steps:
1. In CCS, choose **Tools > RTSC Tools > Platform > New** from the menus. This opens the New Platform wizard.

2. Type a name for the package. This will be the name of the directory created to contain the platform package, and will be the name you select when you choose the platform for the project to use.

   You can use a simple name or a period-delimited name. Periods correspond to directory levels when a platform package is created. For example, myBoards.DA830_bigCache will be created in C:\myRepository\packages\myBoards\DA830_bigCache if you are using C:\myRepository\packages as the repository location.

3. Next to the Platform Package Repository field, click **Browse**. Choose the location of the repository where you want to save your platform package. The default is C:\Users\<username>\myRepository\packages.

   ![Platform Package Details](image)

   If you haven’t created a package repository before, and you don’t want to use the default, create a new directory to contain the repository. In the directory you choose, create a sub-directory called “packages”. For example, you might use C:\myRepository\packages as the repository. The full path to the repository should not contain any spaces.

4. Optionally, if you have already created a CCS project that you want to be able to use this platform, check the **Add Repository to Project Package Path** box. Then select the project that should have access to this repository. You don’t need to do this now; you can also add repositories to projects from the project’s Build Properties dialog.

5. Choose the **Device Family** and **Device Name** from the drop-down lists. For example:

   ![Device Details](image)

6. Click **Next**. You see the Device Page of the platform wizard.

   **Note:** If you want another project to be able to use this platform, you can later add the repository that contains the platform to a project’s properties by right-clicking on the project and choosing **Build Properties**. Choose the **CCS General** category and then the **RTSC** tab. Click **Add** and browse the file-system for the repository you want the project to be able to search for platforms.

   See the subsections that follow for ways to specify the cache, segment, and section use for your platform. You can also visit the **Demo of the RTSC Platform Wizard** to watch demonstrations that use the platform wizard.
7.2.2.1 Getting and Setting the Clock Speed and Default Memory Settings

The Device Page opens with no clock speed setting, no external memory segments, and no memory section assignments. Generally, the first thing you will want to do is to import the default settings from an existing platform so that you can use those as a base for making the modifications you need.

To import defaults, follow these steps:

1. Click the Import button next to the Clock Speed field.
2. In the Select Platform dialog, choose the platform whose defaults you want to import, and click OK.
3. Click Yes in the confirmation dialog that asks if you are sure you want to change the settings.
4. You see the default clock speed and external memory settings. You can change these if you like.
7.2.2.2 Determining Cache Sizes for Custom Platforms

Since cache sizes affect the memory map, if you are using a C6000 target, you need to decide on the sizes you want to use when creating a platform. For example, if you are using the "ti.platforms.evmDA830" platform, the L1P, L1D, and L2 cache sizes affect the size of available L1PSRAM, L1DSRAM, and IRAM.

Since cache sizes are set in the platform, executables that need different cache configurations will also need different platforms.

The following example steps use the Device Page of the platform wizard to specify the maximum cache sizes for TMS320DA830 platform:

1. Set the L1D Cache to 32K. Set the L1P Cache to 32K. Set the L2 Cache to 256K.
2. Notice that the sizes of L1PSRAM, L1DSRAM and IRAM are adjusted down to zero.

See Section 7.6 for more about cache configuration.

7.2.2.3 Selecting Default Memory Segments for Data, Code and Stack

The platform also determines the default memory segment for placement of code, data and stack. If you do not explicitly place a section, then the defaults are used. For example, if you do not configure the location of a Task stack in the *.cfg file, then the task stack will be placed in the stack memory segment specified by the platform.

You can make coarse decisions on where you want your code, data, and stack to be placed by selecting values for data memory, code memory, and stack memory in your platform definition.

For example on the evmDA830, you may want your code in SDRAM and data in RAM. You can achieve this by selecting Code memory to be SDRAM and Data memory to be IRAM in the platform wizard.

See Section 7.3 for details about how you can use the configuration files to be more specific about how memory sections should be placed in memory segments.
7.2.4 Setting Custom Base Addresses and Lengths for Segments

You can customize the names, locations, sizes, type, and access for both internal and external memory segments.

To customize internal memory segments, begin by checking the Customize Memory box in the Device Memory area. You can then click on fields in the list of memory segments and make changes. In the Name, Base, and Length columns, type the value you want to use. In the Space and Access columns, you can select from a list of options.

To customize external memory segments, you can right-click in the External Memory area and choose Insert Row or Delete Row.

In the new row, type a Name, Base address, and Length for the memory segment. Choose the type of memory Space and the permitted Access to this memory.

To watch a demo that shows how to customize memory segments, see http://rtsc.eclipseprojects.io/docs-tip/Demo_of_Customizing_Memory_Sections.

7.3 Placing Sections into Memory Segments

Note: If you are using the MSP430, see Section 7.4 instead. This section does not apply to the MSP430.

The platform defines your application’s memory map along with the default section placement in memory for those segments. (See Section 7.2 for details on platform configuration.) The platform provides general control over the placement of the "code", "data", and "stack" sections. For more fine-grained control of the sections, there are several options:
To define and place new sections that are not managed by SYS/BIOS, you can either modify the project’s configuration (.cfg) file as described in Sections 7.3.1 and 7.3.2 or provide a supplemental linker command file as described in Section 7.3.3.

To modify the placement of sections managed by SYS/BIOS, you can either modify the project’s configuration (.cfg) file as described in Sections 7.3.1 and 7.3.2 or provide your own linker command file to replace part or all of the generated one as described in Section 7.3.4.

Note: To place sections into segments in the *.cfg file, you will need to edit the source of your *.cfg script in a text editor. Currently, you cannot use the XGCONF GUI editor.

### 7.3.1 Configuring Simple Section Placement

In a configuration file, section placement is done through the Program.sectMap[] array.

The simplest way to configure the segment in which a section should be placed is as follows:

```
Program.sectMap[".foo"] = "IRAM";
```

This example would cause the IRAM segment to be used both for loading and running the .foo section.

### 7.3.2 Configuring Section Placement Using a SectionSpec

The Program.sectMap[] array maps section names to structures of the type SectionSpec. If you use the simple statement syntax shown in the previous section, you don’t need to create a SectionSpec structure. Using a SectionSpec structure gives you more precise control over how the run and load memory segments (or addresses) for a section are specified.

The SectionSpec structure contains the following fields.

- **runSegment.** The segment where the section is to be run.
- **loadSegment.** The segment where the section is to be loaded.
- **runAddress.** Starting address where the section is to be run. You cannot specify both the runSegment and runAddress.
- **loadAddress.** Starting address where the section is to be loaded. You cannot specify both the loadSegment and the loadAddress.
- **runAlign.** Alignment of the section specified by runSegment. If you specify the runSegment, you can also specify runAlign.
- **loadAlign.** Alignment of the section specified by loadSegment. If you specify the loadSegment, you can also specify loadAlign.
- **type.** You can use this field to define various target-specific flags to identify the section type. For example, COPY, DSECT, and NOLOAD.
- **fill.** If specified, this value is used to initialize an uninitialized section.
The following .cfg file statements specify the memory segments where the .foo section is to be loaded and run.

```
Program.sectMap[".foo"] = new Program.SectionSpec();
Program.sectMap[".foo"].loadSegment = "FLASH";
Program.sectMap[".foo"].runSegment = "RAM";
```

If you specify only the loadSegment or runSegment for a section, the default behavior is to use the specified segment for both loading and running.

The following statements place the Swi_post() function into the IRAM memory segment:

```
Program.sectMap[".text:_ti_sysbios_knl_Swi_post__F"] = new Program.SectionSpec();
Program.sectMap[".text:_ti_sysbios_knl_Swi_post__F"] = "IRAM";
```

The following statements place all static instances for the Task module into the .taskStatic section:

```
var Task = xdc.useModule('ti.sysbios.knl.Task');

Task.common$.instanceSection = ".taskStatic";
Program.sectMap[".taskStatic"] = new Program.SectionSpec();
Program.sectMap[".taskStatic"].loadSegment = "IRAM";
```

Configuration statements that specify sections using the sectMap array affect the section placement in the linker command file that is generated from the configuration.

### 7.3.3 Providing a Supplemental Linker Command File

It is possible to provide your own linker command file to supplement the one generated by XDC tools. You can do this to define new sections and to leverage all of the features available through the linker command language.

Simply add a linker command file you have written to your CCS project. The file must have a file extension of *.cmd. CCS automatically recognizes such a linker command file and invokes it during the link step.

The definition of the memory map (the "MEMORY" specification of the linker command file) is handled by your platform, so this approach cannot be used to change the definition of the existing memory segments.

This approach works for defining new memory sections. If you want to change the placement of sections managed by SYS/BIOS, however, you must follow one of the approaches described in Section 7.3.1, 7.3.2, or 7.3.4.
7.3.4 Default Linker Command File and Customization Options

The linker command file used by a SYS/BIOS application is auto-generated when the configuration is processed. This command file is typically located with the configuration package, as shown here:

```c
/* ELF symbols */
--symbol_map ___TI_STACK_BASE=__s
--symbol_map ___TI_STACK_SIZE=__S
--symbol_map ___TI_STATIC_BASE=__
--symbol_map __c_int00=0__c_int00

--args 0x200
-heap 0x0
-stack 0x1000

MEMORY
{
    IRAM (RX) : org = 0x11700000,
    IRAM (RA) : org = 0x11800000
    L3_CBA_RAM (RW) : org = 0x80
    SDRAM : org = 0xc3000000, len
}
```

The auto-generated linker command file uses a template specified by the platform associated with the program. This command file defines MEMORY and SECTIONS as determined during the configuration process. The placement of sections in the configuration file is reflected in this auto-generated command file.

You can customize the auto-generated linker command file using any of the following techniques:

- Exclude sections from the auto-generated command file. See the page at [http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html#sections.Exclude](http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html#sections.Exclude) for examples that configure the Program.sectionsExclude parameter.

- Replace the entire SECTIONS portion of the generated linker command file. See information about the Program.sectionsTemplate parameter at [http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html#sections.Template](http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html#sections.Template).
• Specify a template for the program’s linker command file. See http://rtsc.eclipseprojects.io/cdoc-tip/xdc/cfg/Program.html#link.Template for information and examples that use the Program.linkTemplate parameter. The simplest way to create a template for your program is to first auto-generate the linker command file, then edit it to suit your application’s needs, and then set the Program.linkTemplate parameter to reference your edited file.

Important: This technique requires that you copy the auto-generated linker command file and edit it every time you change the configuration, the platform, or install a new version of XDCtools.

7.4 Sections and Memory Mapping for MSP430, Stellaris M3, and C28x

When you create a project in CCS, you must select a device as part of the project settings (for example, MSP430F5435A) in the CCS project creation wizard. A linker command file specific to the selected device is automatically added to the project by CCS.

In the RTSC Configuration Settings page of the wizard, a Target and a Platform are automatically selected based on your previous selections. We recommend using “release” as the Build-Profile, even if you are in the debugging stage of code development. See Section 2.4.5 for more about build settings for reducing the size of your executable for the MSP430.

The platforms for the MSP430, Stellaris Cortex-M3 microcontrollers, and C28x devices differ from other platforms in that they do not define the memory map for the device. Instead, the linker command file added by the project wizard is used directly. Any changes to the memory map and section placement can be made by editing the linker command file directly. See the MSP430 Optimizing C/C++ Compiler User’s Guide for more details on linker command file options.

Note that an additional XDCtools-generated linker command file is added to the project; this file places a few sections that are SYS/BIOS specific. This command file assumes that "FLASH" and "RAM" are part of the memory map.

Note: For hardware-specific information about using SYS/BIOS, see the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html).

7.5 Stacks

SYS/BIOS uses a single system stack for hardware interrupts and a separate task stack for each Task instance.

7.5.1 System Stack

You can configure the size of the System stack, which is used as the stack for hardware interrupts and software interrupts (and by the Idle functions if Task is disabled). You should set the System stack size to meet the application’s needs. See Section 3.5.3 for information about system stack size requirements.
You can use the .stack section to control the location of the system stack. For example, the following configuration statements place the system stack of size 0x400 in the IRAM segment.

```plaintext
Program.stack = 0x400;
Program.sectMap[".stack"] = "IRAM";
```

Setting Program.stack generates appropriate linker options in the linker command file to allow the system stack to be allocated at link time. For example, the linker command file for a C6000 application might include the command option -stack 0x0400.

See your project’s auto-generated linker command file for symbols related to the system stack size and location. For C6000 these include __TI_STACK_END and __STACK_SIZE.

### 7.5.2 Task Stacks

If the Task module is enabled, SYS/BIOS creates an additional stack for each Task instance the application contains (plus one task stack for the Idle threads). See Section 3.6.3 for information about task stack size requirements.

You can specify the size of a Task’s stack in the configuration file. (You can use XGCONF to do this or edit the .cfg file directly.) For example:

```plaintext
var Task = xdc.useModule('ti.sysbios.knl.Task');

/* Set default stack size for tasks */
Task.defaultStackSize = 1024;

/* Set size of idle task stack */
Task.idleTaskStackSize = 1024;

/* Create a Task Instance and set stack size */
var tskParams = new Task.Params;
 tskParams.stackSize = 1024;
var task0 = Task.create('&task0Fxn', tskParams);

/* Place idle task stack section */
Program.sectMap[".idleTaskStackSection"] = "IRAM";

/* Place other static task stacks */
Program.sectMap[".taskStackSection"] = "IRAM";
```
7.6 Cache Configuration

C6000 cache sizes in SYS/BIOS 6.x are determined by the platform you choose. To change cache sizes, create or modify the platform using the platform wizard as described in Section 7.2.2.2.

The following subsections describe ways you can use the family-specify Cache modules in SYS/BIOS to manipulate caching behavior.

7.6.1 Configure Cache Size Registers at Startup

For C6000 targets, the ti.sysbios.hal.Cache module gets cache sizes from the platform and sets the cache size registers at startup time. The ti.sysbios.hal.Cache module is a generic module whose implementations are provided in the family-specific ti.sysbios.family.*.Cache modules. See Section 8.4 for more about the Cache module and its implementations.

For example on the DA830, the Cache module sets the L1PCFG, L1DCFG and L2CFG registers at startup time.

7.6.2 Configure Parameters to Set MAR Registers

For C6000 targets, the ti.sysbios.family.c64p.Cache module defines Cache.MAR##-## configuration parameters that allow you to control which external memory addresses are cacheable or non-cacheable. For example, Cache_MAR128_159 is one such configuration parameter. These configuration parameters directly map to the MAR registers on the device. Each external memory address space of 16 MB is controlled by one MAR bit (0: noncacheable, 1:cacheable).

The SYS/BIOS Cache module has module-wide configuration parameters that map to the MAR registers.

By default the C64P Cache module makes all memory regions defined in the platform cacheable by setting all of the corresponding MAR bits to 0x1. To disable caching on a DA830 device for the external memory range from 8000 0000h to 80FF FFFFh, you set Cache.MAR128_159 = 0x0 as follows. This sets register MAR128 to 0.

```javascript
var Cache = xdc.useModule('ti.sysbios.family.c64p.Cache);
Cache.MAR_128_159 = 0x0;
```

After the MAR bit is set for an external memory space, new addresses accessed by the CPU will be cached in L2 cache or, if L2 is disabled, in L1. See device-specific reference guides for MAR registers and their mapping to external memory addresses.

At system startup, the Cache module writes to the MAR registers and configures them.

7.6.3 Cache Runtime APIs

For any target that has a cache, the ti.sysbios.hal.Cache module provides APIs to manipulate caches at runtime. These include the Cache_enable(), Cache_disable(), Cache_wb(), and Cache_inv() functions. See Section 8.4.1 for details.
7.7 Dynamic Memory Allocation

A "Heap" is a module that implements the IHeap interface. Heaps are dynamic memory managers: they manage a specific piece of memory and support allocating and freeing pieces ("blocks") of that memory.

Memory allocation sizes are measured in "Minimum Addressable Units" (MAUs) of memory. An MAU is the smallest unit of data storage that can be read or written by the CPU. For the C28x, this is an 16-bit word. For the all other currently supported target families—including C6000, ARM, and MSP430—this is an 8-bit byte.

7.7.1 Memory Policy

You can reduce the amount of code space used by an application by setting the memoryPolicy on a global or per-module basis. This is particularly useful on targets where the code memory is significantly constrained.

The options are:

- **DELETE_POLICY.** This is the default. The application creates and deletes objects (or objects for this module) at runtime. You need both the `MODULE_create()` functions and the `MODULE_delete()` functions to be available to the application.

- **CREATE_POLICY.** The application creates objects (or objects for this module) at runtime. It does not delete objects at runtime. You need the `MODULE_create()` functions to be available to the application, but not the `MODULE_delete()` functions.

- **STATIC_POLICY.** The application creates all objects (or all objects for this module) in the configuration file. You don't need the `MODULE_create()` or the `MODULE_delete()` functions to be available to the application.

For example, the following configuration statements set the default memory policy for all modules to static instance creation only:

```javascript
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Types = xdc.useModule('xdc.runtime.Types');
Defaults.memoryPolicy = Types.STATIC_POLICY;
```

7.7.2 Specifying the Default System Heap

The BIOS module creates a default heap for use by SYS/BIOS. When Memory_alloc() is called at runtime with a NULL heap, this system heap will be used. The default system heap created by the BIOS module is a HeapMem instance. The BIOS module provides the following configuration parameters related to the system heap:

- **BIOS.heapSize** can be used to set the system heap size.

- **BIOS.heapSection** can be used to place the system heap.

For example, you can configure the default system heap as follows:

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.heapSize = 0x900;
BIOS.heapSection = "systemHeap";
```

If you want to use a different heap manager for the system heap, you can specify the system heap in your configuration file and SYS/BIOS will not override the setting.
### Note:

The SYS/BIOS system heap cannot be a HeapStd instance. The BIOS module detects this condition and generates an error message.

The following configuration statements specify a system heap that uses HeapBuf instead of HeapMem:

```c
/* Create a heap using HeapBuf */
var heapBufParams = new HeapBuf.Params;
heapBufParams.blockSize = 128;
heapBufParams.numBlocks = 2;
heapBufParams.align = 8;
heapBufParams.sectionName = "myHeap";
Program.global.myHeap = HeapBuf.create(heapBufParams);

Program.sectMap["myHeap"] = "DDR";
Memory.defaultHeapInstance = Program.global.myHeap;
```

If you do not want a system heap to be created, you can set BIOS.heapSize to zero. The BIOS module will then use a HeapNull instance to minimize code/data usage.

#### 7.7.3 Using the xdc.runtime.Memory Module

All dynamic allocation is done through the xdc.runtime.Memory module. The Memory module provides APIs such as `Memory_alloc()` and `Memory_free()`. All Memory APIs take an `IHeap_Handle` as their first argument. The Memory module does very little work itself; it makes calls to the Heap module through the `IHeap_Handle`. The Heap module is responsible for managing the memory. Using Memory APIs makes applications and middleware portable and not tied to a particular heap implementation.

`IHeap_Handle`s to be used with Memory APIs are obtained by creating Heap instances statically or dynamically. When The `IHeap_Handle` passed to the Memory APIs is NULL, the default system heap is used. See Section 7.7.2.

**Runtime example:** This example allocates and frees memory from two different heaps. It allocates from the system heap by passing NULL to `Memory_alloc` as the `IHeap_Handle`. It allocates from a separate heap called "otherHeap" by explicitly passing the "otherHeap" handle.

```c
#include <xdc/std.h>
#include <xdc/runtime/IHeap.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Memory.h>
#include <xdc/runtime/Error.h>

extern IHeap_Handle systemHeap, otherHeap;

Void main()
{
    Ptr buf1, buf2;
    Error_Block eb;
    Error_init(&eb);
```
7.7.4 Specifying a Heap for Module Dynamic Instances

You can specify the default heap to be used when allocating memory for dynamically-created module instances. The configuration property that controls the default heap for all modules is `Default.common$.instanceHeap`.

For example, these configuration statements specify the heap for allocating instances:

```javascript
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');
var heapMemParams = new HeapMem.Params;
heapMemParams.size = 8192;
var heap1 = HeapMem.create(heapMemParams);
Default.common$.instanceHeap = heap1;
```

If you do not specify a separate heap for instances, the heap specified for `Memory.defaultHeapInstance` will be used (see Section 7.7.2).

To specify the heap a specific module uses when it allocates memory for dynamically-created instances, set the `instanceHeap` parameter for that module. For example, the following configuration statements specify the heap for the Semaphore module:

```javascript
var Semaphore = xdc.useModule('ti.sysbios.knl.Semaphore');
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');
var heapMemParams = new HeapMem.Params;
heapMemParams.size = 8192;
var heap1 = HeapMem.create(heapMemParams);
Semaphore.common$.instanceHeap = heap1;
```
7.7.5 Using malloc() and free()

Applications can call the malloc() and free() functions. Normally these functions are provided by the RTS library supplied by the code generation tools. However, when you are using SYS/BIOS, these functions are provided by SYS/BIOS and re-direct allocations to the default system heap (see Section 7.7.2).

To change the size of the heap used by malloc(), use the BIOS.heapSize configuration parameter.

7.8 Heap Implementations

The xdc.runtime.Memory module is the common interface for all memory operations. The actual memory management is performed by a Heap instance, such as an instance of HeapMem or HeapBuf. For example, Memory_alloc() is used at runtime to dynamically allocate memory. All of the Memory APIs take a Heap instance as one of their parameters. Internally, the Memory module calls into the heap's interface functions.

SYS/BIOS provides the following Heap implementations:

- **HeapMin.** Very small code footprint implementation. Supports memory allocation, but does not support freeing memory. Section 7.8.1
- **HeapMem.** Allocate variable-size blocks. Section 7.8.2
- **HeapBuf.** Allocate fixed-size blocks. Section 7.8.3
- **HeapMultiBuf.** Specify variable-size allocation, but internally allocate from a variety of fixed-size blocks. Section 7.8.4
- **HeapTrack.** Used to detect memory allocation and deallocation problems. Section 7.8.5

This table compares SYS/BIOS heap implementations. See the CDOC help for details.

<table>
<thead>
<tr>
<th>Module</th>
<th>Description/Characteristics</th>
<th>Limitations</th>
</tr>
</thead>
<tbody>
<tr>
<td>xdc.runtime.HeapMin</td>
<td>Minimal memory use, non-blocking. No support for freeing heap memory.</td>
<td>free() not supported</td>
</tr>
<tr>
<td>ti.sysbios.heaps.HeapMem</td>
<td>Uses Gate to protect during allocation and freeing, accepts any block size</td>
<td>Slower, non-deterministic</td>
</tr>
<tr>
<td>ti.sysbios.heaps.HeapBuf</td>
<td>Fast, deterministic, non-blocking</td>
<td>Allocates blocks of a single size</td>
</tr>
<tr>
<td>ti.sysbios.heaps.HeapMultiBuf</td>
<td>Fast, deterministic, non-blocking, multiple-block sizes supported</td>
<td>Limited number of block sizes</td>
</tr>
<tr>
<td>ti.sysbios.heaps.HeapTrack</td>
<td>Detects memory leaks, buffer overflows, and double frees of memory.</td>
<td>Performance and size penalty associated with tracking</td>
</tr>
</tbody>
</table>

Different heap implementations optimize for different memory management traits. The HeapMem module (Section 7.8.2) accepts requests for all possible sizes of blocks, so it minimizes internal fragmentation. The HeapBuf module (Section 7.8.3), on the other hand, can only allocate blocks of a fixed size, so it minimizes external fragmentation in the heap and is also faster at allocating and freeing memory.
7.8.1 HeapMin

HeapMin is a minimal footprint heap implementation. This module is designed for applications that generally allocate memory and create module instances at runtime, but never delete created instances or free memory explicitly.

HeapMin does not support freeing memory. By default, an application aborts with an error status if it calls HeapMin_free(). The HeapMin.freeError configuration parameter can be set to "false" to cause HeapMin_free() to simply return without raising an error.

If you call HeapMin_create() at runtime, the application is responsible for specifying the buffer that the heap will manage and aligning the buffer.

If you create HeapMin instances in your static configuration, the heap is aligned to the largest alignment required for targets that support static alignment. For targets that do not support static alignment, the buffer alignment is undefined.

7.8.2 HeapMem

HeapMem can be considered the most "flexible" of the Heaps because it allows you to allocate variable-sized blocks. When the size of memory requests is not known until runtime, it is ideal to be able to allocate exactly how much memory is required each time. For example, if a program needs to store an array of objects, and the number of objects needed isn't known until the program actually executes, the array will likely need to be allocated from a HeapMem.

The flexibility offered by HeapMem has a number of performance tradeoffs.

- **External Fragmentation.** Allocating variable-sized blocks can result in fragmentation. As memory blocks are "freed" back to the HeapMem, the available memory in the HeapMem becomes scattered throughout the heap. The total amount of free space in the HeapMem may be large, but because it is not contiguous, only blocks as large as the "fragments" in the heap can be allocated.

  This type of fragmentation is referred to as "external" fragmentation because the blocks themselves are allocated exactly to size, so the fragmentation is in the overall heap and is "external" to the blocks themselves.

- **Non-Deterministic Performance.** As the memory managed by the HeapMem becomes fragmented, the available chunks of memory are stored on a linked list. To allocate another block of memory, this list must be traversed to find a suitable block. Because this list can vary in length, it's not known how long an allocation request will take, and so the performance becomes "non-deterministic".

A number of suggestions can aide in the optimal use of a HeapMem.

- **Larger Blocks First.** If possible, allocate larger blocks first. Previous allocations of small memory blocks can reduce the size of the blocks available for larger memory allocations.

- **Overestimate Heap Size.** To account for the negative effects of fragmentation, use a HeapMem that is significantly larger than the absolute amount of memory the program will likely need.

When a block is freed back to the HeapMem, HeapMem combines the block with adjacent free blocks to make the available block sizes as large as possible.

---

Note: HeapMem uses a user-provided lock to lock access to the memory. For details, see Section 4.3, Gates.
The following examples create a HeapMem instance with a size of 1024 MAUs.

**Configuration example:** This example statically configures the heap:

```javascript
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');
/* Create heap as global variable so it can be used in C code */
var heapMemParams = new HeapMem.Params();
heapMemParams.size = 1024;
Program.global.myHeap = HeapMem.create(heapMemParams);
```

**Runtime example:** This second example uses C code to dynamically create a HeapMem instance:

```c
HeapMem_Params prms;
static char buf[1024];
HeapMem_Handle heap;
Error_Block eb;
...
Error_init(&eb);
HeapMem_Params_init(&prms);
prms.size = 1024;
prms.buf = (Ptr)buf;
heap = HeapMem_create(&prms, &eb);
if (heap == NULL) {
    System_abort("HeapMem create failed");
}
```

HeapMem uses a Gate (see the Gates section for an explanation of Gates) to prevent concurrent accesses to the code which operates on a HeapMem's list of free blocks. The type of Gate used by HeapMem is statically configurable through the HeapMem's common defaults.

**Configuration example:** This example configures HeapMem to use a GateMutexPri to protect critical regions of code.

```javascript
var GateMutexPri = xdc.useModule('ti.sysbios.gates.GateMutexPri');
var HeapMem = xdc.useModule('ti.sysbios.heaps.HeapMem');
HeapMem.common$.gate = GateMutexPri.create();
```

The type of Gate used depends upon the level of protection needed for the application. If there is no risk of concurrent accesses to the heap, then "null" can be assigned to forgo the use of any Gate, which would improve performance. For an application that could have concurrent accesses, a GateMutex is a likely choice. Or, if it is possible that a critical thread will require the HeapMem at the same time as a low-priority thread, then a GateMutexPri would be best suited to ensuring that the critical thread receives access to the HeapMem as quickly as possible. See Section 4.3.2.2, GateMutexPri for more information.

### 7.8.3 HeapBuf

HeapBuf is used for allocating fixed-size blocks of memory, and is designed to be fast and deterministic. Often a program needs to create and delete a varying number of instances of a fixed-size object. A HeapBuf is ideal for allocating space for such objects, since it can handle the request quickly and without any fragmentation.
A HeapBuf may also be used for allocating objects of varying sizes when response time is more important than efficient memory usage. In this case, a HeapBuf will suffer from "internal" fragmentation. There will never be any fragmented space in the heap overall, but the allocated blocks themselves may contain wasted space, so the fragmentation is "internal" to the allocated block.

Allocating from and freeing to a HeapBuf always takes the same amount of time, so a HeapBuf is a "deterministic" memory manager.

The following examples create a HeapBuf instance with 10 memory blocks of size 128.

**Configuration example:** The first example configures the heap statically. In this example, no buffer or bufSize parameter needs to be specified, since the configuration can compute these values and allocate the correct sections at link time.

```c
var HeapBuf = xdc.useModule('ti.sysbios.heaps.HeapBuf');
/* Create heap as global variable so it can be used in C code */
var heapBufParams = new HeapBuf.Params();
heapBufParams.blockSize = 128;
heapBufParams.numBlocks = 10;
Program.global.myHeap = HeapBuf.create(heapBufParams);
```

**Runtime example:** This second example uses C code to dynamically create a HeapBuf instance. In this example, you must pass the bufSize and buf parameters. Be careful when specifying these runtime parameters. The blockSize needs to be a multiple of the worst-case structure alignment size. And bufSize should be equal to blockSize * numBlocks. The worst-case structure alignment is target dependent. On devices with a 32-bit architecture, the 8-byte alignment is used. The base address of the buffer should also be aligned to this same size.

```c
var HeapBuf_Params prms;
static char buf[1280];
HeapBuf_Handle heap;
Error_Block eb;

... Error_init(&eb);
HeapBuf_Params_init(&prms);
prms.blockSize = 128;
prms.numBlocks = 10;
prms.buf = (Ptr)buf;
prms.bufSize = 1280;
heap = HeapBuf_create(&prms, &eb);
if (heap == NULL) {
    System_abort("HeapBuf create failed");
}
```

### 7.8.4 HeapMultiBuf

HeapMultiBuf is intended to balance the strengths of HeapMem and HeapBuf. Internally, a HeapMultiBuf maintains a collection of HeapBuf instances, each with a different block size, alignment, and number of blocks. A HeapMultiBuf instance can accept memory requests of any size, and simply determines which of the HeapBufs to allocate from.
A HeapMultiBuf provides more flexibility in block size than a single HeapBuf, but largely retains the fast performance of a HeapBuf. A HeapMultiBuf instance has the added overhead of looping through the HeapBufs to determine which to allocate from. In practice, though, the number of different block sizes is usually small and is always a fixed number, so a HeapMultiBuf can be considered deterministic by some definitions.

A HeapMultiBuf services a request for any memory size, but always returns one of the fixed-sized blocks. The allocation will not return any information about the actual size of the allocated block. When freeing a block back to a HeapMultiBuf, the size parameter is ignored. HeapMultiBuf determines the buffer to free the block to by comparing addresses.

When a HeapMultiBuf runs out of blocks in one of its buffers, it can be configured to allocate blocks from the next largest buffer. This is referred to as "block borrowing". See the online reference described in Section 1.6.1 for more about HeapMultiBuf.

The following examples create a HeapMultiBuf that manages 1024 MAUs of memory, which are divided into 3 buffers. It will manage 8 blocks of size 16 MAUs, 8 blocks of size 32 MAUs, and 5 blocks of size 128 MAUs as shown in the following diagram.

```
16 MAUs  16 MAUs  16 MAUs  16 MAUs  16 MAUs  16 MAUs
  32 MAUs  32 MAUs  32 MAUs  32 MAUs
   32 MAUs  32 MAUs
     128 MAUs
```

**Configuration example:** The first example statically configures the HeapMultiBuf instance:

```
var HeapMultiBuf = xdc.useModule('ti.sysbios.heaps.HeapMultiBuf');

/* HeapMultiBuf without blockBorrowing. */
/* Create as a global variable to access it from C Code. */
var heapMultiBufParams = new HeapMultiBuf.Params();
heapMultiBufParams.numBufs = 3;
heapMultiBufParams.blockBorrow = false;
heapMultiBufParams.bufParams =
    [{blockSize: 16, numBlocks:8, align: 0},
     {blockSize: 32, numBlocks:8, align: 0},
     {blockSize: 128, numBlocks:5, align: 0}];
Program.global.myHeap = HeapMultiBuf.create(heapMultiBufParams);
```

**Runtime example:** This second example uses C code to dynamically create a HeapMultiBuf instance:

```
HeapMultiBuf_Params prms;
HeapMultiBuf_Handle heap;
Error_Block eb;
```
...  

Error_init(&eb);

/* Create the buffers to manage */
Char buf0[128];
Char buf1[256];
Char buf2[640];

/* Create the array of HeapBuf_Params */
HeapBuf_Params bufParams[3];

/* Load the default values */
HeapMultiBuf_Params_init(&prms);
prms.numBufs = 3;
prms.bufParams = bufParams;

HeapBuf_Params_init(&prms.bufParams[0]);
prms.bufParams[0].align = 0;
prms.bufParams[0].blockSize = 16;
prms.bufParams[0].numBlocks = 8;
prms.bufParams[0].buf = (Ptr) buf0;
prms.bufParams[0].bufSize = 128;

HeapBuf_Params_init(&prms.bufParams[1]);
prms.bufParams[1].align = 0;
prms.bufParams[1].blockSize = 32;
prms.bufParams[1].numBlocks = 8;
prms.bufParams[1].buf = (Ptr) buf1;
prms.bufParams[1].bufSize = 256;

HeapBuf_Params_init(&prms.bufParams[2]);
prms.bufParams[2].align = 0;
prms.bufParams[2].blockSize = 128;
prms.bufParams[2].numBlocks = 5;
prms.bufParams[2].buf = (Ptr) buf2;
prms.bufParams[2].bufSize = 640;

heap = HeapMultiBuf_create(&prms, &eb);
if (heap == NULL) {
    System_abort("HeapMultiBuf create failed");
}
7.8.5 HeapTrack

HeapTrack is a buffer management module that tracks all currently allocated blocks for any heap instance. HeapTrack is useful for detecting memory leaks, buffer overflows, and double frees of memory blocks. Any XDCtools or SYSBIOS heap instance can plugged into HeapTrack. For each memory allocation, an extra packet of data will be added. This data is used by the Runtime Object View (ROV) to display information about the heap instance.

HeapTrack implements the default heap functions as well as two debugging functions. The first, HeapTrack_printHeap() prints all the memory blocks for a given HeapTrack instance. The second, HeapTrack_printTask() prints all memory blocks for a given Task handle.

HeapTrack has several asserts that detect key memory errors. These include freeing the same block of memory twice, overflowing an allocated block of memory, deleting a non-empty heap instance, and calling HeapTrack_printHeap() with a null heap object.

There is both a performance and size overhead cost when using HeapTrack. These costs should be taken into account when setting heap and buffer sizes.

You can find the amount by which the HeapTrack module increases the block size by using sizeof(HeapTrack_Tracker) in your C code. This is the amount by which the size is increased when your code or some other function calls Memory_alloc() from a heap managed by HeapTrack. The HeapTrack_Tracker structure is added to the end of an allocated block; HeapTrack therefore does not impact the alignment of allocated blocks.

Configuration example: This example statically configures HeapTrack with an existing heap.

```c
var HeapTrack = xdc.useModule('ti.sysbios.heaps.HeapTrack');

var heapTrackParams = new HeapTrack.Params();
heapTrackParams.heap = heapHandle;
Program.global.myHeap = HeapTrack.create(heapTrackParams);
```

You can also use HeapTrack with the default BIOS module heap by setting the heapTrackEnabled configuration parameter.

```c
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.heapTrackEnabled = true;
```

Runtime example: This example uses C code to dynamically create a HeapTrack instance with an existing heap.

```c
HeapTrack_Params prms;
HeapTrack_Handle heap;
Error_Block eb;

Error_init(&eb);
HeapTrack_Params_init(&prms);
prms.heap = heapHandle;
heap = HeapTrack_create(&prms, &eb);
if (heap == NULL) {
    System_abort("HeapTrack create failed");
}
```

```c
```

```c
```
This chapter describes modules that provide hardware abstractions.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>8.1 Hardware Abstraction Layer APIs</td>
<td>166</td>
</tr>
<tr>
<td>8.2 HWI Module</td>
<td>167</td>
</tr>
<tr>
<td>8.3 Timer Module</td>
<td>174</td>
</tr>
<tr>
<td>8.4 Cache Module</td>
<td>179</td>
</tr>
<tr>
<td>8.5 HAL Package Organization</td>
<td>180</td>
</tr>
</tbody>
</table>
8.1 **Hardware Abstraction Layer APIs**

SYS/BIOS provides services for configuration and management of interrupts, cache, and timers. Unlike other SYS/BIOS services such as threading, these modules directly program aspects of a device’s hardware and are grouped together in the Hardware Abstraction Layer (HAL) package. Services such as enabling and disabling interrupts, plugging of interrupt vectors, multiplexing of multiple interrupts to a single vector, and cache invalidation or writeback are described in this chapter.

**Note:** Any configuration or manipulation of interrupts and their associated vectors, the cache, and timers in a SYS/BIOS application must be done through the SYS/BIOS HAL APIs. In earlier versions of DSP/BIOS, some HAL services were not available and developers were expected to use functions from the Chip Support Library (CSL) for a device. The most recent releases of CSL (3.0 or above) are designed for use in applications that do not use SYS/BIOS. Some of their services are not compatible with SYS/BIOS. Avoid using CSL interrupt, cache, and timer functions and SYS/BIOS in the same application, since this combination is known to result in complex interrupt-related debugging problems.

The HAL APIs fall into two categories:

- **Generic APIs** that are available across all targets and devices
- **Target/device-specific APIs** that are available only for a specific device or ISA family

The generic APIs are designed to cover the great majority of use cases. Developers who are concerned with ensuring easy portability between different TI devices are best served by using the generic APIs as much as possible. In cases where the generic APIs cannot enable use of a device-specific hardware feature that is advantageous to the software application, you may choose to use the target/device-specific APIs, which provide full hardware entitlement.

In this chapter, an overview of the functionality of each HAL package is provided along with usage examples for that package’s generic API functions. After the description of the generic functions, examples of target/device-specific APIs, based on those provided for ‘C64x+ devices, are also given. For a full description of the target/device-specific APIs available for a particular family or device, please refer to the API reference documentation. Section 8.5, **HAL Package Organization** provides an overview of the generic HAL packages and their associated target/device-specific packages to facilitate finding the appropriate packages.

**Note:** For hardware-specific information about using SYS/BIOS, see the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html).
8.2 HWI Module

The ti.sysbios.hal.Hwi module provides a collection of APIs for managing hardware interrupts. These APIs are generic across all supported targets and devices and should provide sufficient functionality for most applications.

8.2.1 Associating a C Function with a System Interrupt Source

To associate a user-provided C function with a particular system interrupt, you create a Hwi object that encapsulates information regarding the interrupt required by the Hwi module. The standard static and dynamic forms of the "create" function are supported by the ti.sysbios.hal.Hwi module.

Configuration example: The following example statically creates a Hwi object that associates interrupt 5 with the "myIsr" C function using default instance configuration parameters:

```c
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');
Hwi.create(5, '&myIsr');
```

Runtime example: The C code required to configure the same interrupt dynamically would be as follows:

```c
#include <ti/sysbios/hal/Hwi.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/System.h>

Hwi_Handle myHwi;
Error_Block eb;
...

Error_init(&eb);
myHwi = Hwi_create(5, myIsr, NULL, &eb);
if (myHwi == NULL) {
    System_abort("Hwi create failed");
}
```

The NULL argument is used when the default instance parameters are satisfactory for creating a Hwi object.

8.2.2 Hwi Instance Configuration Parameters

The following configuration parameters and their default values are defined for each Hwi object. For a more detailed discussion of these parameters and their values see the ti.sysbios.hal.Hwi module in the online documentation. (For information on running online help, see Section 1.6.1, Using the API Reference Help System, page 25.)

- The "maskSetting" defines how interrupt nesting is managed by the interrupt dispatcher.

```c
MaskingOption maskSetting = MaskingOption_SELF;
```

- The configured "arg" parameter will be passed to the Hwi function when the dispatcher invokes it.

```c
UArg arg = 0;
```
The "enabledInt" parameter is used to automatically enable or disable an interrupt upon Hwi object creation.

```c
Bool enableInt = true;
```

The "eventId" accommodates 'C6000 devices that allow dynamic association of a peripheral event to an interrupt number. The default value of -1 leaves the eventId associated with an interrupt number in its normal (reset) state (that is, no re-association is required).

```c
Int eventId = -1;
```

The "priority" parameter is provided for those architectures that support interrupt priority setting. The default value of -1 informs the Hwi module to set the interrupt priority to a default value appropriate to the device.

```c
Int priority = -1;
```

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

### 8.2.3 Creating a Hwi Object Using Non-Default Instance Configuration Parameters

Building on the examples given in Section 8.2.1, the following examples show how to associate interrupt number 5 with the "myIsr" C function, passing "10" as the argument to "myIsr" and leaving the interrupt disabled after creation.

**Configuration example:**

```javascript
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');

/* initialize hwiParams to default values */
var hwiParams = new Hwi.Params;

hwiParams.arg = 10;          /* Set myIsr5 argument to 10 */
hwiParams.enableInt = false; /* override default setting */

/* Create a Hwi object for interrupt number 5
 * that invokes myIsr5() with argument 10 */
Hwi.create(5, '&myIsr', hwiParams);
```

**Runtime example:**

```c
#include <ti/sysbios/hal/Hwi.h>
#include <xdc/runtime/Error.h>

Hwi_Params hwiParams;
Hwi_Handle myHwi;
Error_Block eb;
...
```
8.2.4 Enabling and Disabling Interrupts

You can enable and disable interrupts globally as well as individually with the following Hwi module APIs:

- **UInt Hwi_enable();**
  Globally enables all interrupts. Returns the previous enabled/disabled state.

- **UInt Hwi_disable();**
  Globally disables all interrupts. Returns the previous enabled/disabled state.

- **Hwi_restore(UInt key);**
  Restores global interrupts to their previous enabled/disabled state. The "key" is the value returned from Hwi_disable() or Hwi_enable().

- The APIs that follow are used for enabling, disabling, and restoring specific interrupts given by "intNum". They have the same semantics as the global Hwi_enable/disable/restore APIs:
  - **UInt Hwi_enableInterrupt(UInt intNum);**
  - **UInt Hwi_disableInterrupt(UInt intNum);**
  - **Hwi_restoreInterrupt(UInt key);**
  - **Hwi_clearInterrupt(UInt intNum);**
    Clears "intNum" from the set of currently pending interrupts.

Disabling hardware interrupts is useful during a critical section of processing.

On the C6000 platform, Hwi_disable() clears the GIE bit in the control status register (CSR). On the C2000 platform, Hwi_disable() sets the INTM bit in the ST1 register.

There is a situation in which interrupts become re-enabled when you would expect them to remain disabled. This occurs when a Task thread calls Mailbox_post(), Semaphore_post(), or Event_post()—or a similar call not including Swi_post()—while hardware interrupts are disabled but the task scheduler is enabled. All XXX_post() APIs (except Swi_post()) call Task_restore() internally, which in this case ends by calling Hwi_enable().

```c
/* initialize error block and hwiParams to default values */
Error_init(&eb);
Hwi_Params_init(&hwiParams);

hwiParams.arg = 10;
hwiParams.enableInt = FALSE;

myHwi = Hwi_create(5, myIsr, &hwiParams, &eb);
if (myHwi == NULL) {
    System_abort("Hwi create failed");
}
```
8.2.5  A Simple Example Hwi Application

The following example creates two Hwi objects. One for interrupt number 5 and another for interrupt number 6. For illustrative purposes, one interrupt is created statically and the other dynamically. An idle function that waits for the interrupts to complete is also added to the Idle function list.

Configuration example:

```c
/* Pull in BIOS module required by ALL BIOS applications */
xdc.useModule('ti.sysbios.BIOS');

/* Pull in XDC runtime System module for various APIs used */
xdc.useModule('xdc.runtime.System');

/* Get handle to Hwi module for static configuration */
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');

var hwiParams = new Hwi.Params; /* Initialize hwiParams to default values */
hwiParams.arg = 10;             /* Set myIsr5 argument */
hwiParams.enableInt = false;   /* Keep interrupt 5 disabled until later */

/* Create a Hwi object for interrupt number 5 */
/* that invokes myIsr5() with argument 10 */
Hwi.create(5, '&myIsr5', hwiParams);

/* Add an idle thread 'myIdleFunc' that monitors interrupts. */
var Idle = xdc.useModule(ti.sysbios.knl.Idle);

Idle.addFunc('&myIdleFunc');
```

Runtime example:

```c
#include <xdc/std.h>
#include <xdc/runtime/System.h>
#include <xdc/runtime/Error.h>
#include <ti/sysbios/hal/Hwi.h>

Bool Hwi5 = FALSE;
Bool Hwi6 = FALSE;

main(Void) {  
    Hwi_Params hwiParams;
    Hwi_Handle myHwi;
    Error_Block eb;

    /* Initialize error block and hwiParams to default values */
    Error_init(&eb);
    Hwi_Params_init(&hwiParams);
```
8.2.6 The Interrupt Dispatcher

To consolidate code that performs register saving and restoration for each interrupt, SYS/BIOS provides an interrupt dispatcher that automatically performs these actions for an interrupt routine. Use of the Hwi dispatcher allows ISR functions to be written in C.

In addition to preserving the interrupted thread's context, the SYS/BIOS Hwi dispatcher orchestrates the following actions:

```c
/* Set myIsr6 parameters */
hwiParams.arg = 12;
hwiParams.enableInt = FALSE;

/* Create a Hwi object for interrupt number 6
* that invokes myIsr6() with argument 12 */
myHwi = Hwi_create(6, myIsr6, &hwiParams, &eb);
if (myHwi == NULL) {
    System_abort("Hwi create failed");
}

/* enable both interrupts */
Hwi_enableInterrupt(5);
Hwi_enableInterrupt(6);

/* start BIOS */
BIOS_start();
}

/* Runs when interrupt 5 occurs */
Void myIsr5(UArg arg) {
    If (arg == 10) {
        Hwi5 = TRUE;
    }
}

/* Runs when interrupt 6 occurs */
Void myIsr6(UArg arg) {
    If (arg == 12) {
        Hwi6 = TRUE;
    }
}

/* The Idle thread checks for completion of interrupts 5 & 6
* and exits when they have both completed. */
Void myIdleFunc() {
    If (Hwi5 && Hwi6) {
        System_printf("Both interrupts have occurred!");
        System_exit(0);
    }
}
• Disables SYS/BIOS Swi and Task scheduling during interrupt processing
• Automatically manages nested interrupts on a per-interrupt basis.
• Invokes any configured "begin" Hwi Hook functions.
• Runs the Hwi function.
• Invokes any configured "end" Hwi Hook functions.
• Invokes Swi and Task schedulers after interrupt processing to perform any Swi and Task operations resulting from actions within the Hwi function.

On some platforms, such as the MSP430, the Hwi dispatcher is not provided. However, interrupt stubs are generated to provide essentially the same default functionality. However, the generated interrupt stubs do not automatically manage nested interrupts, because this is not supported on the MSP430.

---

**Note:** For hardware-specific information about using SYS/BIOS, see the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html).

---

**Note:** The `interrupt` keyword or INTERRUPT pragma must not be used to define the C function invoked by the Hwi dispatcher (or interrupt stubs, on platforms for which the Hwi dispatcher is not provided, such as the MSP430). The Hwi dispatcher and the interrupt stubs contain this functionality, and the use of the C modifier will cause catastrophic results.

Functions that use the `interrupt` keyword or INTERRUPT pragma may not use the Hwi dispatcher or interrupt stubs and may not call SYS/BIOS APIs.

---

### 8.2.7 Registers Saved and Restored by the Interrupt Dispatcher

The registers saved and restored by the dispatcher in preparation for invoking the user's Hwi function conform to the "saved by caller" or "scratch" registers as defined in the register usage conventions section of the C compiler documents. For more information, either about which registers are saved and restored, or about the TMS320 functions conforming to the Texas Instruments C runtime model, see the Optimizing Compiler User's Guide for your platform.

### 8.2.8 Additional Target/Device-Specific Hwi Module Functionality

As described in Section 8.5, the ti.sysbios.hal.Hwi module is implemented using the proxy-delegate mechanism. All ti.sysbios.hal.Hwi module APIs are forwarded to a target/device-specific Hwi module that implements all of the ti.sysbios.hal.Hwi required APIs. Each of these Hwi module implementations provide additional APIs and functionality unique to the family/device and can be used instead of the ti.sysbios.hal.Hwi module if needed.

For example, the 'C64x+ target-specific Hwi module, ti.sysbios.family.c64p.Hwi, provides the following APIs in addition to those defined in the ti.sysbios.hal.Hwi module:

• `Hwi_eventMap(UInt intNum, UInt eventId);`
  Remaps a peripheral event number to an interrupt number.
- Bits16 Hwi_enableIER(Bits16 mask);
  Bits16 Hwi_disableIER(Bits16 mask);
  Bits16 Hwi_restoreIER(Bits16 mask);

These three APIs allow enabling, disabling and restoring a set of interrupts defined by a "mask" argument. These APIs provide direct manipulation of the 'C64x+'s internal IER registers.

To gain access to these additional APIs, you use the target/device-specific Hwi module associated with the 'C64x+' target rather than the ti.sysbios.hal.Hwi module.

For documentation on the target/device-specific Hwi modules, see the CDOC documentation for ti.sysbios.family.*.Hwi. For example, ti.sysbios.family.c28.Hwi.

The following examples are modified versions of portions of the example in Section 8.2.5. The modifications are shown in bold.

**Configuration example:**

```
var Hwi = xdc.useModule('ti.sysbios.family.c64p.Hwi');

/* Initialize hwiParams to default values */
var hwiParams = new Hwi.Params;

/* Set myIsr5 parameters */
hwiParams.arg = 10;
hwiParams.enableInt = false;

/* Create a Hwi object for interrupt number 5 */
/* that invokes myIsr5() with argument 10 */
Hwi.create(5, '&myIsr5', hwiParams);
```

**Runtime example:**

```
#include <ti/sysbios/family/c64p/Hwi.h>
#include <xdc/runtime/Error.h>

main(Void)   {
    Hwi_Params hwiParams;
    Hwi_Handle myHwi;
    Error_Block eb;

    /* Initialize error block and hwiParams to default values */
    Error_init(&eb);
    Hwi_Params_init(&hwiParams);

    /* Set myIsr6 parameters */
    hwiParams.arg = 12;
    hwiParams.enableInt = FALSE;
```
The ti.sysbios.hal.Timer module presents a standard interface for using the timer peripherals. It hides any target/device-specific characteristics of the timer peripherals. It inherits the ti.sysbios.interfaces.ITimer interface.

You can use this module to create a timer (that is, to mark a timer for use) and configure it to call a tickFxn when the timer expires. Use this module only if you do not need to do any custom configuration of the timer peripheral.

This module has a configuration parameter called TimerProxy which is plugged by default with a target/device-specific implementation. For example, the implementation for C64x targets is ti.sysbios.family.c64.Timer.

The timer can be configured as a one-shot or a continuous mode timer. The period can be specified in timer counts or microseconds.

The timer interrupt always uses the Hwi dispatcher. The Timer tickFxn runs in the context of a Hwi thread. The Timer module automatically creates a Hwi instance for the timer interrupt.

The Timer_create() API takes a timerId. The timerId can range from zero to a target/device-specific value determined by the TimerProxy. The timerId is just a logical ID; its relationship to the actual timer peripheral is controlled by the TimerProxy.

If it does not matter to your program which timer is used, specify a timerId of Timer.ANY (for configuration) or Timer_ANY (in C), which means "use any available timer". For example, in a configuration use:

```c
Timer.create(Timer.ANY, "myIsr", timerParams);
```

In a C program, use:

```c
myTimer = Timer_create(Timer_ANY, myIsr, &timerParams, &eb);
if (myTimer == NULL) {
    System_abort("Timer create failed");
}
```
The `timerParams` includes a number of parameters to configure the timer. For example `timerParams.startMode` can be set to `StartMode_AUTO` or `StartMode_USER`. The `StartMode_AUTO` setting indicates that statically-created timers will be started in `BIOS_start()` and dynamically-created timers will be started at `create()` time. The `StartMode_USER` indicates that your program starts the timer using `Timer_start()`. See the example in Section 8.3.1.

For information about the parameter structure and individual parameters for instances of this module, see the API Reference Help System described in Section 1.6.1.

You can get the total number of timer peripherals by calling `Timer_getNumTimers()` at runtime. This includes both used and available timer peripherals. You can query the status of the timers by calling `Timer_getStatus()`.

If you want to use a specific timer peripheral or want to use a custom timer configuration (setting timer output pins, emulation behavior, etc.), you should use the target/device-specific Timer module. For example, `ti.sysbios.family.c64.Timer`.

The Timer module also allows you to specify the `extFreq` (external frequency) property for the timer peripheral and provides an API to get the timer frequency at runtime. This external frequency property is supported only on targets where the timer frequency can be set separately from the CPU frequency.

You can use `Timer_getFreq()` to convert from timer interrupts to real time.

The Timer module provides APIs to start, stop, and modify the timer period at runtime. These APIs have the following side effects.

- **Timer_setPeriod()** stops the timer before setting the period register. It then restarts the timer.
- **Timer_stop()** stops the timer and disables the timer interrupt.
- **Timer_start()** clears counters, clears any pending interrupts, and enables the timer interrupt before starting the timer.

**Runtime example:** This C example creates a timer with a period of 10 microseconds. It passes an argument of 1 to the `myIsr` function. It instructs the Timer module to use any available timer peripheral:

```c
Timer_Params timerParams;
Timer_Handle myTimer;
Error_Block eb;
...

Error_init(&eb);

Timer_Params_init(&timerParams);
timerParams.period = 10;
timerParams.periodType = Timer_PeriodType_MICROSECS;
timerParams.arg = 1;
myTimer = Timer_create(Timer_ANY, myIsr, &timerParams, &eb);
if (myTimer == NULL) {
  System_abort("Timer create failed");
}
```
**Configuration example:** This example statically creates a timer with the same characteristics as the previous C example. It specifies a timerId of 1:

```javascript
var timer = xdc.useModule('ti.sysbios.hal.Timer');
var timerParams = new Timer.Params();
timerParams.period = 10;
timerParams.periodType = Timer.PeriodType_MICROSECS;
timerParams.arg = 1;
timer.create(1, '"myIsr', timerParams);
```

**Runtime example:** This C example sets a frequency for a timer it creates. The extFreq.hi and extFreq.lo properties set the high and low 32-bit portions of the structure used to represent the frequency in Hz.

```c
Timer_Params timerParams;
Timer_Handle myTimer;
Error_Block eb;
...
Error_init(&eb);
Timer_Params_init(&timerParams);
timerParams.extFreq.lo = 270000000; /* 27 MHz */
timerParams.extFreq.hi = 0;
myTimer = Timer_create(Timer_ANY, myIsr, &timerParams, &eb);
if (myTimer == NULL) {
    System_abort("Timer create failed");
}
```

**Configuration example:** This configuration example specifies a frequency for a timer that it creates.

```javascript
var Timer = xdc.useModule('ti.sysbios.hal.Timer');
var timerParams = new Timer.Params();
timerParams.extFreq.lo = 270000000;
timerParams.extFreq.hi = 0;
...
Timer.create(1, '"myIsr', timerParams);
```

**Runtime example:** This C example creates a timer that runs the tickFxn() every 2 milliseconds using any available timer peripheral. It also creates a task that, when certain conditions occur, changes the timer’s period from 2 to 4 milliseconds. The tickFxn() itself prints a message that shows the current period of the timer.

```c
Timer_Handle timerHandle;

Int main(Void)
{
    Error_Block eb;
    Timer_Params timerParams;
    Task_Handle taskHandle;
    ...
8.3.1 Target/Device-Specific Timer Modules

As described in Section 8.5, the ti.sysbios.hal.Timer module is implemented using the proxy-delegate mechanism. A separate target/device-specific Timer module is provided for each supported family. For example, the ti.sysbios.timers.timer64.Timer module acts as the timer peripherals manager for the 64P family.

These target/device-specific modules provide additional configuration parameters and APIs that are not supported by the generic ti.sysbios.hal.Timer module.

In the case of the ti.sysbios.timers.timer64.Timer module, the configuration parameters controllInit, globalControlInit, and emuMgtInit are provided to configure various timer properties. This module also exposes a Hwi Params structure as part of its create parameters to allow you to configure the Hwi object associated with the Timer. This module also exposes a Timer_reconfig() API to allow you to reconfigure a statically-created timer.

```c
Error_init(&eb);
Timer_Params_init(&timerParams);
timerParams.period = 2000;   /* 2 ms */
timerHandle = Timer_create(Timer_ANY, tickFxn, &timerParams, &eb);
if (timerHandle == NULL) {
    System_abort("Timer create failed");
}
taskHandle = Task_create(masterTask, NULL, &eb);
if (taskHandle == NULL) {
    System_abort("Task create failed");
}

Void masterTask(UArg arg0 UArg arg1)
{
    ...
    // Condition detected requiring a change to timer period
    Timer_stop(timerHandle);
    Timer_setPeriodMicroSecs(4000);  /* change 2ms to 4ms */
    Timer_start(timerHandle);
    ...
}

Void tickFxn(UArg arg0)
{
    System_printf("Current period = %d\n",
                  Timer_getPeriod(timerHandle);
    }
```
Configuration example: This configuration example specifies timer parameters, including target/device-specific parameters for a timer called myTimer that it creates.

```javascript
var Timer = xdc.useModule('ti.sysbios.timers.timer64.Timer');
var timerParams = new Timer.Params();
timerParams.period = 2000; //2ms
timerParams.arg = 1;
timerParams.startMode = Timer.StartMode_USER;
timerParams.controlInit.invout = 1;
timerParams.globalControlInit.chained = false;
timerParams.emuMgtInit.free = false;
timerParams.suspSrc = SuspSrc_ARM;
Program.global.myTimer = Timer.create(1, "&myIsr", timerParams);
```

Runtime example: This C example uses the myTimer created in the preceding configuration example and reconfigures the timer with a different function argument and startMode in the program’s main() function before calling BIOS_start().

```c
#include <ti/sysbios/timers/timer64/Timer.h>
#include <xdc/cfg/global.h>
#include <xdc/runtime/Error.h>

Void myIsr(UArg arg)
{
    System_printf("myIsr arg = %d\n", (Int)arg);
    System_exit(0);
}

Int main(Int argc, char* argv[])
{
    Timer_Params timerParams;
    Error_Block eb;
    Error_init(&eb);
    Timer_Params_init(&timerParams);
    timerParams.arg = 2;
    timerParams.startMode = Timer_StartMode_AUTO;
    Timer_reconfig(myTimer, tickFxnn, &timerParams, &eb);
    if (Error_check(&eb)) {
        System_abort("Timer reconfigure failed");
    }
    BIOS_start();
    return(0);
}
```
8.4 Cache Module

The cache support provides API functions that perform cache coherency operations at the cache line level or globally. The cache coherency operations are:

- **Invalidate.** Makes valid cache lines invalid and discards the content of the affected cache lines.
- **Writeback.** Writes the contents of cache lines to a lower-level memory, such as the L2 cache or external memory, without discarding the lines in the original cache.
- **Writeback-Invalidation.** Writes the contents of cache lines to lower-level memory, and then discards the contents of the lines in the original cache.

8.4.1 Cache Interface Functions

The cache interface is defined in ti.sysbios.interfaces.ICache. The Cache interface contains the following functions. The implementations for these functions are target/device-specific.

- **Cache_enable();** Enables all caches.
- **Cache_disable();** Disables all caches.
- **Cache_inv(blockPtr, byteCnt, type, wait);** Invalidates the specified range of memory. When you invalidate a cache line, its contents are discarded and the cache tags the line as "clean" so that next time that particular address is read, it is obtained from external memory. All lines in the range are invalidated in all caches.
- **Cache_wb(blockPtr, byteCnt, type, wait);** Writes back the specified range of memory. When you perform a writeback, the contents of the cache lines are written to lower-level memory. All lines within the range are left valid in caches and the data within the range is written back to the source memory.
- **Cache_wblInv(blockPtr, byteCnt, type, wait);** Writes back and invalidates the specified range of memory. When you perform a writeback, the contents of the cache lines are written to lower-level memory. When you invalidate a cache line, its contents are discarded. All lines within the range are written back to the source memory and then invalidated in all caches.

These Cache APIs operate on an address range beginning with the starting address of blockPtr and extending for the specified byte count. The range of addresses operated on is quantized to whole cache lines in each cache.

The blockPtr points to an address in non-cache memory that may be cached in one or more caches or not at all. If the blockPtr does not correspond to the start of a cache line, the start of that cache line is used.

If the byteCnt is not equal to a whole number of cache lines, the byteCnt is rounded up to the next size that equals a whole number of cache lines.

The type parameter is a bit mask of the Cache_Type type, which allows you to specify one or more caches in which the action should be performed.

If the wait parameter is true, then this function waits until the invalidation operation is complete to return. If the wait parameter is false, this function returns immediately. You can use Cache_wait() later to ensure that this operation is complete.

- **Cache_wait();** Waits for the cache wb/wblInv/inv operation to complete. A cache operation is not truly complete until it has worked its way through all buffering and all memory writes have landed in the source memory.
As described in Section 8.5, this module is implemented using the proxy-delegate mechanism. A separate target/device-specific Cache module is provided for each supported family.

Additional APIs are added to this module for certain target/device-specific implementations. For example, the ti.sysbios.family.c64p.Cache module adds APIs specific to the C64x+ caches. These extensions have functions that also have the prefix "Cache_".

Currently the C64x+, C674x, and ARM caches are supported.

**C64x+ specific:** The caches on these devices are Level 1 Program (L1P), Level 1 Data (L1D), and Level 2 (L2). See the TMS320C64x+ DSP Megamodule Reference Guide (SPRU871) for information about the L1P, L1D, and L2 caches.

### 8.5 HAL Package Organization

The three SYS/BIOS modules that reside in the ti.sysbios.hal package: Hwi, Timer, and Cache require target/device-specific API implementations to achieve their functionality. In order to provide a common set of APIs for these modules across all supported families/devices, SYS/BIOS uses the proxy-delegate module mechanism. (See the “RTSC Interface Primer: Lesson 12” for details.)

Each of these three modules serves as a proxy for a corresponding target/device-specific module implementation. In use, all Timer/Hwi/Cache API invocations are forwarded to an appropriate target/device-specific module implementation.

During the configuration step of the application build process, the proxy modules in the ti.sysbios.hal package locate and bind themselves to appropriate delegate module implementations based on the current target and platform specified in the user's config.bld file. The delegate binding process is done internally.

---

**Note:** For hardware-specific information about using SYS/BIOS, see the TI-RTOS Kernel Device Addendum (BIOS_INSTALL_DIR/docs/Device_Addendum.html).

The following tables show some of the Timer, Hwi, and Cache delegate modules that may be selected based on an application's target and device. The mapping of target/device to the delegate modules used by Timer, Cache, and Hwi is accessible through a link in the ti.sysbios.hal package online help.

#### Table 8–1. Proxy to Delegate Mappings

<table>
<thead>
<tr>
<th>Proxy Module</th>
<th>Delegate Modules</th>
</tr>
</thead>
<tbody>
<tr>
<td>ti.sysbios.hal.Timer</td>
<td>ti.sysbios.hal.TimerNull *</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.timers.dmtimer.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.timers.gptimer.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.timers.timer64.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c28.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c67p.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.msp430.Timer</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.arm.&lt;various&gt;.Timer</td>
</tr>
</tbody>
</table>
For targets/devices for which a Timer or Cache module has not yet been developed, the hal.TimerNull or hal.CacheNull delegate is used. In TimerNull/CacheNull, the APIs defined in ITimer/ICache are implemented using null functions.

For the proxy-delegate mechanism to work properly, both the proxy and the delegate modules must be implementations of a common interface specification. The Timer, Hwi, and Cache interface specifications reside in ti.sysbios.interfaces and are ITimer, IHwi, and ICache respectively. These interface specifications define a minimum set of general APIs that, it is believed, will satisfy a vast majority of application requirements. For those applications that may need target/device-specific functionality not defined in these interface specifications, the corresponding Timer, Hwi, and Cache delegate modules contain extensions to the APIs defined in the interface specifications.

To access to these extended API sets, you must directly reference the target/device-specific module in your configuration file and include its corresponding header file in your C source files.

### Table 8–1. Proxy to Delegate Mappings

<table>
<thead>
<tr>
<th>Proxy Module</th>
<th>Delegate Modules</th>
</tr>
</thead>
<tbody>
<tr>
<td>ti.sysbios.hal.Hwi</td>
<td>ti.sysbios.family.c28.Hwi</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c64p.Hwi</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c67p.Hwi</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.msp430.Hwi</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.arm.&lt;various&gt;.Hwi</td>
</tr>
<tr>
<td>ti.sysbios.hal.Cache</td>
<td>ti.sysbios.hal.CacheNull *</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c64p.Cache</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.c67p.Cache</td>
</tr>
<tr>
<td></td>
<td>ti.sysbios.family.arm.Cache</td>
</tr>
</tbody>
</table>
This chapter describes modules and other tools that can be used for instrumentation purposes.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>9.1 Overview of Instrumentation</td>
<td>183</td>
</tr>
<tr>
<td>9.2 Load Module</td>
<td>183</td>
</tr>
<tr>
<td>9.3 Error Handling</td>
<td>185</td>
</tr>
<tr>
<td>9.4 Instrumentation Tools in Code Composer Studio</td>
<td>187</td>
</tr>
<tr>
<td>9.5 Performance Optimization</td>
<td>189</td>
</tr>
</tbody>
</table>
9.1 Overview of Instrumentation

Much of the instrumentation available to SYS/BIOS applications is provided by the XDCtools modules and APIs. See the CDOC online reference documentation for details about the Assert, Diags, and Error modules.

9.2 Load Module

The ti.sysbios.utils.Load module reports execution times and load information for threads in a system.

SYS/BIOS manages four distinct levels of execution threads: hardware interrupt service routines, software interrupt routines, tasks, and background idle functions. The Load module reports execution time and load on a per-task basis, and also provides information globally for hardware interrupt service routines, software interrupt routines, and idle functions (in the form of the idle task). It can also report an estimate of the global CPU load, which is computed as the percentage of time in the measurement window that was not spent in the idle loop. More specifically, the load is computed as follows.

\[
global \ CPU \ load = 100 \times \left(1 - \frac{(x \times t)}{w}\right)
\]

where:

- \(x\) is the number of times the idle loop has been executed during the measurement window.
- \(t\) is the minimum time for a trip around the idle loop, meaning the time it takes to complete the idle loop if no work is being done in it.
- \(w\) is the length in time of the measurement window.

Any work done in the idle loop is included in the CPU load. In other words, any time spent in the loop beyond the shortest trip around the idle loop is counted as non-idle time.

The Load module relies on "update" to be called to compute load and execution times from the time when "update" was last called. This is automatically done for every period specified by Load.windowInMs (default = 500 ms) in a ti.sysbios.knl.Idle function when Load.updateInIdle is set to true (the default). The benchmark time window is the length of time between 2 calls to "update".

The execution time is reported as the count stored by the xdc.runtime.Timestamp module (see Section 5.5). The CPU load is reported in percentages.

By default, load data is gathered for all threads. You can use the configuration parameters Load.hwiEnabled, Load.swiEnabled, and Load.taskEnabled to select which type(s) of threads are monitored.
9.2.1 **Load Module Configuration**

The Load module has been set up to provide data with as little configuration as possible. Using the default configuration, load data is gathered and logged for all threads roughly every 500 ms.

The following code configures the Load module to write Load statistics to a LoggerBuf instance.

```javascript
var LoggerBuf = xdc.useModule('xdc.runtime.LoggerBuf');
var Load = xdc.useModule('ti.sysbios.utils.Load');
var Diags = xdc.useModule('xdc.runtime.Diags');

var loggerBuf = LoggerBuf.create();
Load.common$.logger = loggerBuf;
Load.common$.diags_USER4 = Diags.ALWAYS_ON;
```

For information on advanced configuration and caveats of the Load module, see the online reference documentation.

9.2.2 **Obtaining Load Statistics**

Load statistics recorded by the Load module can be obtained in one of two ways:

- **Load module logger.** If you configure the Load module with a logger and have turned on the diags_USER4, the statistics gathered by the Load module are recorded to the load module's logger instance. You can use the RTOS Analyzer tools to visualize the Load based on these Log records. See the System Analyzer User’s Guide (SPRUH43) for more information.

  Alternatively, you can configure the logger to print the logs to the console. The global CPU load log prints a percentage. For example:

  ```
  LS_cpuLoad: 10
  ```

  The global Swi and Hwi load logs print two numbers: the time in the thread, and the length of the measurement window. For example:

  ```
  LS_hwiLoad: 13845300,158370213
  LS_swiLoad: 11963546,158370213
  ```

  These evaluate to loads of 8.7% and 7.6%.

  The Task load log uses the same format, with the addition of the Task handle address as the first argument. For example:

  ```
  LS_taskLoad: 0x11802830,56553702,158370213
  ```

  This evaluates to a load of 35.7%.

- **Runtime APIs.** You can also choose to call Load_getTaskLoad(), Load_getGlobalSwiLoad(), Load_getGlobalHwiLoad() or Load_getCPULoad() at any time to obtain the statistics at runtime.

  The Load_getCPULoad() API returns an actual percentage load, whereas Load_getTaskLoad(), Load_getGlobalSwiLoad(), and Load_getGlobalHwiLoad() return a Load_Stat structure. This structure contains two fields, the length of time in the thread, and the length of time in the
measurement window. The load percentage can be calculated by dividing these two numbers and multiplying by 100%. However, the Load module also provides a convenience function, Load_calculateLoad(), for this purpose. For example, the following code retrieves the Hwi Load:

```c
Load_Stat stat;
UInt32 hwiLoad;

Load_getGlobalHwiLoad(&stat);

hwiLoad = Load_calculateLoad(&stat);
```

### 9.3 Error Handling

A number of SYS/BIOS APIs—particularly those that create objects and allocate memory—have an argument that expects an Error_Block. This type is defined by the xdc.runtime.Error module provided by XDCtools. The following example shows the recommended way to declare and use an error block when creating a Swi:

```c
#include <xdc/std.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/System.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Swi.h>

Swi_Handle swi0;
Swi_Params swiParam;
Error_Block eb;

... Error_init(&eb);
Swi_Params_init(&swiParam);

swi0 = Swi_create(swiFunc, &swiParam, &eb);
if (swi0 == NULL) {
    System_abort("Swi create failed");
}
```

Notice that in the previous example, the test to determine whether to call System_abort() compares the value returned by Swi_create() to NULL as follows:

```c
if (swi0 == NULL) {
    System_abort("Swi create failed");
}
```

Most of the SYS/BIOS APIs that expect an error block also return a handle to the created object or the allocated memory. For this reason, it is simplest and provides the best performance to check the value returned by these APIs.
For APIs that get passed an Error_Block but do not return a handle or other status value, you can use the Error_check() function to test the Error_Block as in the following example, which calls Timer_reconfig() to reconfigure a statically-created Timer object:

```c
Timer_reconfig(myTimer, tickFxns, &timerParams, &eb);
if (Error_check(&eb)) {
    System_abort("Timer reconfigure failed");
}
```

You may see examples in other documentation that pass NULL in place of the Error_Block argument. If an error occurs when creating an object or allocating memory and NULL was passed instead of an Error_Block, the application aborts and a reason for the error is output using System_printf(). This may be the best behavior in systems where any error is fatal, and you do not want to do any error checking.

The advantage to passing and testing an Error_Block is that your program can have control over when it aborts. For example, instead of aborting when memory cannot be allocated, you might want to try to release other memory and try again or switch to a mode with more limited memory needs.

Note that the System_abort() function calls a hook function from its System.SupportProxy module (for example, xdc.runtime.SysStd). You might want to use a different abort() hook function that performs a reset and starts the application over.
9.4 Instrumentation Tools in Code Composer Studio

When you are debugging an application, there are several items in the Tools menu that are especially useful for debugging SYS/BIOS applications.

**Tools > Runtime Object View (ROV)** is a stop-mode debugging tool for use with TI-RTOS applications. For information, see the Runtime Object View (ROV) User’s Guide in TI Resource Explorer.

**Tools > RTOS Analyzer** provides several tools for examining the execution sequence of SYS/BIOS threads, CPU load analysis, logging of messages and errors, and task behavior. In order to use these commands, your application must have enabled the UIA module LoggingSetup. Once you enable the UIA package, SYS/BIOS thread execution and loads are instrumented by default. See System Analyzer User’s Guide (SPRUH43) for information about using the RTOS Analyzer tools.

**Tools > System Analyzer** allow you to access analysis tools to view additional instrumentation in tables and graphs. These tools require additional coding using the UIA C/C++ APIs. If you add additional UIA events to benchmark code and gather additional data, you can use these analysis tools. Multicore data correlation is also provided. For details about using System Analyzer, see the System Analyzer User’s Guide (SPRUH43).
If you installed TI-RTOS, UIA is available for use by your application. By default, UIA is disabled in the SYS/BIOS examples, but you can enable it by selecting the LoggingSetup module in the XGCONF configuration editor and checking the box to enable the module.

If you installed SYS/BIOS and UIA as standalone products (without installing TI-RTOS), you can enable UIA for a project as follows:

1. In the Project Explorer pane of CCS, right-click on the project and choose **Properties**.
2. In the Properties dialog, choose the **General** category, then the **RTSC** tab.
3. In the Products and Repositories area, check the box next to **System Analyzer (UIA Target)** and make sure the most recent version is selected. This causes your application to link with the necessary parts of UIA and makes the UIA modules available within XGCONF. (If you see a TI-RTOS item checked, UIA is already available to your project.)
4. Click **OK**.

UIA runtime data can be gathered via Ethernet for any target that has an NDK driver. Runtime data collection via JTAG is also provided for all C64x+ and C66x targets. Stop-mode data collection is provided for the additional targets listed in the UIA release notes.
9.5 Performance Optimization

This section provides suggestions for optimizing the performance of SYS/BIOS applications. This is accomplished in two ways: by using compiler and linker optimizations, and by optimizing the configuration of SYS/BIOS.

9.5.1 Configuring Logging

Logging can significantly impact the performance of a system. You can reduce the impact of logging by optimizing the configuration. There are two main ways to optimize the logging used in your application:

- **No logging.** In SYS/BIOS, logging is not enabled by default.
- **Optimizing logging.** If you need logging enabled in your application, there are some configuration choices you can make to optimize performance. These are described in the following subsections.

9.5.1.1 Diags Settings

There are four diagnostics settings for each diagnostics level: RUNTIME_OFF, RUNTIME_ON, ALWAYS_OFF, and ALWAYS_ON.

The two runtime settings (RUNTIME_OFF and RUNTIME_ON) allow you to enable or disable a particular diagnostics level at runtime. However, a check must be performed to determine whether logging is enabled or disabled every time an event is logged.

If you use ALWAYS_OFF or ALWAYS_ON instead, you cannot change the setting at runtime. The call will either be a direct call to log the event (ALWAYS_ON) or will be optimized out of the code (ALWAYS_OFF).

```javascript
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Diags = xdc.useModule('xdc.runtime.Diags');

/* 'RUNTIME' settings allow you to turn it off or on at runtime, 
* but require a check at runtime. */
Defaults.common$.diags_USER1 = Diags.RUNTIME_ON;
Defaults.common$.diags_USER2 = Diags.RUNTIME_OFF;

/* These settings cannot be changed at runtime, but optimize out 
* the check for better performance. */
Defaults.common$.diags_USER3 = Diags.ALWAYS_OFF;
Defaults.common$.diags_USER4 = Diags(ALWAYS_ON);
```

9.5.1.2 Choosing Diagnostics Levels

SYS/BIOS modules only log to two levels: USER1 and USER2. They follow the convention that USER1 is for basic events and USER2 is for more detail. To improve performance, you could only turn on USER1, or turn on USER2 for particular modules only. Refer to each module's documentation to see which events are logged as USER1 and which are logged as USER2.

9.5.1.3 Choosing Modules to Log

To optimize logging, enable logging only for modules that interest you for debugging. For example, Hwi logging tends to be the most expensive in terms of performance due to the frequency of hardware interrupts. Two Hwi events are logged on every Clock tick when the Clock's timer expires.
9.5.2 Configuring Diagnostics

By default, Asserts are enabled for all modules. SYS/BIOS uses asserts to check for common user mistakes such as calling an API with an invalid argument or from an unsupported context. Asserts are useful for catching coding mistakes that may otherwise lead to confusing bugs.

To optimize performance after you have done basic debugging of API calls, your configuration file can disable asserts as follows:

```
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Diags = xdc.useModule('xdc.runtime.Diags');

/* Disable asserts in all modules. */
Defaults.common$.diags_ASSERT = Diags.ALWAYS_OFF;
```

9.5.3 Choosing a Heap Manager

SYS/BIOS provides several different heap manager implementations. Each of these has various performance trade-offs when allocating and freeing memory. See Section 7.8 for a detailed discussion of the trade-offs of each module.

HeapMem can allocate a block of any size, but is the slowest of the three. HeapBuf can only allocate blocks of a single configured size, but is very quick. HeapMultiBuf manages a pool of HeapBuf instances and balances the advantages of the other two. HeapMultiBuf is quicker than HeapMem, but slower than HeapBuf. HeapMin uses minimal memory but cannot deallocate memory.

Consider also using different heap implementations for different roles. For example, HeapBuf is ideally suited for allocating a fixed-size object that is frequently created and deleted. If you were allocating and freeing many fixed sized data buffers, you could create a HeapBuf instance just for allocating these data buffers.

9.5.4 Hwi Configuration

The hardware interrupt dispatcher provides a number of features by default that add to interrupt latency. If your application does not require some of these features, you can disable them to reduce interrupt latency.

- **dispatcherAutoNestingSupport.** You may disable this feature if you don't need interrupts enabled during the execution of your Hwi functions.
- **dispatcherSwiSupport.** You may disable this feature if no Swi threads will be posted from any Hwi threads.
- **dispatcherTaskSupport.** You may disable this feature if no APIs are called from Hwi threads that would lead to a Task being scheduled. For example, Semaphore_post() would lead to a Task being scheduled.
- **dispatcherIrpTrackingSupport.** This feature supports the Hwi_getIrp() API, which returns an interrupt's most recent return address. You can disable this feature if your application does not use that API.

9.5.5 Stack Checking

By default, the Task module checks to see whether a Task stack has overflowed at each Task switch. To improve Task switching latency, you can disable this feature the Task.checkStackFlag property to false.
This appendix describes how to rebuild the SYS/BIOS source code.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>A.1 Overview</td>
<td>192</td>
</tr>
<tr>
<td>A.2 Prerequisites</td>
<td>192</td>
</tr>
<tr>
<td>A.3 Building SYS/BIOS Using the bios.mak Makefile</td>
<td>192</td>
</tr>
<tr>
<td>A.4 Building Your Project Using a Rebuilt SYS/BIOS</td>
<td>195</td>
</tr>
</tbody>
</table>
A.1 Overview

The SYS/BIOS product includes source files and build scripts that allow you to modify the SYS/BIOS sources and rebuild its libraries. You can do this in order to modify, update, or add functionality. If you edit the SYS/BIOS source code and/or corresponding build scripts, you must also rebuild SYS/BIOS in order to create new libraries containing these modifications.

Note that starting with SYS/BIOS v6.32, you can cause the SYS/BIOS libraries to be rebuilt as part of the application build within CCS. See Section 2.4.5 for details. The custom-built libraries will be stored with your CCS project and will contain only modules and APIs that your application needs to access. You can cause such a custom build to occur by configuring the BIOS.libType property as follows.

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.libType = BIOS.LibType_Custom;
```

Caution: This appendix provides details about rebuilding the SYS/BIOS source code. We strongly recommend that you copy the SYS/BIOS installation to a directory with a different name and rebuild that copy, rather than rebuilding the original installation.

For information about building SYS/BIOS applications (that is, applications that use SYS/BIOS), see Section 2.4.

A.2 Prerequisites

In order to rebuild SYS/BIOS, the SYS/BIOS and XDCtools products must both be installed. It is important to build SYS/BIOS with a compatible version of XDCtools. To find out which versions are compatible, see the "Dependencies" section of the Release Notes in the top-level directory of your SYS/BIOS installation.

Note: You should generally avoid installing the various Texas Instruments tools and source distributions in directories that have spaces in their paths.

A.3 Building SYS/BIOS Using the bios.mak Makefile

Rebuilding SYS/BIOS itself from the provided source files is straightforward, whether you are using the TI compiler toolchain or the GNU GCC toolchain.

SYS/BIOS ships with a bios.mak file in the top-level installation directory. This makefile enables you to easily (re)build SYS/BIOS using your choice of compilers and desired "targets". A target incorporates a particular ISA and a runtime model; for example, Cortex-M3 and the GCC compiler with specific options.

The instructions in this section can be used to build SYS/BIOS applications on Windows or Linux. If you are using a Windows machine, you can use the regular DOS command shell provided with Windows. However, you may want to install a Unix-like shell, such as Cygwin.

For Windows users, the XDCtools top-level installation directory contains gmake.exe, which is used in the commands that follow to run the Makefile. The gmake utility is a Windows version of the standard GNU "make" utility provided with Linux.

If you are using Linux, change the "gmake" command to "make" in the commands that follow.
For these instructions, suppose you have the following directories:

- `$BASE/sysbios/bios_6_##_##_##` — The location where you installed SYS/BIOS.
- `$BASE/sysbios/copy-bios_6_##_##_##` — The location of a copy of the SYS/BIOS installation.
- `$BASE/xdctools_3_##_##_##` — The location where you installed XDCtools.
- `$TOOLS/gcc/bin` — The location of a compiler, in this case a GCC compiler for M3.

The following steps refer to the top-level directory of the XDCtools installation as `<xdc_install_dir>`. They refer to the top-level directory of the copy of the SYS/BIOS installation as `<bioscopy_install_dir>`.

Follow these steps to rebuild SYS/BIOS:

1. If you have not already done so, install XDCtools and SYS/BIOS.
2. Make a copy of the SYS/BIOS installation that you will use when rebuilding. This leaves you with an unmodified installation as a backup. For example, use commands similar to the following on Windows:

   ```
   mkdir c:\sysbios\copy-bios_6_##_##_##
   copy c:\sysbios\bios_6_##_##_## c:\sysbios\copy-bios_6_##_##_##
   ```

   Or, use the a command similar to the following on Linux:

   ```
   cp -r $BASE/sysbios/bios_6_##_##_##/* $BASE/sysbios/copy-bios_6_##_##_##
   ```

3. Make sure you have access to compilers for any targets for which you want to be able be able to build applications using the rebuilt SYS/BIOS. Note the path to the directory containing the executable for each compiler. These compilers can include Texas Instruments compilers, GCC compilers, and any other command-line compilers for any targets supported by SYS/BIOS.

4. If you are using Windows and the gmake utility provided in the top-level directory of the XDCtools installation, you should add the `<xdc_install_dir>` to your PATH environment variable so that the gmake executable can be found.

5. You may remove the top-level doc directory located in `<bioscopy_install_dir>/docs` if you need to save disk space.

6. At this point, you may want to add the remaining files in the SYS/BIOS installation tree to your Software Configuration Management (SCM) system.
7. Open the `<bioscopy_install_dir>/bios.mak` file with a text editor, and make the following changes for any options you want to hardcode in the file. (You can also set these options on the command line if you want to override the settings in the `bios.mak` file.)

   — Ignore the following lines near the beginning of the file. These definitions are used internally, but few users will have a need to change them.

   ```
   # Where to install/stage the packages
   # Typically this would point to the devkit location
   DESSIDR ?= <UNDEFINED>
   
   prefix ?= /
   docdir ?= /docs/bios
   packagesdir ?= /packages
   ```

   — Specify the location of XDCtools. For example:

   ```
   XDC_INSTALL_DIR ?= $(BASE)/xdctools_3_30_##_##
   ```

   — Specify the location of the compiler executable for all targets you want to be able to build for with SYSBIOS. Use only the directory path; do not include the name of the executable file. Any targets for which you do not specify a compiler location will be skipped during the build. For example, on Linux you might specify the following:

   ```
   gnu.targets.arm.M3 ?= $TOOLS/gcc/bin
   ```

   Similarly, on Windows you might specify the following compiler locations:

   ```
   ti.targets.arm.elf.M3 ?= c:/ti/ccsv6/tools/compiler/tms470
   gnu.targets.arm.M3 ?= c:/tools/gcc/bin
   ```

   — If you need to add any repositories to your XDCPATH (for example, to reference the packages directory of another component), you should edit the XDCPATH definition.

   — You can uncomment the line that sets XDCOPTIONS to “v” if you want more information output during the build.

8. Clean the SYSBIOS installation with the following commands. (If you are running the build on Linux, change all "gmake" commands to "make".)

   ```
   cd <bioscopy_install_dir>
   gmake -f bios.mak clean
   ```

9. Run the `bios.mak` file to build SYSBIOS as follows. (Remember, if you are running the build on Linux, change all "gmake" commands to "make".)

   ```
   gmake -f bios.mak
   ```

10. If you want to specify options on the command line to override the settings in `bios.mak`, use a command similar to the following.

    ```
    gmake -f bios.mak XDC_INSTALL_DIR=<xdc_install_dir> gnu.targets.arm.M3=<compiler_path>
    ```
A.4 Building Your Project Using a Rebuilt SYS/BIOS

To build your application using the version of SYS/BIOS you have rebuilt, you must point your project to this rebuilt version by following these steps:

1. Open CCS and select the application project you want to rebuild.
2. Right-click on your project and choose Properties. If you have a configuration project that is separate from your application project, open the properties for the configuration project.
3. In the CCS General category of the Properties dialog, choose the RTSC tab.
4. Under the Products and Repositories tab, uncheck all the boxes for SYS/BIOS (and DSP/BIOS if there are any). This ensures that no version is selected.

5. Click the Add button next to the Products and Repositories tab.
6. Choose Select repository from file-system, and browse to the “packages” directory of the location where you copied and rebuilt SYS/BIOS. For example, the location may be C:\myBiosBuilds\custom_bios_6_##_##_##\packages on Windows or $BASE/sysbios/copy-bios_6_##_##_##/packages on Linux.

7. Click OK to apply these changes to the project.
8. You may now rebuild your project using the re-built version of SYS/BIOS.
This appendix describes the timing benchmarks for SYS/BIOS functions, explaining the meaning of the values as well as how they were obtained, so that designers may better understand their system performance. The application used to generate the following timing benchmarks can be built and run by using the source files within the \BIOS_INSTALL_DIR\packages\ti\sysbios\benchmarks directory in your SYS/BIOS installation.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>B.1 Timing Benchmarks</td>
<td>197</td>
</tr>
<tr>
<td>B.2 Interrupt Latency</td>
<td>197</td>
</tr>
<tr>
<td>B.3 Hwi-Hardware Interrupt Benchmarks</td>
<td>197</td>
</tr>
<tr>
<td>B.4 Swi-Software Interrupt Benchmarks</td>
<td>198</td>
</tr>
<tr>
<td>B.5 Task Benchmarks</td>
<td>199</td>
</tr>
</tbody>
</table>
B.1 Timing Benchmarks

This appendix describes the timing benchmarks for SYS/BIOS functions, explaining the meaning of the values as well as how they were obtained, so that designers may better understand their system performance.

The sections that follow explain the meaning of each of the timing benchmarks. The name of each section corresponds to the name of the benchmark in the actual benchmark data table.

The explanations in this appendix are best viewed along side the actual benchmark data. Since the actual benchmark data depends on the target and the memory configuration, and is subject to change, the data is provided in HTML files in the ti.sysbios.benchmarks package (that is, in the BIOS_INSTALL_DIR\packages\ti\sysbios\benchmarks directory).

The benchmark data was collected with the Build-Profile set to “release” and the BIOS.libType configuration parameter set to BIOS.LibType_Custom. See Section 2.4.5 for more on these settings.

B.2 Interrupt Latency

The Interrupt Latency benchmark is the maximum number of cycles during which the SYS/BIOS kernel disables maskable interrupts. Interrupts are disabled in order to modify data shared across multiple threads. SYS/BIOS minimizes this time as much as possible to allow the fastest possible interrupt response time.

The Interrupt Latency is measured within the context of a SYS/BIOS application containing SYS/BIOS API calls that internally disable interrupts. A timer is triggered to interrupt on each instruction boundary within the application, and the latency is calculated to be the time, in cycles, to vector to the first instruction of the interrupt dispatcher. Note that this is not the time to the first instruction of the user's ISR.

B.3 Hwi-Hardware Interrupt Benchmarks

Hwi_enable(). This is the execution time of a Hwi_enable() function call, which is used to globally enable hardware interrupts.

Hwi_disable(). This is the execution time of a Hwi_disable() function call, which is used to globally disable hardware interrupts.

Hwi dispatcher. These are execution times of specified portions of Hwi dispatcher code. This dispatcher handles running C code in response to an interrupt. The benchmarks provide times for the following cases:

• Interrupt prolog for calling C function. This is the execution time from when an interrupt occurs until the user's C function is called.

• Interrupt epilog following C function call. This is the execution time from when the user's C function completes execution until the Hwi dispatcher has completed its work and exited.
**Hardware interrupt to blocked task.** This is a measurement of the elapsed time from the start of an ISR that posts a semaphore, to the execution of first instruction in the higher-priority blocked task, as shown in Figure B–1.

![Figure B–1 Hardware Interrupt to Blocked Task](image)

**Hardware interrupt to software interrupt.** This is a measurement of the elapsed time from the start of an ISR that posts a software interrupt, to the execution of the first instruction in the higher-priority posted software interrupt.

This duration is shown in Figure B–2. Swi 2, which is posted from the ISR, has a higher priority than Swi 1, so Swi 1 is preempted. The context switch for Swi 2 is performed within the Swi executive invoked by the Hwi dispatcher, and this time is included within the measurement. In this case, the registers saved/restored by the Hwi dispatcher correspond to that of "C" caller saved registers.

![Figure B–2 Hardware Interrupt to Software Interrupt](image)

### B.4 Swi-Software Interrupt Benchmarks

**Swi_enable().** This is the execution time of a Swi_enable() function call, which is used to enable software interrupts.

**Swi_disable().** This is the execution time of a Swi_disable() function call, which is used to disable software interrupts.

**Swi_post().** This is the execution time of a Swi_post() function call, which is used to post a software interrupt. Benchmark data is provided for the following cases of Swi_post():

- Task 1 executing Semaphore_post() Task 2 executing
- Task 1 executing Swi_post() Task 2 executing

- Interrupt asserted
- Interrupt response
- Hwi dispatcher prolog
- Semaphore_post()
- Hwi dispatcher epilog
- Task 2 executing

- Interrupt asserted
- Interrupt response
- Swi 1 executing
- Hwi dispatcher
- Swi_post()
- Hwi dispatcher
- Swi 2 executing

- Hardware Interrupt to Blocked Task
- Hardware Interrupt to Software Interrupt
• **Post software interrupt again.** This case corresponds to a call to Swi_post() of a Swi that has already been posted but hasn't started running as it was posted by a higher-priority Swi. Figure B–3 shows this case. Higher-priority Swi1 posts lower-priority Swi2 twice. The cycle count being measured corresponds to that of second post of Swi2.

<table>
<thead>
<tr>
<th>Swi 1 executing</th>
<th>Swi_post() of Swi 2</th>
<th>Swi 1 executing</th>
<th>Swi_post() of Swi 2 again</th>
<th>Swi 1 executing</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

→ Time

Post a Swi that has already been posted

*Figure B–3 Post of Software Interrupt Again*

• **Post software interrupt, no context switch.** This is a measurement of a Swi_post() function call, when the posted software interrupt is of lower priority then currently running Swi. Figure B–4 shows this case.

<table>
<thead>
<tr>
<th>Swi 1 executing</th>
<th>Swi_post() of Swi 2</th>
<th>Swi 1 executing</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

→ Time

Post Software Interrupt, No Context Switch

*Figure B–4 Post Software Interrupt without Context Switch*

• **Post software interrupt, context switch.** This is a measurement of the elapsed time between a call to Swi_post() (which causes preemption of the current Swi) and the execution of the first instruction in the higher-priority software interrupt, as shown in Figure B–5. The context switch to Swi2 is performed within the Swi executive, and this time is included within the measurement.

<table>
<thead>
<tr>
<th>Swi 1 executing</th>
<th>Swi_post() of Swi 2</th>
<th>Swi 2 executing</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

→ Time

Post Software Interrupt, Context Switch

*Figure B–5 Post Software Interrupt with Context Switch*

**B.5 Task Benchmarks**

**Task_enable().** This is the execution time of a Task_enable() function call, which is used to enable SYS/BIOS task scheduler.

**Task_disable().** This is the execution time of a Task_disable() function call, which is used to disable SYS/BIOS task scheduler.

**Task_create().** This is the execution time of a Task_create() function call, which is used to create a task ready for execution. Benchmark data is provided for the following cases of Task_create():

---

**Timing Benchmarks**

Submit Documentation Feedback
- **Create a task, no context switch.** The executing task creates and readies another task of lower or equal priority, which results in no context switch. See Figure B–6.

  ```
  Task 1 executing  Task_create() (Readies lower priority new Task 2)  Task 1 executing
  ────────────────  ───────────────────────────────────────────────────  ────────────────
  Time
  ────────────────
  Create a Task, No Context Switch
  ```

*Figure B–6 Create a New Task without Context Switch*

- **Create a task, context switch.** The executing task creates another task of higher priority, resulting in a context switch. See Figure B–7.

  ```
  Task 1 executing  Task_create() (Readies higher priority new Task 2, Task Context Switch)  Task 2 executing
  ────────────────  ───────────────────────────────────────────────────  ────────────────
  Time
  ────────────────
  Create a Task, Context Switch
  ```

*Figure B–7 Create a New Task with Context Switch*

**Note:** The benchmarks for Task_create() assume that memory allocated for a Task object is available in the first free list and that no other task holds the lock to that memory. Additionally the stack has been pre-allocated and is being passed as a parameter.

- **Task_delete().** This is the execution time of a Task_delete() function call, which is used to delete a task. The Task handle created by Task_create() is passed to the Task_delete() API.

- **Task_setPri().** This is the execution time of a Task_setPri() function call, which is used to set a task's execution priority. Benchmark data is provided for the following cases of Task_setPri():
  - **Set a task priority, no context switch.** This case measures the execution time of the Task_setPri() API called from a task Task1 as in Figure B–8 if the following conditions are all true:
    - Task_setPri() sets the priority of a lower-priority task that is in ready state.
    - The argument to Task_setPri() is less than the priority of the current running task.

  ```
  Task 1 executing  Task_setPri()  Task 1 executing
  ────────────────  ────────────────  ────────────────
  Time
  ────────────────
  Set a Task Priority, No Context Switch
  ```

*Figure B–8 Set a Task's Priority without a Context Switch*
• **Lower the current task's own priority, context switch.** This case measures execution time of Task_setPri() API when it is called to lower the priority of currently running task. The call to Task_setPri() would result in context switch to next higher-priority ready task. Figure B–9 shows this case.

![Lower the Current Task's Own Priority, Context Switch](image)

**Figure B–9 Lower the Current Task's Priority, Context Switch**

• **Raise a ready task's priority, context switch.** This case measures execution time of Task_setPri() API called from a task Task1 if the following conditions are all true:
  — Task_setPri() sets the priority of a lower-priority task that is in ready state.
  — The argument to Task_setPri() is greater then the priority of current running task.

The execution time measurement includes the context switch time as shown in Figure B–10.

![Raise a Ready Task's Priority, Context Switch](image)

**Figure B–10 Raise a Ready Task's Priority, Context Switch**

• **Task_yield().** This is a measurement of the elapsed time between a function call to Task_yield() (which causes preemption of the current task) and the execution of the first instruction in the next ready task of equal priority, as shown in Figure B–11.

![Task Yield](image)

**Figure B–11 Task Yield**

### B.6 Semaphore Benchmarks

Semaphore benchmarks measure the time interval between issuing a Semaphore_post() or Semaphore_pend() function call and the resumption of task execution, both with and without a context switch.

**Semaphore_post().** This is the execution time of a Semaphore_post() function call. Benchmark data is provided for the following cases of Semaphore_post():
• **Post a semaphore, no waiting task.** In this case, the Semaphore_post() function call does not cause a context switch as no other task is waiting for the semaphore. This is shown in Figure B–12.

![Figure B–12 Post Semaphore, No Waiting Task](image)

• **Post a semaphore, no context switch.** This is a measurement of a Semaphore_post() function call, when a lower-priority task is pending on the semaphore. In this case, Semaphore_post() readies the lower-priority task waiting for the semaphore and resumes execution of the original task, as shown in Figure B–13.

![Figure B–13 Post Semaphore, No Context Switch](image)

• **Post a semaphore, context switch.** This is a measurement of the elapsed time between a function call to Semaphore_post() (which readies a higher-priority task pending on the semaphore causing a context switch to higher-priority task) and the execution of the first instruction in the higher-priority task, as shown in Figure B–14.

![Figure B–14 Post Semaphore with Task Switch](image)

**Semaphore_pend().** This is the execution time of a Semaphore_pend() function call, which is used to acquire a semaphore. Benchmark data is provided for the following cases of Semaphore_pend():

• **Pend on a semaphore, no context switch.** This is a measurement of a Semaphore_pend() function call without a context switch (as the semaphore is available.) See Figure B–15.

![Figure B–15 Pend on Semaphore, No Context Switch](image)
• **Pend on a semaphore, context switch.** This is a measurement of the elapsed time between a function call to Semaphore_pend() (which causes preemption of the current task) and the execution of first instruction in next higher-priority ready task. See Figure B–16.

<table>
<thead>
<tr>
<th>Time</th>
<th>Task 1 executing</th>
<th>(Task 1 suspends, Task Context Switch)</th>
<th>Task 2 executing</th>
</tr>
</thead>
</table>

*Figure B–16  Pend on Semaphore with Task Switch*
This appendix describes SYS/BIOS size benchmark statistics.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>C.1 Overview</td>
<td>205</td>
</tr>
<tr>
<td>C.2 Constructed Application Sizes</td>
<td>206</td>
</tr>
<tr>
<td>C.3 Created Module Application Sizes</td>
<td>209</td>
</tr>
<tr>
<td>C.4 POSIX Application Sizes</td>
<td>211</td>
</tr>
</tbody>
</table>
C.1 Overview

The size benchmarks are a series of applications that are built on top of one another. Moving down Table C–1, each application includes all of the configuration settings and API calls in the previous applications. All of the test applications, by default, include the commonly used Hwi and Swi modules. In turn, this pulls in all related code for that module while excluding most other non-dependent module code.

The applications are split into three categories:

- Those which construct SYS/BIOS objects
- Those which create SYS/BIOS objects
- Those which create POSIX objects.

Applications lower on the table generally require the modules in the applications above them, (The Semaphore module, for example, requires the Task module.) This progression allows for measuring the size impact of a module by subtracting the sizes of all of the other modules it depends upon. The data in the table, however, are provided in absolute numbers.

In addition to the test application size benchmarks, module-specific size benchmarks are provided. These benchmarks demonstrate how much memory is necessary to create a single instance of a specific SYS/BIOS object.

The actual size benchmark data are included in the SYS/BIOS 6 installation in the ti.sysbios.benchmarks package (that is, in the BIOS_INSTALL_DIR\packages\ti\sysbios\benchmarks\doc-files directory). There is a separate HTML file for each target that can be reached through the benchmarks.html page. The sections that follow should be read alongside the actual sizing information as a reference.

The benchmark data was collected with the Build-Profile set to “release” and the BIOS.libType configuration parameter set to BIOS.LibType_Custom. See Section 2.4.5, Compiler and Linker Optimization for more on these settings.

For each benchmark application, the table provides four pieces of sizing information, all in 8-bit or 16-bit bytes for the C28x devices.

- **Code Size** is the total size of all of the code in the final executable.
- **Initialized Data Size** is the total size of all constants (the size of the .const section).
- **Uninitialized Data Size** is the total size of all variables.
- **C-Initialization Size** is the total size of C-initialization records code built against the TI compiler.

<table>
<thead>
<tr>
<th>Constructed Applications</th>
<th>Created Applications</th>
<th>POSIX Applications</th>
</tr>
</thead>
<tbody>
<tr>
<td>Task</td>
<td>Task</td>
<td>Pthread</td>
</tr>
<tr>
<td>Semaphore</td>
<td>Semaphore</td>
<td>Semaphore</td>
</tr>
<tr>
<td>Mutex</td>
<td>Mutex</td>
<td>Mutex</td>
</tr>
<tr>
<td>Clock</td>
<td>Clock</td>
<td>Timer</td>
</tr>
</tbody>
</table>

Table C–1 SYS/BIOS 6 Benchmark Applications
C.2 Constructed Application Sizes

Each application builds on top of the applications above it in Table C–1 and for each module there are generally two benchmarks: the application size and the amount of memory taken up by the modules’ object. The following applications construct their objects and do not use any memory allocation.

Note: All of the following applications include the Hwi and Swi Modules within their respective configuration files.

The code snippets for each application apply to all targets, except where noted.

C.2.1 Constructed Task Application

The Task Application configuration script enables Task scheduling while attempting to minimize code size by disabling the task stack check debug feature. The script also reduces the number of allowed priorities task, to reduce RAM usage.

Configuration Script Addition

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
var Task = xdc.useModule('ti.sysbios.knl.Task');

BIOS.taskEnabled = true;
Task.checkStackFlag = false;
Task.numPriorities = 2;
```

C Code Addition

```c
Task_Struct taskStruct;
Task_Handle taskHandle;
Task_Params taskParams;

Task_Params_init(&taskParams);
taskParams.stackSize = TASKSTACKSIZE;
taskParams.stack = &taskStack;

Task_construct(&taskStruct, twoArgsFx, &taskParams, NULL);
taskHandle = Task_handle(&taskStruct);

Task_getPri(taskHandle);
Task_destruct(&taskStruct);
```
C.2.2 Constructed Semaphore Application

This application also includes the constructed Task application described in Section C.2.1. The Semaphore Application configuration script disables support for Events in the Semaphore in order to reduce code size.

Configuration Script Addition

```javascript
var Sem = xdc.useModule('ti.sysbios.knl.Semaphore');
Sem.supportsEvents = false;
```

C Code Addition

```c
Semaphore_Struct semStruct;
Semaphore_Handle semHandle;

Semaphore_construct(&semStruct, 0, NULL);
semHandle = Semaphore_handle(&semStruct);

Semaphore_post(semHandle);
Semaphore_pend(semHandle, BIOS_WAIT_FOREVER);
Semaphore_destruct(&semStruct);
```

C.2.3 Constructed Mutex Application

This application includes the constructed Semaphore application described in Section C.2.2. No Mutex module specific configuration settings were changed in this application.

Configuration Script Addition

```javascript
var GateMutex = xdc.useModule('ti.sysbios.gates.GateMutex');
```

C Code Addition

```c
GateMutex_Struct mutexStruct;
GateMutex_Handle mutexHandle;
IArg mutexKey;

GateMutex_construct(&mutexStruct, NULL);
mutexHandle = GateMutex_handle(&mutexStruct);

mutexKey = GateMutex_enter(mutexHandle);
GateMutex_leave(mutexHandle, mutexKey);
GateMutex_destruct(&mutexStruct);
```
C.2.4 Constructed Clock Application

This application includes the Mutex application described in Section C.2.3.

The Clock Application configuration script enables the SYS/BIOS Clock services, this has the effect of registering a timer Hwi to generate the system tick.

Configuration Script Addition

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
var Clock = xdc.useModule('ti.sysbios.knl.Clock');

BIOS.clockEnabled = true;
```

C Code Addition

```c
Clock_Struct clockStruct;
Clock_Handle clockHandle;

Clock_construct(&clockStruct, oneArgFxn, 10, NULL);
clockHandle = Clock_handle(&clockStruct);

Clock_start(clockHandle);
Clock_stop(clockHandle);
Clock_destruct(&clockStruct);
```
C.3 Created Module Application Sizes

The created applications demonstrate the size effects of dynamically creating objects (in the C code). The configuration files are identical to their constructed counterparts, as seen in Section C.2, apart from enabling run-time creation by including and setting the following:

```c
/* Allow for run-time creates/memory allocation */
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Types = xdc.useModule('xdc.runtime.Types');

Defaults.common$.memoryPolicy = Types.CREATE_POLICY;
BIOS.runtimeCreatesEnabled = true;
```

The .c files are also identical to their constructed counterparts apart from the <Module>_construct() calls being replaced with <Module>_create calls.

---

**Note:** All of the applications that use <Module>_create() internally pull in code from the Memory module to allocate space for the new object instances.

---

C.3.1 Created Task Application

The Created Task Application demonstrates the size impact of dynamically creating and deleting a Task instance.

**C Code Addition**

```c
Task_Handle dynamicTask;
Task_Params taskParams;

Task_Params_init(&taskParams);
taskParams.stackSize = TASKSTACKSIZE;
taskParams.stack = &taskStack;

dynamicTask = Task_create(twoArgsFx, &taskParams, NULL);

Task_getPri(dynamicTask);
Task_delete(&dynamicTask);
```
C.3.2 Created Semaphore Application

The Created Semaphore Application demonstrates the size impact of dynamically creating and deleting a Semaphore instance.

C Code Addition

```c
Semaphore_Handle dynamicSem;
dynamicSem = Semaphore_create(0, NULL, NULL);
Semaphore_post(dynamicSem);
Semaphore_pend(dynamicSem, BIOS_WAIT_FOREVER);
Semaphore_delete(&dynamicSem);
```

C.3.3 Created Mutex Application

The Created Mutex Application demonstrates the size impact of dynamically creating and deleting a GateMutex instance.

C Code Addition

```c
GateMutex_Handle dynamicMutex;
IArg dynamicMutexKey;
dynamicMutex = GateMutex_create(NULL, NULL);
dynamicMutexKey = GateMutex_enter(dynamicMutex);
GateMutex_leave(dynamicMutex, dynamicMutexKey);
GateMutex_delete(&dynamicMutex);
```

C.3.4 Created Clock Application

The Created Clock Application demonstrates the size impact of dynamically creating and deleting a Clock instance.

C Code Addition

```c
Clock_Handle dynamicClock;
dynamicClock = Clock_create(oneArgFxn, 10, NULL, NULL);
Clock_start(dynamicClock);
Clock_stop(dynamicClock);
Clock_delete(&dynamicClock);
```
C.4 POSIX Application Sizes

The POSIX size benchmarks use a set of applications that dynamically create their respective objects.

C.4.1 POSIX Pthread Application

The POSIX Pthread Application demonstrates the size impact of dynamically initializing and creating a POSIX thread. Since Pthreads are built on top of the native SYS/BIOS Task module, their size can be compared to benchmarks for the Created Task application.

C Code Addition

```c
pthread_t threadHandle = (pthread_t)NULL;
int threadArg = 1;
UInt32 retVal;

/* Set up stack buffer, pthread_create calls Task_create internally */
pthread_attr_t pattrs;
pthread_attr_init(&pattrs);
pthread_attr_setstack(&pattrs,(void *)taskStack, TASKSTACKSIZE);
pthread_create(&threadHandle, &pattrs, threadFxn, (void *)&threadArg);
pthread_join(threadHandle, (void **)&retVal);
```

C.4.2 POSIX Semaphore Application

The POSIX Semaphore Application demonstrates the size impact of dynamically creating an instance of a POSIX Semaphore. This application includes the PThread module. Its size can be compared to benchmarks for the Created Semaphore application.

C Code Addition

```c
static sem_t sem1;
sem_init(&sem1, 0, 0);
sem_trywait(&sem1);
sem_destroy(&sem1);
```
C.4.3  POSIX Mutex Application

The POSIX Mutex Application demonstrates the size impact of dynamically creating an instance of a POSIX Mutex. This application includes the PThread module. Its size can be compared to benchmarks for the Created Mutex application.

C Code Addition

```c
int getVal;
pthread_mutexattr_t attrs;

/* Create minimal mutex (No priority) */
pthread_mutexattr_setprotocol(&attrs, PTHREAD_PRIO_NONE);
pthread_mutex_init(&mutex, &attrs);

pthread_mutexattr_getprotocol(&attrs, &getVal);

pthread_mutexattr_destroy(&attrs);
pthread_mutex_destroy(&mutex);
```

C.4.4  POSIX Timer Application

The POSIX Timer Application demonstrates the size impact of dynamically creating an instance of a POSIX Timer. This application includes the PThread module. Its size can be compared to benchmarks for the Created Clock application.

C Code Addition

```c
sigevent    sev;
timer_t     timer;

timer_create(CLOCK_MONOTONIC, &sev, &timer);
```
This appendix describes how to minimize the size of a SYS/BIOS application.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>D.1 Overview</td>
<td>214</td>
</tr>
<tr>
<td>D.2 Reducing Data Size</td>
<td>214</td>
</tr>
<tr>
<td>D.3 Reducing Code Size</td>
<td>216</td>
</tr>
<tr>
<td>D.4 Basic Size Benchmark Configuration Script</td>
<td>218</td>
</tr>
</tbody>
</table>
D.1 Overview

This section provides tips and suggestions for minimizing the memory requirements of a SYS/BIOS-based application. This is accomplished by disabling features of the operating system that are enabled by default and by reducing the size of certain buffers in the system.

Most of the tips described here are used in the “Kernel only” configuration for the size benchmarks. The final section of this chapter presents a full configuration script that minimizes memory requirements.

The actual size benchmark data is included in the SYS/BIOS 6 installation in the ti.sysbios.benchmarks package (that is, in the BIOS_INSTALL_DIR\packages\ti\sysbios\benchmarks directory). There is a separate HTML file for each target. For example, the 'C64x sizing information can be found in the c6400Sizing.html file.

The following sections simply describe different configuration options and their effect on reducing the application size. For further details on the impact of these settings, refer to the documentation for the relevant modules.

Because the code and data sections are often placed in separate memory segments, it may be more important to just reduce either code size data size. Therefore the suggestions are divided based on whether they reduce code or data size. In general, it is easier to reduce data size than code size.

D.2 Reducing Data Size

D.2.1 Removing the malloc Heap

Calls to malloc are satisfied by a separate heap, whose size is configurable. The following code removes the heap to minimize to data footprint. Applications that remove the heap cannot dynamically allocate memory. Therefore, such applications should not use the SYS/BIOS Memory module APIs or other SYS/BIOS APIs that internally allocate memory from a heap.

```c
BIOS.heapSize = 0;
```

D.2.2 Reducing the Size of Stacks

See Section 3.5.3 for information about system stack size requirements and Section 3.6.3 for information about task stack size requirements.

The Program variable is automatically available to all scripts. It defines the "root" of the configuration object model. It comes from the xdc.cfg.Program module, and is implicitly initialized as follows:

```c
var Program = xdc.useModule('xdc.cfg.Program');
```

The size of the System stack, which is used as the stack for interrupt service routines (Hwis and Swis), is configurable, and can be reduced depending on the application’s needs.

```c
Program.stack = 1024;
```
Likewise, the size of each Task stack is individually configurable, and can be reduced depending on the application's needs. See Section 3.6.3 for information about task stack sizes.

```javascript
var tskParams = new Task.Params;
tskParams.stackSize = 512;
var task0 = Task.create('&task0Fxn', tskParams);
```

You can configure the size of the Idle task stack as follows:

```javascript
Task.idleTaskStackSize = 512;
```

### D.2.3 Setting the Default Task Stack Size

Task.defaultStackSize specifies the size of the task stacks if no size is provided in the Task creation parameters:

```javascript
Task.defaultStackSize = 1024;
```

### D.2.4 Disabling Named Modules

The space used to store module name strings can be reclaimed with the following configuration setting:

```javascript
var Defaults = xdc.useModule('xdc.runtime.Defaults');
Defaults.common$.namedModule = false;
```

### D.2.5 Leaving Text Strings Off the Target

By default, all of the text strings in the system, such as module and instance names and error strings, are loaded into the target's memory. These strings can be left out of the application using the following settings.

```javascript
var Text = xdc.useModule('xdc.runtime.Text');
Text.isLoaded = false;
```

### D.2.6 Reduce the Number of atexit Handlers

By default, up to 8 System_atexit() handlers can be specified that will be executed when the system is exiting. You can save data space by reducing the number of handlers that can be set at runtime to the number you are actually intending to use. For example:

```javascript
System.maxAtexitHandlers = 0;
```
D.3 Reducing Code Size

D.3.1 Use the Custom Build SYS/BIOS Libraries

Set the Build-Profile (shown when you are creating a new CCS project or modifying the CCS General > RTSC settings) to “release.”

In the configuration settings, change the BIOS.libType parameter to BIOS.LibType_Custom. This causes the SYS/BIOS libraries to be recompiled with optimizations that reduce code size.

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.libType = BIOS.LibType_Custom;
```

See Section 2.4.5 for more on these settings.

D.3.2 Disabling Logging

Logging and assertion handling can be disabled with the following configuration settings:

```javascript
BIOS.assertsEnabled = false;
BIOS.logsEnabled = false;
```

D.3.3 Setting Memory Policies

The BIOS module supports two methods for creating and deleting objects. If all of the objects in an application can be statically created in the configuration script, then all of the code associated with dynamically creating instances of modules can be left out of the application. This is referred to as a static memory policy.

```javascript
BIOS.runtimeCreatesEnabled = false;
```

D.3.4 Disabling Core Features

Some of the core features of SYS/BIOS can be enabled or disabled as needed. These include the Swi, Clock, and Task modules.

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.swiEnabled = false;
BIOS.clockEnabled = false;
BIOS.taskEnabled = false;
```

Applications typically enable at least one of the Swi and Task handlers. Some applications may not need to use both Swi and Task, and can disable the unused thread type.
D.3.5 Eliminating printf()

There is no way to explicitly remove printf from the application. However, printf code and related data structures are not included if an application has no references to System_printf(). This requires:

- The application code cannot contain any calls to System_printf().
- The following configuration settings need to be made to SYS/BIOS:

```javascript
var System = xdc.useModule('xdc.runtime.System');
var SysMin = xdc.useModule('xdc.runtime.SysMin');
SysMin.bufSize = 0;
SysMin.flushAtExit = false;
System.SupportProxy = SysMin;

//Remove Error_raiseHook, which brings System_printf
var Error = xdc.useModule('xdc.runtime.Error');
Error.raiseHook = null;
```

See the module documentation for details. Essentially, these settings will eliminate all references to the printf code.

D.3.6 Disabling RTS Thread Protection

If an application does not require the RTS library to be thread safe, it can specify to not use any Gate module in the RTS library. This can prevent the application from bringing in another type of Gate module.

```javascript
var BIOS = xdc.useModule('ti.sysbios.BIOS');
BIOS.rtsGateType = BIOS.NoLocking;
```

D.3.7 Disable Task Stack Overrun Checking

If you are not concerned that any of your Task instances will overrun their stacks, you can disable the checks that make sure the top of the stack retains its initial value. This saves on code space. See Section 3.6.3 for information about task stack sizes.

By default, stack checking is performed. Use these statements if you want to disable stack checking for all Tasks:

```javascript
Task.checkStackFlag = false;
Task.initStackFlag = false;
```

D.3.8 Cortex-M3/M4 Exception Management

The Cortex-M exception module uses System_printf() to send useful information about an exception. This is useful when debugging, but it uses some code and data space. If you set M3Hwi.excHandler to "null", the exception module will simply spin in an infinite loop when an exception occurs.

```javascript
if ((Program.build.target.name == "M3") ||
    (Program.build.target.name == "M4") ||
    (Program.build.target.name == "M4F")) {
    M3Hwi = xdc.useModule('ti.sysbios.family.arm.m3.Hwi');
    M3Hwi.excHandlerFunc = null;
}
```
D.4 Basic Size Benchmark Configuration Script

The basic size benchmark configuration script puts together all of these concepts to create an application that is close to the smallest possible size of a SYS/BIOS application.

Note that in a real-world application, you would want to enable at least either Swi or Task handlers so that your application could use some threads.

This configuration script works on any target.

```javascript
var Defaults = xdc.useModule('xdc.runtime.Defaults');
var Diags = xdc.useModule('xdc.runtime.Diags');
var Error = xdc.useModule('xdc.runtime.Error');
var Main = xdc.useModule('xdc.runtime.Main');
var SysMin = xdc.useModule('xdc.runtime.SysMin');
var System = xdc.useModule('xdc.runtime.System');
var Text = xdc.useModule('xdc.runtime.Text');

var BIOS = xdc.useModule('ti.sysbios.BIOS');
var Hwi = xdc.useModule('ti.sysbios.hal.Hwi');
var Task = xdc.useModule('ti.sysbios.knl.Task');

/*
 * Keep module names from being loaded on the target.
 * The module name strings are placed in the .const section. Setting this
 * parameter to false will save space in the .const section. Error and
 * Assert messages will contain an "unknown module" prefix instead
 * of the actual module name.
 */
Defaults.common$.namedModule = false;

/*
 * Minimize exit handler array in System. The System module includes
 * an array of functions that are registered with System_atexit() to be
 * called by System_exit().
 */
System.maxAtexitHandlers = 4;

/*
 * Disable the Error print function.
 * We lose error information when this is disabled since the errors are
 * not printed. Disabling the raiseHook will save some code space if
 * your app is not using System_printf() since the Error_print() function
 * calls System_printf().
 */
Error.raiseHook = null;
```
/*
 * Keep Error, Assert, and Log strings from being loaded on the target.
 * These strings are placed in the .const section.
 * Setting this parameter to false will save space in the .const section.
 * Error, Assert and Log message will print raw ids and args instead of
 * a formatted message.
 */
Text.isLoaded = false;

/*
 * Disable the output of characters by SysMin when the program exits.
 * SysMin writes characters to a circular buffer.
 * This buffer can be viewed using the SysMin Output view in ROV.
 */
SysMin.flushAtExit = false;

/* Circular buffer size for System_printf() */
SysMin.bufSize = 128;
System.SupportProxy = SysMin;

/*
 * The BIOS module will create the default heap for the system if this
 * value is non-zero.
 */
BIOS.heapSize = 0;

/*
 * Tune the stack sizes.
 */
Program.stack = 1024;   /* System stack size (used by ISRs and Swis) */
Task.defaultStackSize = 1024;
Task.idleTaskStackSize = 1024;
/*
 * Build a custom BIOS library. The custom library will be smaller than the
 * "instrumented" (default) and "non-instrumented" libraries.
 *
 * The BIOS.logsEnabled parameter specifies whether the Logging is enabled
 * within BIOS for this custom build. These logs are used by the RTA and
 * UIA analysis tools.
 *
 * The BIOS.assertsEnabled parameter specifies whether BIOS code will
 * include Assert() checks. Setting this parameter to 'false' will generate
 * smaller and faster code, but having asserts enabled is recommended for
 * early development as the Assert() checks will catch lots of programming
 * errors (invalid parameters, etc.)
 */
BIOS.libType = BIOS.LibType_Custom;
BIOS.logsEnabled = false;
BIOS.assertsEnabled = false;

/*
 * Disable runtime ModXyz_create(). This can only be used in static systems
 * where all objects are created in the configuration tool.
 */
BIOS.runtimeCreatesEnabled = false;

/*
 * Disable key features of BIOS. It would be unusual to set all of these
 * to false as this would disable all of the thread types except Hwi.
 */
BIOS.swiEnabled = false;
BIOS.taskEnabled = false;
BIOS.clockEnabled = false;

/*
 * Do not provide any thread protection for the compiler runtime library.
 */
BIOS.rtsGateType = BIOS.NoLocking;

/*
 * By default, the idle task checks the ISR and system stack to make sure that
 * they have not overflowed. The task scheduler also checks the task stacks to
 * make sure that they have not overflowed. You can eliminate this code by
 * setting the following variables to false.
 */
Hwi.checkStackFlag = false;
Task.checkStackFlag = false;
Task.initStackFlag = false;
/*
 * Set the error and abort functions to simple spin loops.
 */
System.abortFxn = System.abortSpin;
System.exitFxn = System.exitSpin;
Error.policyFxn = Error.policySpin;

/*
 * The Cortex-M exception module uses System_printf() to dump a lot of
 * useful information about the exception. This is very useful while
 * debugging but takes up some code and data space. If you set
 * M3Hwi.excHandler to 'null', the exception module will simply spin
 * in an infinite loop when and exception occurs.
 */
if ((Program.build.target.name == "M3") ||
    (Program.build.target.name == "M4") ||
    (Program.build.target.name == "M4F")) {
    M3Hwi = xdc.useModule('ti.sysbios.family.arm.m3.Hwi');
    M3Hwi.excHandlerFunc = null;
}
This chapter describes modules that can be used to handle input and output data.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>E.1 GIO Drivers and TI-RTOS</td>
<td>223</td>
</tr>
<tr>
<td>E.2 Overview of the GIO Model</td>
<td>223</td>
</tr>
<tr>
<td>E.3 Configuring Drivers in the Device Table</td>
<td>224</td>
</tr>
<tr>
<td>E.4 Using GIO APIs</td>
<td>227</td>
</tr>
<tr>
<td>E.5 Using GIO in Various Thread Contexts</td>
<td>235</td>
</tr>
<tr>
<td>E.6 GIO and Synchronization Mechanisms</td>
<td>237</td>
</tr>
</tbody>
</table>
E.1 GIO Drivers and TI-RTOS

The GIO model was used for DSP-side device drivers. This model is deprecated, but is still provided with SYS/BIOS for use in legacy applications.

The GIO model is not compatible with TI-RTOS drivers. If you are using TI-RTOS, see the SimpleLink 
MCU SDK User's Guide (an HTML file at the top of the installation) to learn about TI Drivers.

E.2 Overview of the GIO Model

This chapter describes how to use the GIO (General Purpose I/O Manager) module (ti.sysbios.io.GIO) and drivers written to implement the IOM interface for input and output. You use the GIO module to send data to an output channel or receive data from an input channel.

The GIO driver model is intended for use only by SYS/BIOS applications that do not use TI-RTOS drivers.

The GIO module is an abstraction of device drivers. It allows you to develop applications that do not directly interact with driver code. This allows the application to be developed in a driver-independent and platform-independent manner.

The GIO module also allows your application to process one buffer while the driver is working on another buffer. This improves performance and minimizes the copying of data. The typical use case for the GIO module is that an application gets a buffer of data from an input channel, processes the data and sends the data to an output channel.

The following figure shows a typical flow of data for an output channel:

In this use case, the application calls GIO module APIs. Those APIs manage buffers and the driver abstraction. Internally, the GIO module APIs calls the implementation of the IOM interface for the driver. As this figure shows, the high-level application does not interact with the drivers directly. Instead, the application uses the GIO module to interface with the drivers.
IOM drivers implement the IOM interface in `<bios_install_dir>/packages/ti/sysbios/io/IOM.h` to manage a peripheral. Such drivers are provided in some certain other software components (for example, the PSP and SDK products), or you can write your own. See Appendix F for details about the IOM interface. IOM drivers are sometimes called "mini-drivers".

The are two types of objects that applications must create in order to use GIO channels and IOM drivers:

- **DEV table**: The DEV module maintains a table of IOM driver names for use by the GIO module. You can manage this table statically using the XGCONF configuration tool or at runtime using C APIs. See Section E.3.
- **GIO channels**: The GIO module manages GIO objects that correspond to input and output channels. You can create GIO channels using C APIs, but they cannot be created statically. See Section E.4.2.

The GIO module supports both synchronous (blocking) APIs and asynchronous (non-blocking) APIs:

- **Synchronous APIs** will yield to other threads while waiting for a buffer to be ready or for the timeout to occur. GIO calls this model GIO_Model_STANDARD. APIs can only be used in the context of Task threads, because Hwi and Swi threads cannot use blocking calls. The main GIO APIs used with the synchronous model are GIO_read and GIO_write. In addition, drivers typically implement GIO_submit, GIO_flush, GIO_abort, and GIO_control as synchronous APIs.
- **Asynchronous APIs** do not yield to other threads. If the buffer is not ready, these functions return a failure status. GIO calls this model GIO_Model_ISSUERECLAIM. APIs can be used in any thread context. The main GIO APIs used with the asynchronous model are GIO_issue, GIO_reclaim, and GIO_prime.

### E.3 Configuring Drivers in the Device Table

The DEV module manages a table of IOM drivers. This a table of devices that can be found by name, allowing GIO channels to be created using the name. To configure the driver table in XGCONF, follow these steps:

1. Open your application’s *.cfg file with XGCONF.
2. In the Available Products area, select the **SYS/BIOS > I/O > DEV** module.
3. In the Device Manager -- Module Settings page, check the box to **Add DEV to my configuration**. You can change the size of the device table to the maximum number of devices you will create both statically and dynamically.
4. Click the **Instance** button near the top of the Device Manager page.

5. Click the **Add** button next to the DEVs list. Type a name for the driver. This is the name that you will use when creating GIO channels, either with XGCONF or the GIO_create() API.

6. Set the required and additional settings for the selected DEV driver. The information to specify should be provided in the documentation for the IOM driver you are using. See the CDOC online help for the DEV module for information about the properties.

7. Create an item for each IOM driver your application will use. You can create multiple GIO channels for a single driver (if the driver supports it). So, for example, you do not need to specify two separate drivers if the application will use an IOM driver on two channels.

If you prefer to edit the *.cfg files using the **cfg Script** tab or a text editor, the following statements create the “generator” driver in the DEV table as shown in the previous figure:

```javascript
var DEV = xdc.useModule('ti.sysbios.io.DEV');
...
DEV.tableSize = 4;
var dev0Params = new DEV.Params();
dev0Params.instance.name = "generator";
dev0Params.initFxn = "&DGN_init";
dev0Params.deviceParams = "&DGN_DEVPARAMS_GENSINE";
Program.global.generator = DEV.create("/sinewave", "&DGN_FXNS", dev0Params);
```

The runtime APIs for managing the table maintained by the DEV module are:

- DEV_create()
- DEV_delete()
For example, the following C code fragment creates the same "generator" driver in the DEV table at runtime:

```c
#include <ti/sysbios/io/DEV.h>

DEV_Params params;
Error_Block eb;
...

DEV_Params_init(&params);
Error_init(&eb);

DEV_create("/sinewave", &DGN_FXNS, &params, &eb);
```

If you use a variable for the name of the driver (instead of a hardcoded string as used in the previous example), that variable must remain defined with the driver name as long as the DEV instance exists.

The DEV module provides additional APIs for matching a device name to a driver handle and for getting various information from the DEV table. These APIs are using internally by the GIO module; your application will probably not need to use them. Details are available in the CDOC online help system.

### E.3.1 Configuring the GIO Module

The GIO module manages a set of channel instances. You can create these instances only with C APIs; they cannot be created statically. However, you do need to enable the GIO module in the configuration by following these steps:

1. Open your application’s *.cfg file with XGCONF.
2. In the Available Products area, select the `SYS/BIOS > I/O > GIO` module.
3. In the General Purpose I/O Manager -- Module Settings page, check the box to **Add GIO module to my configuration**.

If you prefer to edit *.cfg files using the **cfg Script** tab or a text editor, the following statement enables the GIO module:

```c
var GIO = xdc.useModule('ti.sysbios.io.GIO');
```

For the runtime C APIs used to create and delete GIO channels, see **Section E.4.2**.
E.4 Using GIO APIs

This section describes how to use the GIO module as an abstraction for drivers after you have associated a driver handle with a driver name as described in the previous section. The ti.sysbios.io.GIO module provides the following API functions to create and delete devices and channels:

<table>
<thead>
<tr>
<th>API Function</th>
<th>Description</th>
<th>Standard Model</th>
<th>Issue/Reclaim Model</th>
</tr>
</thead>
<tbody>
<tr>
<td>GIO_create()</td>
<td>Allocate and initialize a GIO_Obj object and open a communication channel.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>GIO_delete()</td>
<td>Finalize and free a previously-allocated GIO_Obj instance.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>GIO_Params_init</td>
<td>Initialize a config-params structure with supplied values before creating an instance.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The ti.sysbios.io.GIO module provides the following API functions to control sending data over channels and to control the behavior of channels:

<table>
<thead>
<tr>
<th>API Function</th>
<th>Description</th>
<th>Standard Model</th>
<th>Issue/Reclaim Model</th>
</tr>
</thead>
<tbody>
<tr>
<td>GIO_abort()</td>
<td>Abort all pending I/O.</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>GIO_control()</td>
<td>Send a control command to the driver.</td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>GIO_flush()</td>
<td>Drain output buffers and discard any pending input.</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_issue()</td>
<td>Issue a buffer to the channel.</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_prime()</td>
<td>Prime an output channel instance.</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_read()</td>
<td>Synchronously read from a GIO instance.</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_reclaim()</td>
<td>Reclaim a buffer that was previously issued by calling issue</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_submit()</td>
<td>Submit an I/O job to the mini-driver.</td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>GIO_write()</td>
<td>Synchronously write to a GIO instance.</td>
<td></td>
<td>X</td>
</tr>
</tbody>
</table>

The Standard model is described in Section E.4.3, and the Issue/Reclaim model is described in Section E.4.4.

E.4.1 Constraints When Using GIO APIs

- A GIO instance can be used only by a single Task.
- GIO_issue() and GIO_reclaim() can only be called by a single thread (Task or Swi) or in the callback context.
- GIO_issue(), GIO_reclaim(), GIO_read(), and GIO_write() cannot be called during Module startup. Some drivers may require that these APIs not be called even from main() if they require hardware interrupts to enable their peripherals.
E.4.2 Creating and Deleting GIO Channels

The GIO module manages GIO instances, which correspond to communication channels. Such channels are assigned a mode when you create them; they can be input, output, or in/out channels. You also specify the device driver a channel should use when you create it.

GIO channels can only be created using C APIs. They cannot be created statically with XGCONF.

Several GIO instances can be created to use the same driver instance. If the driver instance supports two channels, then two GIO instances can be created using the same driver.

The GIO_create() API creates a GIO instance. It has the following parameters:

```c
GIO_Handle GIO_create(
    String             name,
    UInt               mode,
    const GIO_Params   *params,
    Error_Block        *eb)
```

The GIO_Params structure allows you to specify the following parameters:

```c
struct GIO_Params {
    IInstance_Params     *instance;
    Ptr                  chanParams;
    GIO_Model            model;
    Int                  numPackets;
    ISync_Handle         sync;
    Uint                 timeout;
};
```

The driver name and mode are required parameters for creating a GIO instance. The driver name must match a string that is present in the driver table. See Section E.3.

The mode must be GIO_INPUT, GIO_OUTPUT, or GIO_INOUT. Note that some drivers only support certain modes. See the documentation for your driver for details.

GIO_Params allow you to set optional parameters.

- The "instance" field allows you to specify a name for the GIO instance. The name will show up in the ROV debugging tool.
- The "chanParams" is a pointer to driver-specific parameters used to configure the driver. These parameters are sent to the driver in the driver’s open() call. Typically drivers have default values for such parameters.
- The "model" field can be set to GIO_Model_STANDARD or GIO_Model_ISSUERECLAIM. The Standard model is the default and the simplest option to use; it is used with synchronous GIO APIs that can block while waiting for a buffer, and so can only be called in the context of a Task thread. The IssueReclaim model must be used with asynchronous GIO APIs that cannot block, but can be called in the context of Hwi, Swi, and Task threads.
- The "numPackets" field specifies the number of packets that can be used if you are using the GIO_Model_ISSUERECLAIM asynchronous model. This is 2 by default. If you are using the GIO_Model_STANDARD model and the GIO_read()/GIO_write() functions, you should set this parameter to 1 to save the memory used by the extra packet.
The "sync" field selects a synchronization mechanism to use if the GIO_Model_ISSUERECLAIM model is selected. GIO uses the xdc.runtime.knl.Sync module for synchronization when the GIO channel waits for the driver. If you pass NULL for this field, Sync uses a Semaphore for synchronization. Most applications will not need to change this default. See Section E.6 for more on synchronization mechanisms.

The "timeout" field specifies how long to wait (in Clock module ticks) for a buffer to be ready when a blocking call is made. This field is used only with the GIO_Model_STANDARD model. The default is BIOS_WAIT_FOREVER.

GIO_create() also takes an Error_Block and can fail. GIO_create() returns a GIO_Handle that is passed to many other GIO APIs to send or receive data.

This example creates two GIO channels that will use the Issue/Reclaim model:

```c
#include <xdc/std.h>
#include <xdc/runtime/Error.h>
#include <xdc/runtime/System.h>
#include <ti/sysbios/BIOS.h>
#include <ti/sysbios/knl/Task.h>
#include <xdc/cfg/global.h>
#include <ti/sysbios/io/GIO.h>

GIO_Handle handleIn, handleOut;
Error_Block eb;

/*
 * ======== main ========
 */
Int main(Int argc, Char* argv[])
{
    GIO_Params gioParams;

    Error_init(&eb);

    /* Create input GIO instance */
    GIO_Params_init(&gioParams);
    gioParams.model = GIO_Model_ISSUERECLAIM;
    handleIn = GIO_create("sampleDriver", GIO_INPUT, &gioParams, &eb);

    /* Create output GIO instance */
    gioParams.timeout = 5000;
    handleOut = GIO_create("sampleDriver", GIO_OUTPUT, &gioParams, &eb);

    BIOS_start();
    return(0);
}
```
Important: A GIO_Handle cannot be used by more than one Task simultaneously. One Task must finish a GIO_read() or GIO_write() or GIO_issue()/GIO_reclaim() before another Task can use the same GIO_Handle. It is safer for only one Task to use each GIO_Handle.

GIO_delete() deletes a GIO instance and frees all resources allocated during GIO_create(). This frees up the driver channel in use.

GIO_control() can be called to configure or perform control functionality on the communication channel. The calling syntax is as follows:

```c
Int GIO_control(
    GIO_Handle   handle,
    UInt         cmd,
    Ptr          args);
```

The cmd argument may be one of the command code constants listed in ti/sysbios/io/iom.h. Drivers may add command codes to provide additional functionality.

The args parameter points to a data structure defined by the device to allow control information to be passed between the device and the application. In some cases, this argument may point directly to a buffer holding control data. In other cases, there may be a level of indirection if the mini-driver expects a data structure to package many components of data required for the control operation. In the simple case where no data is required, this parameter may just be a predefined command value.

GIO_control() returns IOM_COMPLETED upon success. If an error occurs, the device returns a negative value. For a list of error values, see ti/sysbios/io/iom.h. The underlying driver IOM function call is typically a blocking call, so calling GIO_control() can result in blocking.

### E.4.3 Using GIO_read() and GIO_write() — The Standard Model

The Standard GIO model is the simplest way to use GIO. It waits for a buffer to be ready and allows other threads to run while waiting. The Standard model can only be used if you are reading from and writing to the device in the context of a Task thread. For a more complicated model that uses double-buffering, see Section E.4.4.

When you are using GIO_Model_STANDARD, your application should call GIO_read() and GIO_write() from the context of Task threads. GIO_read() is used to get a buffer from an input channel. GIO_write() is used to write to a buffer for an output channel.

GIO_read() has the following calling syntax:

```c
Int GIO_read(
    GIO_Handle   handle,
    Ptr          buf,
    SizeT        *pSize);
```

An application calls GIO_read() to read a specified number of MADUs (minimum addressable data units) from a GIO channel. The GIO_Handle is a handle returned by GIO_create().

The buf parameter points to a device-defined data structure for passing buffer data between the device and the application. This structure may be generic across a domain or specific to a single mini-driver. In some cases, this parameter may point directly to a buffer that holds the read data. In other cases, this parameter may point to a structure that packages buffer information, size, offset to be read from, and
other device-dependent data. For example, for video capture devices, this structure may contain pointers to RGB buffers, their sizes, video format, and a host of data required for reading a frame from a video capture device. Upon a successful read, this argument points to the returned data.

The pSize parameter points to the size of the buffer or data structure pointed to by the buf parameter. When GIO_read() returns, this parameter points to the number of MADUs actually read from the device. This parameter is relevant only if the buf parameter points to a raw data buffer. In cases where it points to a device-defined structure, it is redundant—the size of the structure is known to the mini-driver and the application.

GIO_read() returns IOM_COMPLETED upon successfully reading the requested number of MADUs from the device. If an error occurs, the device returns a negative value. For a list of error values, see ti/sysbios/io/iom.h.

GIO_read() blocks until the buffer is filled or the timeout period specified when this GIO instance was created occurs.

A call to GIO_read() results in a call to the mdSubmit function of the associated mini-driver. The IOM_READ command is passed to the mdSubmit function. The mdSubmit call is typically a blocking call, so calling GIO_read() can result in the thread blocking.

**GIO_write()** has the following parameters:

```c
Int GIO_write(
    GIO_Handle   handle,
    Ptr          buf,
    SizeT        *pSize);
```

An application calls GIO_write() to write a specified number of MADUs (minimum addressable data units) to the GIO channel.

The handle, buf, and pSize parameters are the same as those passed to GIO_read(). When you call GIO_write(), the buf parameter should already contain the data to be written to the device. When GIO_write() returns, the pSize parameter points to the number of MADUs written to the device.

GIO_write() returns IOM_COMPLETED upon successfully writing the requested number of MADUs to the device. If an error occurs, the device returns a negative value. For a list of error values, see ti/sysbios/io/iom.h.

GIO_write() blocks until the buffer can be written to the device or the timeout period specified when this GIO instance was created occurs.

A call to GIO_write() results in a call to the mdSubmit function of the associated mini-driver. The IOM_WRITE command is passed to the mdSubmit function. The mdSubmit call is typically a blocking call, so calling GIO_write() can result in the thread blocking.

**GIO_submit()** is called internally by GIO_read() and GIO_write(). Typically, your application will not call it directly. The only reason to call it directly is if your application needs to call the mdSubmit function of the associated driver in some specialized way.
E.4.4 Using GIO_issue(), GIO_reclaim(), and GIO_prime() — The Issue/Reclaim Model

GIO has another model for using channels called GIO_Model_ISSUERECLAIM. This model uses asynchronous API calls, which cannot yield to other threads. If the buffer is not ready, the functions return a failure status. These APIs can be used in any thread context. The main GIO APIs used with the synchronous model are GIO_issue, GIO_reclaim, and GIO_prime.

- A GIO_issue()/GIO_reclaim() pair on an input channel is similar to a call to GIO_read().
- A GIO_issue()/GIO_reclaim() pair on an output channel is similar to a call to GIO_write().

GIO_issue() sends a buffer to a channel. Full buffers are sent to output channels, and empty buffers are sent to input channels. No buffer is returned during the call to GIO_issue(), and the application no longer owns the buffer. GIO_issue() returns control to the thread from which it was called without blocking. The buffer has been given to the driver for processing.

Later, when the application is ready to get the buffer back, it calls GIO_reclaim(). By default, this call to GIO_reclaim() blocks if the driver has not completed processing the I/O packet. If you want GIO_reclaim() to be non-blocking, you can specify that it should use a different Sync implementation when you create the GIO channel. In normal operation each GIO_issue() call is followed by a GIO_reclaim() call.

The GIO_issue/GIO_reclaim model provides flexibility by allowing your application to control the number of outstanding buffers at runtime. Your application can send multiple buffers to a channel without blocking by using GIO_issue(). This allows the application to choose how deep to buffer a device.

The GIO_issue/GIO_reclaim APIs guarantee that the client's buffers are returned in the order in which they were issued. This allows a client to use memory for streaming. For example, if a SYS/BIOS Task receives a large buffer, that Task can pass the buffer to the channel in small pieces simply by advancing a pointer through the larger buffer and calling GIO_issue() for each piece. The pieces of the buffer are guaranteed to come back in the same order they were sent. In this case, the number of buffer pieces to be passed should be less than or equal to the numPackets parameter you set when creating the channel.

Short bursts of multiple GIO_issue() calls can be made without an intervening GIO_reclaim() call followed by short bursts of GIO_reclaim() calls. However, over the life of the channel GIO_issue() and GIO_reclaim() must be called the same number of times. The number of GIO_issue() calls can exceed the number of GIO_reclaim() calls by the value specified by the gioParams.numPackets parameter when creating the channel (2 by default).

GIO_issue() has the following calling syntax:

```c
Int GIO_issue(
    GIO_Handle   handle,
    Ptr          buf,
    SizeT        size,
    UArg         arg);
```

The GIO_Handle is a handle returned by GIO_create() and the buf argument is a pointer to a buffer to issue to the channel.

The size argument is a direction dependent logical size value. For an output channel, use the size argument to specify the number of MADUs (minimum addressable data units) of data the buffer contains. For an input channel, the size argument indicates the number of MADUs being requested. In either case, this logical size value must be less than or equal to the physical size of the buffer.
The arg argument is not interpreted by GIO, but can be used to pass associated information along with the buffer to the driver. All compliant device drivers preserve the value of arg and maintain its association with the data it was issued with. For example, arg could be used to send a timestamp to an output device, indicating exactly when the data is to be rendered. The driver treats the arg as a read-only field, and the arg is returned by GIO_reclaim().

If GIO_issue() returns a failure value, the channel was not able to accept the buffer being issued or there was an error from the underlying driver. Note that the error could be driver-specific. If GIO_issue() fails because of an underlying driver problem, your application should call GIO_abort() before attempting to perform more I/O through the channel.

GIO_reclaim() requests a buffer back from a channel. The buffers are returned in the same order that they were issued. GIO_reclaim() has the following calling syntax:

```
Int GIO_reclaim(
    GIO_Handle   handle,
    Ptr          *pBuf,
    SizeT        *pSize,
    UArg         *pArg);
```

For output channels, GIO_reclaim() returns a processed buffer, and the size is zero. For input channels, GIO_reclaim() returns a full buffer, and the size is the number of MADUs of data in the buffer.

As with GIO_issue(), the GIO_reclaim() API does not modify the contents of the pArg argument.

GIO_reclaim() calls Sync_wait() with the timeout you specified when you created the GIO channel. Sync_wait() used the Sync implementation you specified when you created the GIO channel. If this is a blocking Sync implementation, then GIO_reclaim() can block. If you are calling this API from a Swi thread, you should use a non-blocking Sync. See Section E.6 for more about Sync implementations.

If GIO_reclaim() returns a failure status (!= IOM_COMPLETED), no buffer was returned. Therefore, if GIO_reclaim() fails, your application should not attempt to de-reference pBuf, since it is not guaranteed to contain a valid buffer pointer.

If you attempt to call GIO_reclaim() without having a previous unmatched call to GIO_issue(), the GIO_reclaim() call will either block forever or fail. The error is not detected at run-time.

GIO_prime() sends a buffer to an output channel to prime it at startup. It has the following calling syntax:

```
Int GIO_prime(
    GIO_Handle   handle,
    Ptr          buf,
    SizeT        size,
    UArg         arg);
```

This API is useful, for example, if an application uses double-buffering and you want to issue a buffer an output channel, but the driver cannot handle dummy buffers. GIO_prime() makes a buffer available for GIO_reclaim() without actually sending the buffer to the driver.

This API is non-blocking, and can be called from any thread context.

If GIO_prime() returns a failure status (!= IOM_COMPLETED), the GIO channel was not able to accept the buffer being issued due to the un-availability of GIO packets.

See the CDOC online reference described in Section 1.6.1 for more about these APIs.
E.4.5 GIO_abort() and Error Handling

Your application can delete all pending buffers being sent to a channel by calling GIO_abort(). If the error is less serious, you may instead call GIO_flush() to finish all pending output requests but discard any pending input requests. One of these actions may need to be done in response to the following situations:

- The application has decided to cancel current I/O and work on something else.
- A GIO API returned an error.

GIO_abort() cancels all input and output from the device. When this call is made, all pending calls are completed with a status of GIO_ABORTED. An application can use this call to return the device to its initial state. Usually this is done in response to an unrecoverable error at the device level.

GIO_abort() returns IOM_COMPLETED after successfully aborting all input and output requests. If an error occurs, the device returns a negative value. The list of error values are defined in the ti/sysbios/io/iom.h file. The underlying call sent to the driver is typically implemented as a blocking call, so calling GIO_abort() can result in the thread blocking.

If you are using the ISSUERECLAIM model, the underlying device connected to the channel is idled as a result of calling GIO_abort(), and all buffers are made ready for GIO_reclaim(). The application needs to call GIO_reclaim() to get back the buffers. However the client will not block when calling GIO_reclaim() after GIO_abort().

GIO_abort() has the following calling syntax:

```c
Int GIO_abort(
    GIO_Handle   handle
);
```

GIO_flush() discards all buffers pending for input channels and completes any pending requests to output channels. All pending input calls are completed with a status of IOM_FLUSHED, and all output calls are completed routinely.

This call returns IOM_COMPLETED upon successfully flushing all input and output. If an error occurs, the device returns a negative value. For a list of error values, see ti/sysbios/io/iom.h. The underlying call sent to the driver is typically implemented as a blocking call, so calling GIO_flush() can result in the thread blocking.

GIO_flush() has the following calling syntax:

```c
Int GIO_flush(
    GIO_Handle   handle
);
```
E.5 Using GIO in Various Thread Contexts

The GIO module can be used in a number of different thread context and with various synchronization mechanisms. The following subsections discuss special cases.

E.5.1 Using GIO with Tasks

GIO_read and GIO_write can be used only with Task threads. In addition, GIO_issue() and GIO_reclaim() can be used with either Task or Swi threads. By default GIO uses SyncSem for synchronization.

E.5.1.1 Using a Semaphore Instance Created by the Application with a GIO Instance

There may be cases where you do not want GIO to create a Semaphore for synchronization, and instead wants to supply your own Semaphore to GIO. The following code snippet shows how to do this.

```c
#include <ti/sysbios/syncs/SyncSem.h>
#include <ti/sysbios/knl/Semaphore.h>
#include <ti/sysbios/io/GIO.h>

/*
* ======== main ========
*/
Int main(Int argc, Char* argv[])
{
    GIO_Params gioParams;
    SyncSem_Params syncParams;
    SyncSem_Handle syncSem;

    /* Create input channel */
    SyncSem_Params_init(&syncParams);
    syncParams.sem = Semaphore_create(0, NULL, NULL);
    syncSem = SyncSem_create(&syncParams, NULL);

    GIO_Params_init(&gioParams);
    gioParams.chanParams = (UArg)&genParamsIn;
    gioParams.sync = SyncSem_Handle_upCast(syncSem);
    handleIn = GIO_create("/genDevice", GIO_INPUT, &gioParams, &eb);
}
```
E.5.2 Using GIO with Swis

GIO_issue() and GIO_reclaim() can also be used with Swi threads. The client first creates a GIO Instance. The application has to populate the "sync" field in the GIO_Params struct. This sync field needs to be assigned to a SyncSwi instance.

The application needs to follow these steps to get a SyncSwi_Handle.

1. Create a Swi instance.
2. Populate the "swi" field of a SyncSwi_Params struct with the Swi_Handle received in the previous step.
3. Create a SyncSwi instance using the SyncSwi_Params struct.
4. Populate the "sync" field in GIO_Params struct with the SyncSwi_Handle received from the previous step.

The GIO module calls ISync_signal() in the callback path when I/O completes. This results in a Swi_post(). The swi runs and calls GIO_reclaim() to get back the processed buffer.

SyncSwi_wait() does nothing and returns FALSE for timeout.

E.5.3 Using GIO with Events

There are cases where a Task needs to wait on multiple events. For example, a Task may need to wait for a buffer from an input channel or a message from the host. In such cases, the Task should use the Event_pend() API to "wait on multiple" items. See Section 4.2 for more information on Events.

In order to use GIO with the Event module, the GIO instance needs to be created using the following steps:

1. Create an Event instance.
2. Populate the "event" field of the SyncEvent_Params struct with the Event_Handle received from the previous step.
3. Create a SyncEvent instance using the SyncEvent_Params struct.
4. Populate the "sync" field in the GIO_Params struct with the SyncEvent_Handle received from the previous step.

For the example later in this section, the application first primes the channel by calling GIO_issue(). When the worker task runs, it calls Event_pend() and BLOCKS waiting for I/O completion.

When I/O is complete, the GIO module calls ISync_signal(), which results in an Event_post() call. The Task wakes up, checks which event happened, and calls GIO_reclaim() to get the processed buffer.
The xdc.runtime.knl.ISync module specifies the interface for two functions that are used internally by a GIO channel instance: ISync_signal() and ISync_wait(). Various implementations of these interfaces handle synchronization of GIO in different ways. SYS/BIOS contains several implementations of the ISync interface:

- **SyncSem** (the default) is based on Semaphores and is BLOCKING. This module is found in ti.sysbios.syncs.
- **SyncSwi** is based on Swis and is NON-BLOCKING. This module is found in ti.sysbios.syncs.
- **SyncEvent** is based on Events and is NON-BLOCKING. This module is found in ti.sysbios.syncs.
- **SyncGeneric** can be used with any two functions that provide the ISync_signal() and ISync_wait() functionality. This module is found in XDCtools in xdc.runtime.knl.
- **SyncNull** performs no synchronization. This module is found in XDCtools in xdc.runtime.knl.

Instead of tying itself to a particular module for synchronization, the GIO module allows you to pick an ISync implementation module. You can select an ISync implementation for all GIO instances using the module-level configuration parameter "GIO_SyncProxy". You can select an ISync implementation for a specific GIO channel instance using the instance-level configuration parameter "sync" (see Section E.4.2).

By default GIO_SyncProxy is bound to SyncSem by the BIOS module. If you pass NULL for the "sync" parameter to GIO_create(), the GIO module creates a SyncSem instance for the GIO instance. This translates to a Semaphore instance for this GIO instance.

The GIO module calls ISync_signal() in the callback path when I/O completes. It calls ISync_wait() from GIO_reclaim() when I/O has not been completed.

### E.6.1 Using GIO with Generic Callbacks

It is possible for the application to provide two functions equivalent to ISync_signal() and ISync_wait() using the xdc.runtime.knl.SyncGeneric module.

One use case for this is when the application will use GIO with a simple callback. The application then provides its callback function as the signal function to SyncGeneric_create(). The GIO module then invokes the application callback when I/O completes. The application can reclaim the buffer within the callback function.
This appendix provides reference details for the IOM (I/O Mini-driver) interface.

<table>
<thead>
<tr>
<th>Topic</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>F.1 Mini-Driver Interface Overview</td>
<td>239</td>
</tr>
<tr>
<td>mdBindDev</td>
<td>242</td>
</tr>
<tr>
<td>mdControlChan</td>
<td>243</td>
</tr>
<tr>
<td>mdCreateChan</td>
<td>244</td>
</tr>
<tr>
<td>mdDeleteChan</td>
<td>245</td>
</tr>
<tr>
<td>mdSubmitChan</td>
<td>246</td>
</tr>
<tr>
<td>mdUnBindDev</td>
<td>247</td>
</tr>
</tbody>
</table>
F.1 Mini-Driver Interface Overview

The mini-driver interface specifies how to implement a mini-driver.

Functions

A mini-driver should implement the following functions:

- `mdBindDev`. Bind device to mini-driver.
- `mdControlChan`. Perform channel control command.
- `mdCreateChan`. Create a device channel.
- `mdDeleteChan`. Delete a channel.
- `mdSubmitChan`. Submit a packet to a channel for processing.
- `mdUnBindDev`. Unbind device from mini-driver.

Description

A mini-driver contains the device-specific portions of the driver. Once you create the specified functions for your mini-driver, application integrators can easily use your mini-driver through GIO channels.

The sections that follow describe how to implement the mini-driver functions in detail. Once implemented, these functions should be referenced in an interface table of type `IOM_Fxns`, which applications will reference to integrate the mini-driver. For example:

```c
IOM_Fxns UART_FXNS = {
    mdBindDev,
    IOM_UNBINDDEVNOTIMPL,
    mdControlChan,
    mdCreateChan,
    mdDeleteChan,
    mdSubmitChan
};
```

Note: Any mini-driver functions you choose not to implement should either plug the mini-driver function table with `IOM_`xxxNOTIMPL`, where `xxx` corresponds to the function name. Alternately, you may implement a function that returns a status of `IOM_ENOTIMPL`. 
## Constants, Types, and Structures

The following code can be found in `<bios_install_dir>/packages/ti/sysbios/io/IOM.h`

```c
/* Modes for mdCreateChan */
#define IOM_INPUT    0x0001
#define IOM_OUTPUT   0x0002
#define IOM_INOUT    (IOM_INPUT | IOM_OUTPUT)

/* IOM Status Codes */
#define IOM_COMPLETED      0   /* I/O successful */
#define IOM_PENDING        1   /* I/O queued and pending */
#define IOM_FLUSHED        2   /* I/O request flushed */
#define IOM_ABORTED        3   /* I/O aborted */

/* IOM Error Codes */
#define IOM_EBADPORT       -1    /* generic failure */
#define IOM_ETIMEOUT       -2    /* timeout occurred */
#define IOM_ENOPACKETS     -3    /* no packets available */
#define IOM_EFREE          -4    /* unable to free resources */
#define IOM_EALLOC         -5    /* unable to alloc resource */
#define IOM_EBADMODE       -7    /* illegal device mode */
#define IOM_EOF            -8    /* end-of-file encountered */
#define IOM_ENOTIMPL       -9    /* operation not supported */
#define IOM_EBADARGS       -10   /* illegal arguments used */
#define IOM_ETIMEOUTUNREC  -11   /* unrecoverable timeout */
#define IOM_EINUSE         -12   /* device already in use */

/* Command codes for IOM_Packet */
#define IOM_READ           0
#define IOM_WRITE          1
#define IOM_ABORT          2
#define IOM_FLUSH          3
#define IOM_USER           128   /* 0-127 reserved for system */

/* Command codes for GIO_control and mdControlChan */
#define IOM_CHAN_RESET      0 /* reset channel only */
#define IOM_CHAN_TIMEDOUT   1 /* channel timeout occurred */
#define IOM_DEVICE_RESET    2 /* reset entire device */

typedef struct IOM_Fxns
{
    IOM_TmdBindDev       mdBindDev;
    IOM_TmdUnBindDev     mdUnBindDev;
    IOM_TmdControlChan   mdControlChan;
    IOM_TmdCreateChan    mdCreateChan;
    IOM_TmdDeleteChan    mdDeleteChan;
    IOM_TmdSubmitChan    mdSubmitChan;
} IOM_Fxns;
```
#define IOM_BINDDEVNOTIMPL (IOM_TmdBindDev)IOM_mdNotImpl
#define IOM_UNBINDEVDENOTIMPL (IOM_TmdUnBindDev)IOM_mdNotImpl
#define IOM_CONTROLCHANNOTIMPL (IOM_TmdControlChan)IOM_mdNotImpl
#define IOM_CREATECHANNOTIMPL (IOM_TmdCreateChan)IOM_mdNotImpl
#define IOM_DELETECHANNOTIMPL (IOM_TmdDeleteChan)IOM_mdNotImpl
#define IOM_SUBMITCHANNOTIMPL (IOM_TmdSubmitChan)IOM_mdNotImpl

typedef struct IOM_Packet {   /* frame object */
    Queue_Elem   link;       /* queue link */
    Ptr          addr;       /* buffer address */
    SizeT        size;       /* buffer size */
    UArg         arg;        /* user argument */
    UInt         cmd;        /* mini-driver command */
    Int          status;     /* status of command */
    UArg         misc;       /* reserved for driver */
} IOM_Packet;

/* Mini-driver's callback function. */
Void (*IOM_TiomCallback)(Ptr arg, IOM_Packet *packet);
**mdBindDev**  
*Bind device to mini-driver*

**C Interface**

**Syntax**

```c
status = mdBindDev(*devp, devid, devParams);
```

**Parameters**

<table>
<thead>
<tr>
<th>Type</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pointer</td>
<td>devp</td>
<td>Address for global device data pointer */</td>
</tr>
<tr>
<td>Integer</td>
<td>devid</td>
<td>Device id */</td>
</tr>
<tr>
<td>Pointer</td>
<td>devParams</td>
<td>Pointer to config parameters */</td>
</tr>
</tbody>
</table>

**Return Value**

<table>
<thead>
<tr>
<th>Type</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Integer</td>
<td>status</td>
<td>Success or failure code */</td>
</tr>
</tbody>
</table>

**Description**

The mdBindDev function is called by SYS/BIOS during device initialization. It is called once per configured device and is called after the mini-driver’s initialization function.

This function is typically used to specify device-specific global data, such as interrupts IDs and global data structures (for ROM-ability). Additional system resources may be allocated by the mini-driver at runtime.

The devp parameter provides the address where the function should place the global device data pointer.

The devid parameter is used to identify specific devices for systems that have more than one device of a specific type. For example, several McBSP mini-drivers use the devid parameter to specify which McBSP port to allocate and configure.

The devParams parameter is a pointer to the configuration parameters to be used to configure the device.

This function should return IOM_COMPLETED if it is successful. If a failure occurs, it should return one of the a negative error codes listed in Section F.1, Mini-Driver Interface Overview. If this function returns a failure code, the SYS/BIOS initialization fails with a call to System_abort().
mdControlChan  
**Perform channel control command**

**C Interface**

**Syntax**
```c
status = mdControlChan (chanp, cmd, arg);
```

**Parameters**
- **Ptr chanp;** /* channel handle */
- **UInt cmd;** /* control functionality to perform */
- **Ptr arg;** /* optional device-defined data structure */

**Return Value**
- **Int status;** /* success or failure code */

**Description**
A class driver calls this function to cause the mini-driver to perform some type of control functionality. For example, it may cause the mini-driver to reset the device or get the device status. Calling GIO_control results in execution of the appropriate mini-driver’s mdControlChan function.

The chanp parameter provides a channel handle to identify the device instance.

The cmd parameter indicates which control functionality should be carried out.

```c
/* Command codes for GIO_control and mdControlChan */
#define IOM_CHAN_RESET      0   /* reset channel only */
#define IOM_CHAN_TIMEDOUT   1   /* channel timeout occurred */
#define IOM_DEVICE_RESET    2   /* reset entire device */
```

The arg parameter is an optional device-defined data structure used to pass control information between the device and the application.

If successful, this function should return IOM_COMPLETED. If the cmd value provided is unsupported, this function should return a status of IOM_ENOTIMPL.
### mdCreateChan

**Create a device channel**

#### C Interface

**Syntax**

```c
status = mdCreateChan (*chanp, devp, name, mode, chanParams, cbFxn, cbArg);
```

**Parameters**

- **Ptr *chanp;** /* channel handle */
- **Ptr devp;** /* device global data structure */
- **String name /* name of device instance */
- **Int mode /* input or output mode */
- **Ptr chanParams /*pointer to channel parameters */
- **IOM_TiomCallback cbFxn /* poi nter to callback function */
- **Ptr cbArg /* callback function argument */

**Return Value**

- **Int status;** /* success or failure code */

**Description**

A class driver calls this function to create a channel instance. Calling GIO_create results in execution of the appropriate mini-driver’s mdCreateChan function.

The chanp parameter provides an address at which this function should place a channel handle to identify the device instance. The channel handle is a pointer to a device-specific data structure. See “mdBindDev” on page 242 for an example.

The devp parameter is a pointer to the device’s global data structure. This is the value returned by the mini-driver’s mdBindDev call.

The name parameter is the name of the device instance. This is the remainder of the device name after getting a complete match from the SYS/BIOS device driver table. For example, this might contain channel parameters.

The mode parameter specifies whether the device is being opened in input mode, output mode, or both. The mode may be IOM_INPUT, IOM_OUTPUT, or IOM_INOUT. If your driver does not support one or more modes, this function should return IOM_EBADMODE for unsupported modes.

The chanParams parameter is used to pass device- or domain-specific arguments to the mini-driver.

The cbFxn parameter is a function pointer that points to the callback function to be called by the mini-driver when it has completed a request.

The cbArg parameter is an argument to be passed back by the mini-driver when it invokes the callback function.

Typically, the mdCreateChan function places the callback function and its argument in the device-specific data structure. For example:

```c
chan->cbFxn = cbFxn;
chan->cbArg = cbArg;
```

If successful, this function should return IOM_COMPLETED. If unsuccessful, this function should return one of the a negative error codes listed in Section F.1, *Mini-Driver Interface Overview.*
mdDeleteChan  
*Delete a channel*

**C Interface**

**Syntax**

```c
status = mdDeleteChan (chanp)
```

**Parameters**

- **Ptr** `chanp`; /* channel handle */

**Return Value**

- **Int** `status`; /* success or failure code */

**Description**

A class driver calls this function to delete the specified channel instance. Calling GIO_delete results in execution of the appropriate mini-driver’s mdDeleteChan function.

The `chanp` parameter provides a channel handle to identify the device instance. The channel handle is a pointer to a device-specific data structure. See the `mdBindDev` topic for an example.

If successful, this function should return IOM_COMPLETED. If unsuccessful, this function should return one of the negative error codes listed in Section F.1, *Mini-Driver Interface Overview.*
mdSubmitChan

Submit a packet to a channel for processing

C Interface

Syntax

    status = mdSubmitChan (chanp, *packet);

Parameters

- `Ptr chanp; /* channel handle */`
- `IOM_Packet *packet; /* pointer to IOM_Packet */`

Return Value

- `Int status; /* success or failure code */`

Description

A class driver calls this function to cause the mini-driver to process the IOM_Packet. Calls to `GIO_submit`, `GIO_read`, `GIO_write`, `GIO_abort`, and `GIO_flush` result in execution of the appropriate mini-driver's `mdSubmitChan` function.

Note:
The mini-driver function `mdSubmitChan` must be written to be reentrant to allow it to be called from multiple thread contexts.

The `chanp` parameter provides a channel handle to identify the device instance. The channel handle is a pointer to a device-specific data structure. See the `mdBindDev` topic for an example.

The `packet` parameter points to a structure of type `IOM_Packet`. This structure is defined as follows:

```c
typedef struct IOM_Packet { /* frame object */
    Queue_Elem   link;       /* queue link */
    Ptr          addr;       /* buffer address */
    SizeT        size;       /* buffer size */
    UArg         arg;        /* user argument */
    Uint         cmd;        /* mini-driver command */
    Int          status;     /* status of command */
    UArg         misc;       /* reserved for driver */
} IOM_Packet;
```

The value for the `cmd` code may be one of the following:

```c
#define IOM_READ        0
#define IOM_WRITE       1
#define IOM_ABORT       2
#define IOM_FLUSH       3
```

Additional `cmd` codes may be added for domain-specific commands. Such codes should be constants with values greater than 127. See the `iom.h` file for these `cmd` codes.

If the `cmd` code is `IOM_READ` or `IOM_WRITE`, this function should queue the packet on the pending list. If the `cmd` code is `IOM_ABORT`, this function should abort both read and write packets. If the `cmd` code is `IOM_FLUSH`, this function should complete queued writes, but abort queued reads.

If this function successfully completes a read or write `IOM_Packet` request, it should return `IOM_COMPLETED`. If this function successfully aborts or flushes a packet, it should return `IOM_COMPLETED`. If unsuccessful, this function should return one of the negative error codes listed in Section F.1.
**mdUnBindDev**  
**Unbind device from mini-driver**

**C Interface**

**Syntax**

```c
status = mdUnBindDev(devp);
```

**Parameters**

- `Ptr devp; /* global device data pointer */`

**Return Value**

- `Int status; /* success or failure code */`

**Description**

This function should free resources allocated by the `mdBindDev` function.

Currently, this function is not called as a result of any GIO functions. It may be used in the future to support dynamic device driver loading and unloading.

The `devp` parameter is a pointer to the device’s global data structure. This is the value returned by the mini-driver’s `mdBindDev` call.

If successful, this function should return `IOM_COMPLETED`. If unsuccessful, this function should return a negative error code.
Table G–2 lists significant changes made between SPRUEX3U and SPRUEX3V.

### Table G–1. SPRUEX3V Revision History

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document was published for SYS/BIOS software version 6.83. It can be used with later versions of SYS/BIOS if changes to the software do not cause this document to be incorrect.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.6</td>
<td>Updated links to URLs throughout and referenced Documentation Overview (BIOS_INSTALL_DIR/docs/Documentation_Overview.html) page.</td>
</tr>
<tr>
<td>Configuration</td>
<td>Section 2.1</td>
<td>Updated information about creating projects with different products of which SYS/BIOS is a part.</td>
</tr>
<tr>
<td>Threading (and others)</td>
<td>Section 3.4.1 and others</td>
<td>Added ellipses to examples containing both #include statements and calls to TI-RTOS Kernel APIs, such as Error_init(). This is to indicate that the examples are fragments. Calls to initialization APIs should be performed within main(), and C calls in general should be made from within a function.</td>
</tr>
<tr>
<td>Threading</td>
<td>Section 3.6.3</td>
<td>Added information about Task stack size for C7000 targets.</td>
</tr>
<tr>
<td>Timing</td>
<td>Section 5.5</td>
<td>Corrected declared variable name in configuration statement to use module.</td>
</tr>
<tr>
<td>Hardware Abstraction</td>
<td>Section 8.2.4</td>
<td>Hardware interrupts are re-enabled in a specific situation when Mailbox_post(), Semaphore_post(), or Event_post() is called.</td>
</tr>
</tbody>
</table>

Table G–2 lists significant changes made between SPRUEX3T and SPRUEX3U.

### Table G–2. SPRUEX3U Revision History

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document was published for SYS/BIOS software version 6.70. It can be used with later versions of SYS/BIOS if changes to the software do not cause this document to be incorrect.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.6</td>
<td>Updated links to other documentation.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.2.1</td>
<td>Added information about how binary event behavior is modified when an Event is synchronized with a Semaphore or Mailbox.</td>
</tr>
<tr>
<td>Timing</td>
<td>Section 5.2</td>
<td>Corrected capitalization of Clock.TickSource_ settings.</td>
</tr>
<tr>
<td>Timing</td>
<td>Section 5.4</td>
<td>Added an example for Seconds module configuration.</td>
</tr>
</tbody>
</table>
Table G–3 lists significant changes made between SPRUEX3S and SPRUEX3T.

**Table G–3. SPRUEX3T Revision History**

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document was published for SYS/BIOS software version 6.51. It can be used with later versions of SYS/BIOS if changes to the software do not cause this document to be incorrect.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.4.2</td>
<td>Added section to reference documentation of POSIX thread APIs.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.1</td>
<td>Corrected information about the maximum value for a semaphore’s counter.</td>
</tr>
</tbody>
</table>

Table G–4 lists significant changes made between SPRUEX3R and SPRUEX3S.

**Table G–4. SPRUEX3S Revision History**

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.50.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.2.1</td>
<td>Added information that the supportsEvents property of the Semaphore module must be set in order to post events from Mailbox and Semaphore objects.</td>
</tr>
<tr>
<td>Instrumentation</td>
<td>Section 9.4</td>
<td>Updated link to information about Tools &gt; Runtime Object View.</td>
</tr>
</tbody>
</table>

Table G–5 lists significant changes made between SPRUEX3P and SPRUEX3R.

**Table G–5. SPRUEX3R Revision History**

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.46.01.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.4</td>
<td>Added information about Module_construct() APIs and comparison of various ways of creating instance objects.</td>
</tr>
</tbody>
</table>

Table G–6 lists significant changes made between SPRUEX3P and SPRUEX3Q.

**Table G–6. SPRUEX3Q Revision History**

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.46.</td>
</tr>
<tr>
<td>Threading</td>
<td>Section 3.6.3</td>
<td>The table listing Task stack usage has been modified to include more target families.</td>
</tr>
<tr>
<td>Memory</td>
<td>Section 7.2.1</td>
<td>Modified instructions for choosing a platform for the memory map to show that the packages repository is now part of the SYS/BIOS installation (not the XDCtools installation).</td>
</tr>
<tr>
<td>Memory</td>
<td>Section 7.8.2 and Section 7.8.3</td>
<td>Modified declaration and use of buffers in C code examples for HeapMem and HeapBuf.</td>
</tr>
</tbody>
</table>
### Table G–6. SPRUEX3Q Revision History

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>Appendix C</td>
<td>The descriptions of size benchmark applications have been modified to reflect the new set of benchmark applications.</td>
</tr>
<tr>
<td>Benchmarks</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table G–7 lists significant changes made between SPRUEX3O and SPRUEX3P.

### Table G–7. SPRUEX3P Revision History

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.45.</td>
</tr>
<tr>
<td>Threading</td>
<td>Section 3.3</td>
<td>Added information about using SYS/BIOS on Symmetric Multiprocessing (SMP) systems.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.1</td>
<td>Added information about priority-aware semaphores.</td>
</tr>
<tr>
<td>Run-Time Support</td>
<td>Section 6.2</td>
<td>Clarified that BIOS_exit() does not return to the function that called BIOS_start().</td>
</tr>
</tbody>
</table>

Table G–8 lists significant changes made between SPRUEX3N and SPRUEX3O.

### Table G–8. SPRUEX3O Revision History

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.41.</td>
</tr>
<tr>
<td>Configuration</td>
<td>Section 2.4.5</td>
<td>Pre-built libraries are no longer included with SYS/BIOS. Instead, the various BIOS.libType options determine how the libraries will be built.</td>
</tr>
<tr>
<td>Threading</td>
<td>Section 3.1</td>
<td>Added more detail to the startup sequence steps.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.4</td>
<td>Corrected and explained names of mailbox events and event IDs.</td>
</tr>
<tr>
<td>Synchronization</td>
<td>Section 4.5</td>
<td>Corrected references to the Queue module to refer to the ti.sysbios.knl package, not the ti.sysbios.misc package.</td>
</tr>
<tr>
<td>Timing</td>
<td>Section 5.4</td>
<td>Added Seconds module. This is a new module.</td>
</tr>
<tr>
<td>Timing</td>
<td>Section 5.5</td>
<td>Added more information about Timestamp module.</td>
</tr>
<tr>
<td>Run-Time Support</td>
<td>Chapter 6</td>
<td>Added chapter to provide information about support modules. These modules existed previously, but were described only in XDCtools documents.</td>
</tr>
<tr>
<td>Memory</td>
<td>Section 7.8 and Section 7.8.1</td>
<td>Add HeapMin heap implementation. This module existed previously, but was described only in XDCtools documents.</td>
</tr>
<tr>
<td>HAL</td>
<td>Section 8.3</td>
<td>Corrected configuration examples to use Timer.ANY, not Timer_ANY.</td>
</tr>
<tr>
<td>Rebuilding</td>
<td>Appendix A</td>
<td>The bios.mak file should refer to the directory that contains the gcc compiler, not to the compiler executable itself.</td>
</tr>
<tr>
<td>I/O Modules</td>
<td>Appendix E</td>
<td>Moved GIO model driver information to the appendices and identified as deprecated.</td>
</tr>
</tbody>
</table>
Table G–9 lists significant changes made between SPRUEX3M and SPRUEX3N.

**Table G–9. SPRUEX3N Revision History**

<table>
<thead>
<tr>
<th>Chapter</th>
<th>Location</th>
<th>Additions/Modifications/Deletions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>This document applies to SYS/BIOS software version 6.40.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.1</td>
<td>SYS/BIOS is not provided as part of the CCS installation. Instead, the easiest way to get SYS/BIOS is to install TI-RTOS from the CCS App Center.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.2</td>
<td>SYS/BIOS is the &quot;TI-RTOS Kernel&quot; component of TI-RTOS.</td>
</tr>
<tr>
<td>About</td>
<td>Section 1.6.1</td>
<td>The CDOC Reference help for SYS/BIOS is integrated with the CDOC help for TI-RTOS and other TI-RTOS components in the CCS Help.</td>
</tr>
<tr>
<td>Configuration</td>
<td>Section 2.1</td>
<td>SYS/BIOS examples are provided in TI Resource Explorer as &quot;Kernel Examples&quot; in the TI-RTOS example tree and should be imported from there.</td>
</tr>
<tr>
<td>Configuration</td>
<td>Section 2.3.5</td>
<td>SYS/BIOS modules are integrated into TI-RTOS in the list of Available Products in XGCONF.</td>
</tr>
<tr>
<td>Configuration</td>
<td>Section 2.4.3</td>
<td>Made several revisions to instructions for building an application with GCC.</td>
</tr>
<tr>
<td>HAL</td>
<td>Section 8.3</td>
<td>Corrected the call syntax for a Timer tickFxn.</td>
</tr>
<tr>
<td>Instrumentation</td>
<td>Section 9.4</td>
<td>Updated information about Tools &gt; RTOS Analyzer and Tools &gt; System Analyzer menu commands.</td>
</tr>
<tr>
<td>Instrumentation</td>
<td>Section 9.5.3</td>
<td>Corrected recommendations about choosing a Heap manager.</td>
</tr>
<tr>
<td>Minimizing Footprint</td>
<td>Appendix D</td>
<td>Added and updated several sections to the appendix and updated the examples.</td>
</tr>
</tbody>
</table>
### A
- abort() function, GIO module 234
- alloc() function
- Memory module 156
- alloc() function, Memory module 158
- andn() function, Swi module 70, 71, 73
- API Reference help 25
- application
  - configuring 29
  - debugging 44
- application stack size 69
- Assert module 183
  - optimizing 190
- asynchronous APIs (non-blocking) 224
- atexit() handlers 215
- Available Products view 33

### B
- background thread (see Idle Loop)
- Begin hook function
  - for hardware interrupts 60, 61
  - for software interrupts 75, 76
- binary semaphores 107
- BIOS module 134, 155
  - libType parameter 45, 192
- BIOS_exit() function 134
- BIOS_getCpuFreq() function 134
- BIOS_getThreadType() function 135
- BIOS_setCpuFreq() function 134
- BIOS_start() function 48, 134
- bios.mak file 192
- blocked state 54, 84, 85
- books (resources) 24
- buffer overflows 164
- Build Configuration 45
- build flow 41
- Build-Profile field 45

### C
- C++ 22
- C28x devices 152
- Cache interface 179
- Cache module 19, 179, 181
- Cache_disable() function 179
- Cache_enable() function 179
- Cache_inv() function 179
- Cache_wait() function 179
- Cache_wb() function 179
- Cache_wbinv() function 179
- caches 179
  - coherence operations for 179
  - disabling all caches 179
  - enabling all caches 179
  - invalidating range of memory 179
  - size 147, 154
  - waiting on 179
  - writing back a range of memory 179
  - writing back and invalidating a range of memory 179
- CCS
  - build properties 42
  - building 41
  - creating a project 27
  - debugging 44
  - other documentation 24
- channels, I/O 226
  - creating 228
  - checkStackFlag property 217
  - Chip Support Library (CSL) 58
  - cinit() function 48
  - class constructor 24
  - class destructor 24
  - class methods 23
  - Clock module 19, 127
  - Clock_create() function 97, 128, 129
  - Clock_getTicks() function 98, 128, 130
  - Clock_setFunc() function 129
  - Clock_setPeriod() function 129
  - Clock_setTimeout() function 129
  - Clock_start() function 129
  - Clock_stop() function 129
  - Clock_tick() function 127
  - Clock_tickReconfig() function 128, 130
  - Clock_tickStart() function 128, 130
  - Clock_tickStop() function 128, 130
  - clocks 50, 127
  - creating dynamically 128, 129
  - creating statically 130
  - disabling 216
  - speed 146
  - starting 128
  - stopping 129
  - ticks for, manipulating 128, 130
  - ticks for, tracking 130
  - when to use 51
- code size, reducing 216
- command line
  - building applications 42
building SYS/BIOS 192
compiler options 46
configuration namespace 137
Configuration Results view 34
configuration size
basic size benchmark configuration script 218
configuro tool 41
counting semaphores 107
Create hook function
for hardware interrupts 60, 61
for software interrupts 75, 76
for tasks 88, 89
create() function
Clock module 128
GIO module 228
Hwi module 59
Mailbox module 122
memory policy 155
Semaphore module 108
Swi module 67
Task module 83, 199
Timer module 174
critical regions, protecting (see gates)
custom libType 46
customCCOpts parameter 46
Cygwin shell 42
data size, reducing 214
debug build profile 45
debug libType 46
debugging 183
dec() function, Swi module 70, 71, 73
defaults module 18
delegate modules 180
Delete hook function
for hardware interrupts 60, 61
for software interrupts 75, 76
for tasks 88, 89
delete() function
GIO module 230
Mailbox module 122
memory policy 155
Semaphore module 108
Swi module 75
Task module 83, 200
dequeue() function, Queue module 124
deterministic performance 159
DEV module 224
configuring 224
DEV_create() function 225
DEV_delete() function 225
device drivers 223
device-specific modules 180
Diags module 18, 183, 189
disable() function
Cache interface 179
Hwi module 54, 169, 197
Swi module 54, 75, 198
Task module 54, 199
disableInterrupt() function, Hwi module 54
dispatcher 171
optimization 190
documents (resources) 24
download 24
drivers 223
table of names 224
dynamic configuration 12
enable() function
Cache interface 179
Hwi module 58, 169, 197
Swi module 198
Task module 199
End hook function
for hardware interrupts 60, 61
for software interrupts 75, 76
enqueue() function, Queue module 124
enter() function, Gate module 119
error block 185
Error module 18, 139, 183, 185
Error_check() function 139, 186
Error_getData() function 139
Error_getMsg() function 139
Error_init() function 139, 185
Error_print() function 139
Error_raise() function 139
ers
finding in configuration 39
handling 185
Event module 19, 113
used with Mailbox 123
Event_create() function 114, 117
Event_pend() function 113, 118, 123
Event_post() function 113, 118
events 113
associating with mailboxes 123
channels used with 236
creating dynamically 114
creating statically 114
examples of 114
posting 113, 114
posting implicitly 116
waiting on 113, 114
execution states of tasks 84
Task_Mode_BLOCKED 84, 85
Task_Mode_INACTIVE 84
Task_Mode_READY 84, 85
Task_Mode_RUNNING 84, 85
Task_Mode_TERMINATED 84, 85
execution states of threads 52
Exit hook function, for tasks 88, 89
exit() function, Task module 85
eXpress Dsp Components (see XDCtools)
external memory 143
family-specific modules 180
flush() function, GIO module 234
C++ 22

free() function 158

function names 22

functions

(see also hook functions)

G

Gate module 18

Gate_leave() function 119

Gate_mutex module 120

GateMutex module 120

GateMutexPri module 120, 121

gates 119

preemption-based implementations of 120

priority inheritance with 121

priority inversion, resolving 121

semaphore-based implementations of 120

GateSwi module 120

GateTask module 120

GCC (GNU Compiler Collection) 42

g() function, Queue module 125

getFreq() function, Timer module 175

gHookContext() function

Hwi module 57

gHookContext() function, Swi module 76

getNumTimers() function, Timer module 175

getStatus() function, Timer module 175

getTicks() function, Clock module 128

getTrigger() function, Swi module 71

GIO module 223

APIs 227

channels 224

configuring 226

synchronization 237

GIO_abort() function 234

GIO_control() function 230

GIO_create() function 228, 235

GIO_delete() function 230

GIO_flush() function 234

GIO_issue() function 232

GIO_Params structure 228

GIO_prime() function 233

GIO_read() function 230

GIO_reclaim() function 233

GIO_submit() function 231

GIO_write() function 231

global namespace 40

gmake utility 42, 192

H

hal package 180

hardware interrupts 50, 58

compared to other types of threads 52

creating 59

disabling 169

enabled at startup 48

enabling 169

hook functions for 60, 62

interrupt dispatcher for 171, 190

priority of 53

registers saved and restored by 172

timing benchmarks for 197

when to use 51

head() function, Queue module 125

Heap implementation

used by Memory module 158

HeapBuf module 19, 158, 160, 190

HeapMem module 19, 158, 159, 190

system heap 155

HeapMin module 159

HeapMultiBuf module 19, 158, 161, 190

heaps 158

default 157

HeapBuf implementation 160

HeapMem implementation 159

HeapMultiBuf implementation 161

implementations of 158

module-specific 157

optimizing 190

system 155

HeapTrack module 158, 164

help system 25

hook context pointer 57

hook functions 53, 56

for hardware interrupts 60, 62

for software interrupts 75

for tasks 88, 89

hook sets 56

Hwi dispatcher 171

Hwi module 19, 58, 60, 181

logging and performance 189

Hwi threads (see hardware interrupts)

Hwi_create() function 59

Hwi_delete() function

hook function 60

Hwi_disable() function 54, 130

Hwi_disableInterrupt() function 54

Hwi_enable() function 58

Hwi_getHookContext() function 57, 61, 63

Hwi_plug() function 58

Hwi_restore() function 130

Hwi_setHookContext() function 61, 62

I

I/O modules 223

ICache interface 179

Idle Loop 50, 100

compared to other types of threads 52

priority of 54

when to use 51

Idle module 19

manager 100

inactive state 84

inc() function, Swi module 70, 71, 72

insert() function, Queue module 125
instance
  adding 34
  deleting 34
setting properties 37
instrumentation 183
instrumented libType 45
interrupt keyword 172
Interrupt Latency benchmark 197
INTERRUPT pragma 172
Interrupt Service Routines (ISRs) (see hardware interrupts)
interrupts (see hardware interrupts, software interrupts)
ter-task synchronization (see semaphores)
inv() function, Cache interface 179
IOM module
  driver names 224
  interface 238
IOM_Fxns structure 239
ISR stack (see system stack)
ISRs (Interrupt Service Routines) (see hardware interrupts)
issue() function, GIO module 232, 235, 236
Issue/Reclaim model 228, 232
ISync module 237

L
latency benchmark 197
leave() function, Gate module 119
libraries, SYS/BIOS 45
libType parameter 45, 192
linker command file
  customizing 151
  supplemental 150
linking 45
Load module 19, 183
  configuration 184
  logger 184
Load_calculateLoad() function 185
Load_getCPUload() function 184
Load_getGlobalHwiLoad() function 184
Load_getGlobalSwiLoad() function 184
Load_getTaskLoad() function 184
Log module 18
logging
  disabling 216
  implicit, for threads 53
  optimizing 189

M
M3 microcontrollers 152
Mailbox module 19, 122
Mailbox_create() function 118, 122
Mailbox_delete() function 122
Mailbox_pend() function 118, 122
Mailbox_post() function 118, 122
mailboxes 122
  associating events with 123
  creating 122
  deleting 122
posting buffers to 123
posting implicitly 116
reading buffers from 123
Main module 18
main() function 48
  calling BIOS_start() 48
  functions called before 60, 75
make utility 42, 192
malloc heap, reducing size of 214
malloc() function 158
C++ 22
MAR registers 154
MAUs (Minimum Addressable Units) 155
mdBindDev function 242
mdControlChan function 243
mdCreateChan function 244
mdDeleteChan function 245
mdSubmitChan function 246
mdUnBindDev function 247
memory
  allocation of (see heaps)
    fragmentation 159
  leaks, detecting 164
  policies for, setting 216
  requirements for, minimizing 214
memory map 143
Memory module 18, 156, 158
Memory_alloc() function 111, 156
Memory_free() function 156
memoryPolicy property 155
microcontrollers 152
Minimum Addressable Units (MAUs) 155
modules
  adding to configuration 33
  list of 19
  named, disabling 215
  removing from configuration 35
  setting properties 37
MSP430 device 152
multithreading (see threads)
mutex 120
mutual exclusion (see semaphores)

N
name mangling 22, 23
name overloading 23
named modules, disabling 215
naming conventions 22
next() function, Queue module 125
non-instrumented libType 46
non-synchronous APIs (non-blocking) 224
NULL, in place of error block 185

O
optimization 189
or() function, Swi module 70, 71, 74
Outline view 34
P
packages
  SYS/BIOS list 19
  XDCTools 18
pend() function
  Event module 113, 114, 123
  Mailbox module 123
  Semaphore module 108, 202
performance 189
platform
  custom 144
Platform field 143
platform wizard 144
POSIX threads 22, 50
post() function
  Event module 113, 114
  Mailbox module 123
  Semaphore module 108, 201
  Swi module 70, 71, 198
posting Swis 67
preemption 55
preemption-based gate implementations 120
prev() function, Queue module 125
prime() function, GIO module 233
printf() function, removing 217
priority inheritance 121
priority inheritance, with gates 121
priority inversion 120, 121
priority inversion problem, with gates 121
priority levels of threads 52
Program module 18, 137
Program.global namespace 40
Program.sectMap array 149
project
  building 41
  Property view 35
proxy-delegate modules 180
put() function, Queue module 125

Q
Queue module 124
  Queue_create() function 103
  Queue_dequeue() function 124
  Queue_empty() function 124
  Queue_enqueue() function 124
  Queue_get() function 111
  Queue_head() function 125
  Queue_insert() function 125
  Queue_next() function 102, 125
  Queue_prev() function 125
  Queue_put() function 103, 111, 125
  Queue_remove() function 125
queues 124
  atomic operation of 125
  FIFO operation on 124
  inserting elements in 125
  iterating over 125
  removing elements from 125

R
read() function, GIO module 230
Ready hook function
  for software interrupts 75, 76
  for tasks 88, 89
ready state 84, 85
reclaim() function, GIO module 233, 235, 236
Register hook function
  for hardware interrupts 60, 61
  for software interrupts 75, 76
  for tasks 88
release build profile 45
remove() function, Queue module 125
repository 145
Reset module 48, 138
resources 24
restore() function
  Hwi module 169
  Swi module 75
ROV tool 187
RTS thread protection, disabling 217
RTSC-pedia wiki 145
running state 84, 85

S
sections
  configuration 149
  placement 149
  segment placement 147
SectionSpec structure 149
sectMap array 149
segments 143
  configuration 148
  section placement 147
semaphore application size 207, 210
Semaphore module 19, 107
Semaphore_create() function 103, 108, 235
Semaphore_delete() function 108
Semaphore_pend() function 102, 108, 111
Semaphore_post() function 97, 102, 108, 112
semaphore-based gate implementations 120
semaphores 107
  binary semaphores 107
  configuring type of 107
  counting semaphores 107
  creating 108
  deleting 108
  example of 109
  posting implicitly 116
  signaling 108
  timing benchmarks for 201
  waiting on 108
setHookContext() function, Swi module 76
setPeriod() function, Timer module 175
setPri() function, Task module 200
size benchmarks 205
static module application sizes 206
software interrupts 50, 66
  channels used with 236
  compared to other types of threads 52
creating dynamically 68
creating statically 67, 68
deleting 75
disabling 216
enabled at startup 48
enabling and disabling 75
hook functions for 75, 77
posting, functions for 67, 70
posting multiple times 70
posting with Swi_andn() function 73
posting with Swi_dec() function 73
posting with Swi_inc() function 72
posting with Swi_or() function 74
preemption of 70, 75
priorities for 53, 68, 69
priority levels, number of 68
timing benchmarks for 198
trigger variable for 71
vs. hardware interrupts 74
when to use 51, 74
speed, clock 146
stacks used by threads 52, 152
optimization 87, 190
tasks 86
standardization 12
start() function
Clock module 128
Timer module 175
Startup module 18, 48, 138
startup sequence for SYS/BIOS 48
stat() function, Task module 87
static configuration 12
static module application sizes 206
statistics, implicit, for threads 53
Stellaris Cortex-M3 microcontrollers 152
stop() function
Clock module 129
Timer module 175
Swi module 19, 66
Swi threads (see software interrupts)
Swi_andn() function 67, 70, 71, 73
Swi_create() function 67
hook function 75
Swi_dec() function 67, 70, 71, 73
Swi_delete() function 75
Swi_disable() function 54, 75
Swi_getHookContext() function 76, 77
Swi_getTrigger() function 71
Swi_inc() function 67, 70, 71, 72
Swi_or() function 67, 70, 71, 74
Swi_post() function 56, 67, 70, 71, 101
Swi_restore() function 75
Swi_setHookContext() function 76, 77
Switch hook function, for tasks 88, 89
Sync module 18
SyncEvent module 237
SyncGeneric module 237
SyncGeneric_create() function 237
synchronization
channels 235
GIO module 237
see also events, semaphores 113
synchronous APIs (blocking) 224
SyncNull module 237
SyncSem module 237
SyncSem_create() function 235
SyncSwi module 237
SYS/BIOS 12
benefits of 12
other documentation 24
packages in 19
relationship to XDCtools 14
startup sequence for 48
SYS/BIOS libraries 45
SysCallback module 137
SysMin module 137
System Analyzer 187
system heap 155
System module 18, 135
System Overview page 30, 35
system stack 54
configuring size 152
reducing size of 214
threads using 52
System_abort() function 81, 136, 185
System_atexit() function 136
System_exit() function 136
System_flush() function 136
System_printf() function 136
Target Configuration File 44
Target field 143
target/device-specific timers 177
target-specific modules 180
task application size 206, 209
Task module 19, 82
task stack
configuring size 153
determining size used by 86
overflow checking for 87
threads using 52
task synchronization (see semaphores)
Task_create() function 83, 96
Task_delete() function 83
Task_disable() function 54
Task_exit() function 85, 89
Task_getHookContext() function 88
Task_idle task 85
Task_Mode_BLOCKED state 84, 85
Task_Mode_INACTIVE state 84
Task_Mode_READY state 84, 85
Task_Mode_RUNNING state 84, 85
Task_Mode_TERMINATED state 84, 85
Task_setHookContext() function 88
Task_setPri() function 84, 85
Task_stat() function 85, 87
Task_yield() function 85, 92, 97
tasks 50, 82
begun at startup 48
blocked 54, 86
channels used with 235
cmpared to other types of threads 52
creating dynamically 83
creating statically 83
deleting 83
disabling 216
evolution states of 84
hook functions for 88, 89
idle 85
priority level 54, 84
scheduling 84
terminating 85
timing benchmarks for 199
when to use 51
yielding 95
terminated state 84, 85
Text module 18, 140
text strings, not storing on target 215
thread scheduler, disabling 52
threads 49
creating dynamically 53
creating statically 53
evolution states of 52
hook functions in 53, 56
implicit logging for 53
implicit statistics for 53
pending, ability to 52
posting mechanism of 52
preemption of 54, 55
priorities of 53
priorities of, changing dynamically 53
priority levels, number of 52
sharing data with 52
stacks used by 52
synchronizing with 52
types of 50, 51
types of, comparing 52
yielding of 52, 54
ti.sysbios.family.* packages 19
ti.sysbios.gates package 19
ti.sysbios.hal package 19
ti.sysbios.heaps package 19
ti.sysbios.interfaces package 19
ti.sysbios.knl package 19
ti.sysbios.utils package 19
tick() function, Clock module 127
tickReconfig() function, Clock module 128
tickSource parameter 127
tickStart() function, Clock module 128
tickStop() function, Clock module 128
Timer module 19, 130, 180
timer peripherals 175
Timer_create() function 104
Timer_reconfig() function 186
TimerNull module 180
timers 127, 130
clocks using 127
converting from timer interrupts to real time 175
creating 174, 175, 176
frequency for, setting 176
initialized at startup 48
modifying period for 175
starting 175
stopping 175
target/device-specific 177
when to use 51
time-slice scheduling 95
Timestamp module 18, 132
Timestamp_get32() function 62, 132
timestamps 127, 132
timing benchmarks 197
hardware interrupt benchmarks 197
Interrupt Latency benchmark 197
semaphore benchmarks 201
software interrupt benchmarks 198
task benchmarks 199
timing services (see clocks; timers; timestamps)
trigger variable for software interrupts 71
trigger variable, software interrupts 71
U
UIA module 187
User Configuration view 34
V
validation of configuration 40
W
wait() function, Cache interface 179
waiting thread 54
wb() function, Cache interface 179
wblnv() function, Cache interface 179
whole_program build profile 45
whole_program_debug build profile 45
wrapper function 23
write() function, GIO module 230
X
xdc.cfg package 18
xdc.runtime package 18
xdc.runtime.knl package 237
xdc.useModule() statement 17, 29
XDCtools
build settings 41
command line 41
other documentation 24
relationship to SYS/BIOS 14
XGCONF 29
opening 30
saving 31
views 32
Y
yield() function, Task module 201
yielding 54, 85
IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI’s Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2020, Texas Instruments Incorporated