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* TS3A225E

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File Description

‘TS3A225E_fet1_ron_TB_linux.sp’ -which is the test bench- invokes other 2 files:
‘process_models_linux.lib’ –nominal process library, and ‘TS3A225E_subckts_linux.sp’ –
subcircuits’ definition.

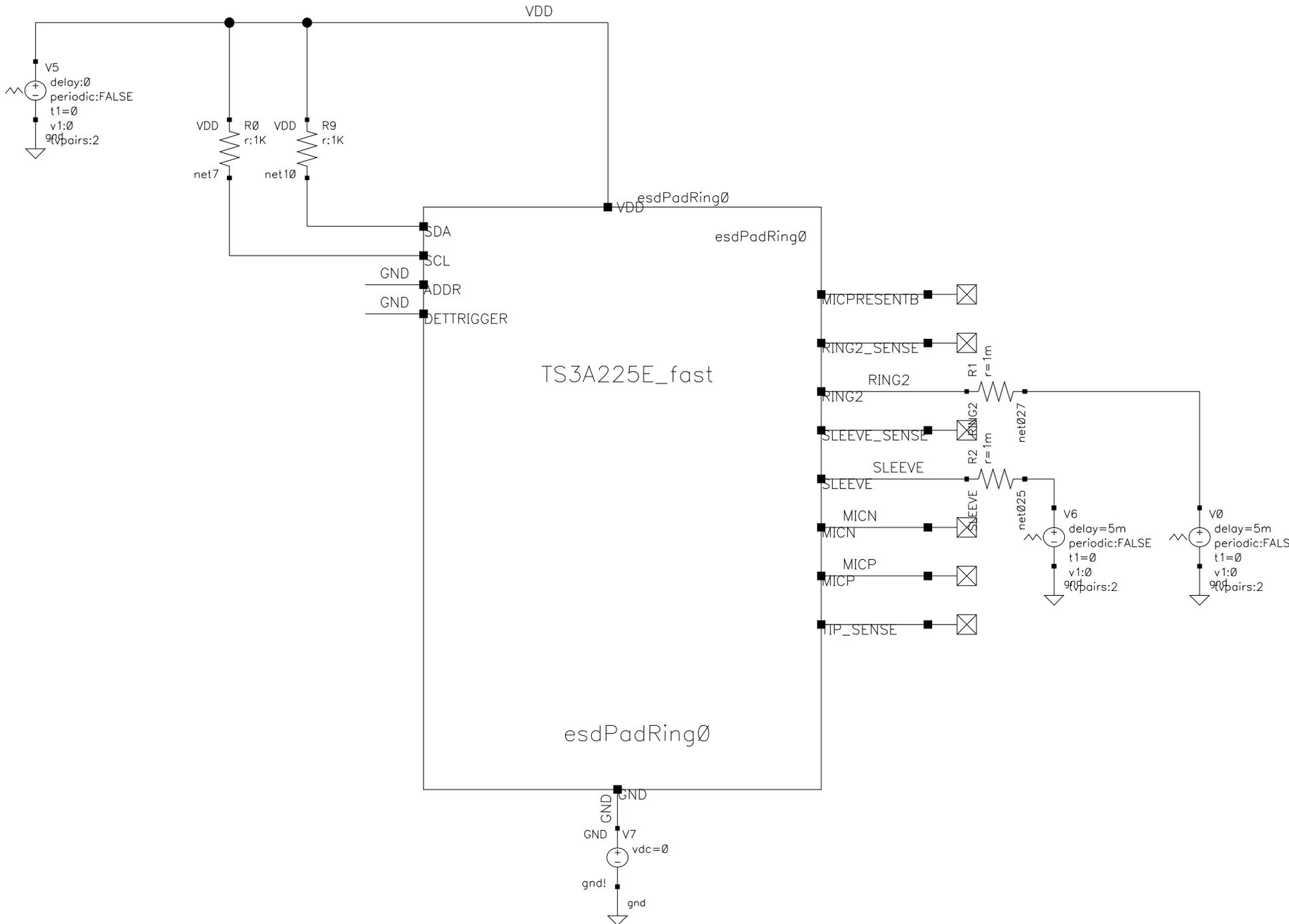
Instructions

1) Open your simulator HSPICE D-2010.03-SP2 32 BIT

on Linux

2) Run the file 'TS3A225E_fet1_roun_TB_linux.sp' by using the Hspice simulator in command line.

Plots of the test-setup schematic and simulation results are shown below:



TS3A225E: FET rds_on

