

Cell Balancing Using the bq20zxx

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ABSTRACT

This application report discusses three types of cell imbalances that are observed in a battery pack with serially connected cells.

1 Types of Cell Imbalances

1. State of Charge Imbalance

Charging cells to different states of charge (SOC) causes this type of imbalance. For example, given a configuration of 3 x 2200-mAh cells (Q_{Max}), if one cell is discharged by 100 mAh (Q₁), the second by 100 mAh, and the third by 200 mAh from a fully charged state, the first and second cells' chemical state of charge is $(Q_{\text{max}}-Q_1)/Q_{\text{max}} = 95.4\%$, but the third cell is 91%. Therefore, cell 3 is imbalanced by 4.4%.

This results in a different open-circuit voltage (OCV) for cell 3 compared to cells 1 and 2, because the OCV directly correlates with the chemical state of charge.

2. Total Cell Capacity Imbalance

A specific cell's total chemical capacity, Q_{max}, initially may be different from the others in the cell package. So, even if all cells were discharged by an equal amount from a fully charged state, their chemical states of charge may be different. Indeed, if all 3 cells are discharged by 100 mAh, but cell 3 has different total capacity (e.g., 2000 mAh), the resulting chemical states of charge is 95.4% and 95%.

This results in different OCVs. A 200-mAh difference in Q_{max} causes only a 0.4% difference in SOC because the SOC correlates with voltage. This indicates that the capacity imbalance causes less voltage difference than charge imbalance (cause 1).

3. Impedance Imbalance

Internal impedance differences between the cells (that can be an approximate 15% range in the same production batch) do not cause differences in the OCV. However, they can cause differences in cell voltage during discharge. Indeed, cell voltage can be approximated as $V = \text{OCV} + I \times R$. If current is negative (discharge), voltage is lower for a cell with higher R. If current is positive (charge), voltage is higher for a cell with higher R.

No balancing algorithm can help against resistance imbalance. However, it can significantly distort attempts to balance the SOC. If significant (< 200 mA) current is flowing, attempting to use voltage as a determining factor for passing more charge through a cell with higher voltage, fails to determine if the voltage differences is caused by differences in the SOC or by impedance. If it is caused by impedance imbalance, bypassing more current through this cell results in the opposite effect – increasing the SOC difference from other cells to a larger value than it would be without balancing. As a result, the OCV of this cell at the end of charge is different from the other cells, and can reach high levels, potentially causing the safety circuit to trip.

2 Cell Balancing Methods

The bq20zxx uses the unique capabilities of Impedance Track™ technology to identify the chemical SOC of each cell, which does not rely on measuring voltage during charge or discharge. This removes the distortion caused by impedance imbalance, and permits precise SOC balancing. The cell-balancing algorithm operates as follows:

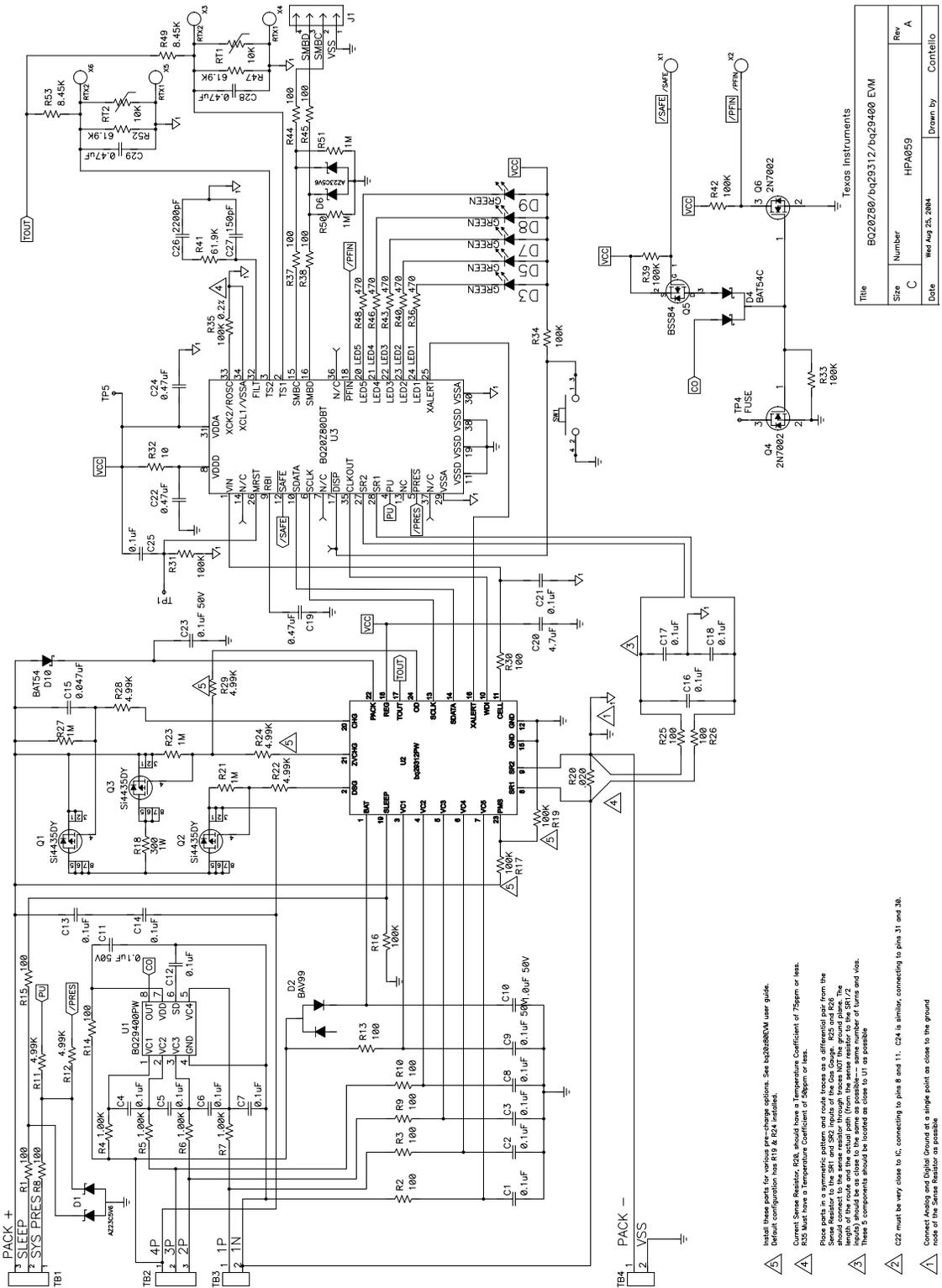
- (a) Determine the initial SOC for each series cell bank separately.
- (b) Determine the charge needed for each cell to reach a fully charged state.

Impedance Track is a trademark of Texas Instruments.

- (c) Find the cell requiring the most charge to be fully charged, and then find the differences, dQ , between all the other cells requiring charge and that of the one requiring the most charge.
- (d) These differences must be bypassed for each “excessive” cell during one or multiple cycles. To achieve this, the bypass FET is turned ON during charging for the calculated duration for each cell bypass time.
- (e) The bypass time is calculated based on the value of bypass current, which in turn depends on bypass resistance values, R , as $\text{time} = dQ \times R / (V \times \text{duty_cycle})$.
- (f) R is calculated as the sum of the internal bypass resistor (340Ω , typical) and the series filter resistors (R_x) leading to the cells. $R = R_x \times 2 + 340$. The resistors in question are R_2 , R_3 , R_9 , and R_{10} . Their default value is 100Ω , which results in $R = 540$.
- (g) The bq20zxx stores the value of $R(\Omega) \times 3.6 / (V \times \text{duty_cycle (ratio)})$ as a flash constant DF.MinCellDeviation (s/mAh). Here, 3.6 is the mAh correction factor. For default values of voltage, $V = 3.6 \text{ V}$, $R = 540 \Omega$ and duty cycle = 40% (ratio 0.4), the value is calculated as $\text{DF.MinCellDeviation} = 540 \times 3.6 / (3.6 \times 0.4) = 1350$

This value must be changed if values of resistors R_2 , R_3 , R_9 , and R_{10} are changed from the default value.

The schematic appears on the following page.



SCHMATIC

- ⑤ Indicate these paths for voltage into-chips solutions. See bq20z80M user guide.
- ⑥ Current Sense Resistor, R20, should show a Temperature Coefficient of 75ppm or less.
- ⑦ C22 must be very close to IC, connecting to pins 8 and 11. C24 is similar, connecting to pins 31 and 30.
- ⑧ Connect Analog and Digital Ground at a single point as close to the ground node of the Sense Resistor as possible.

Title	BQ20Z80/bq20z312/bq20z400 EVM		
Size	Number	Rev	A
C	HPA059		
Date	Wed Aug 25, 2004	Drawn by	Contello

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