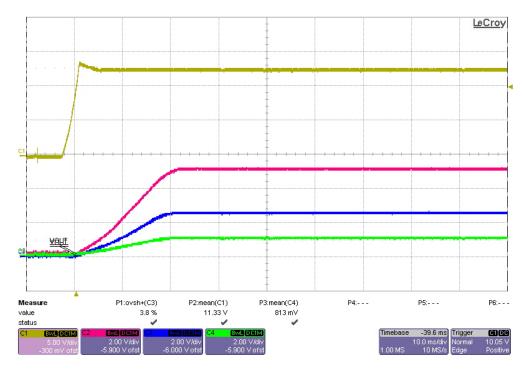


1 Startup

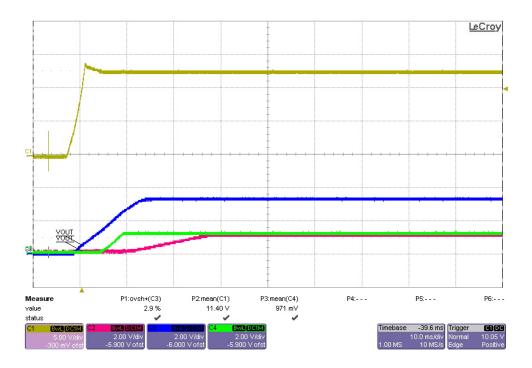
The startup waveforms are shown in the figures below. The input voltage was set at 12V, with no loads on the outputs.

Yellow = Input, Red = 5V0, Blue = 2V5, Green = 1V0



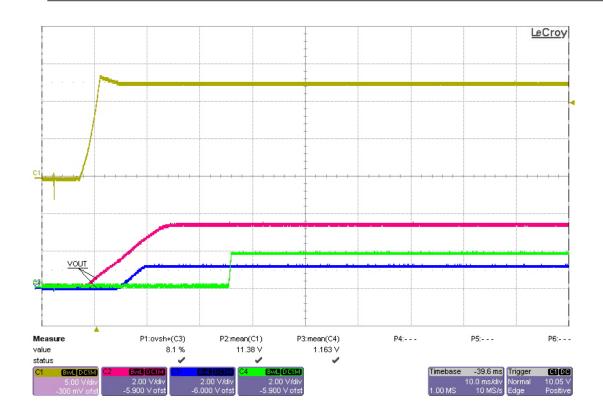
Yellow = Input, Red = 1V0 AVS, Blue = 3V3, Green = 1V2





Yellow = Input, Red = 3V3, Blue = 1V2, Green = 1V8





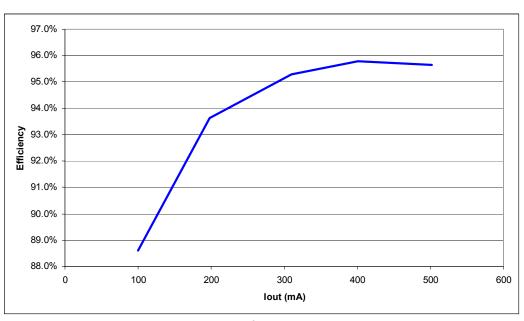
2 Efficiency



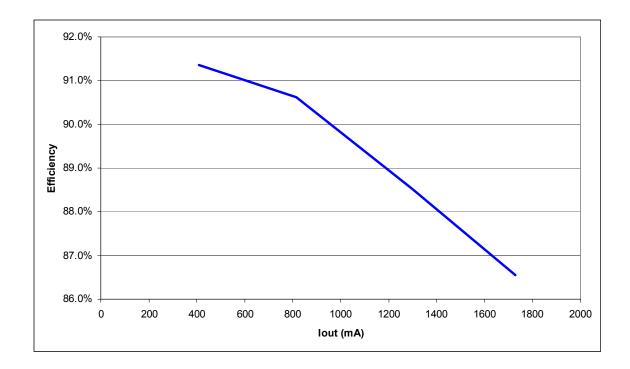


The efficiency is shown in the figures below.

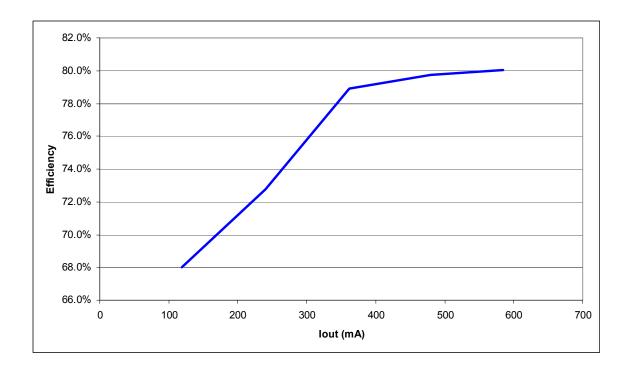
5V0



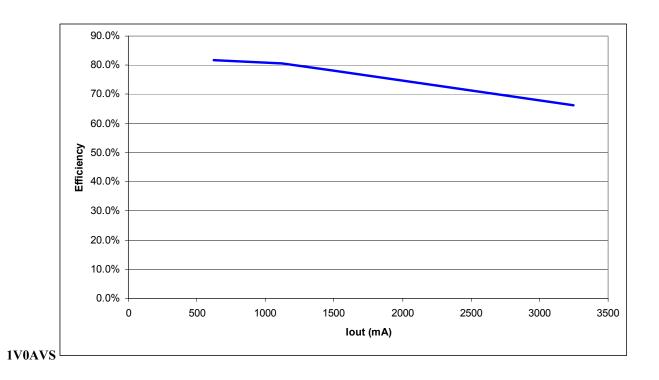




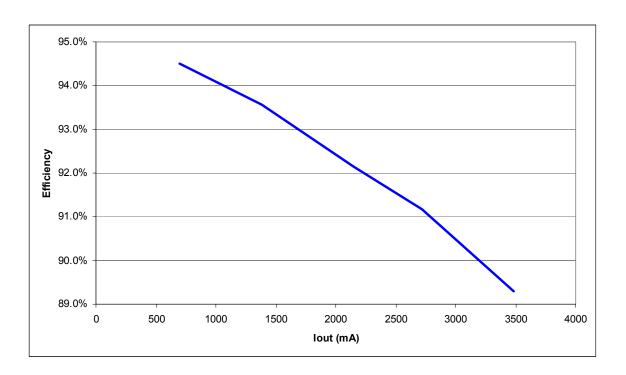






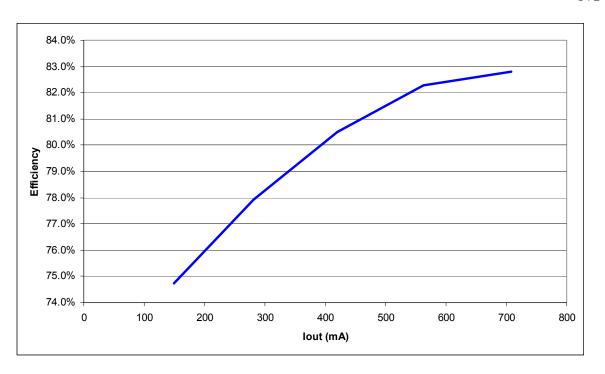




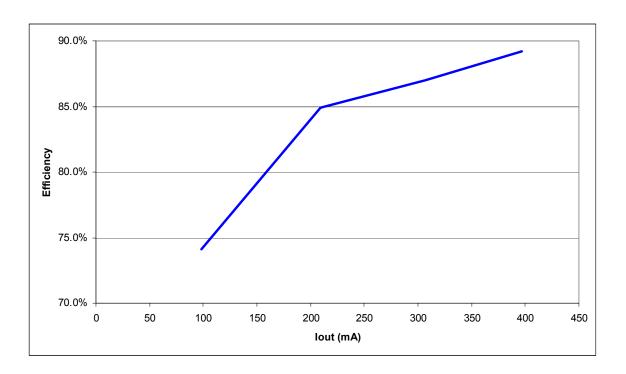




1V2





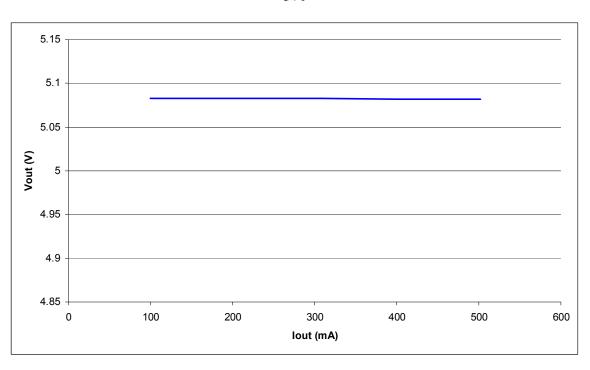




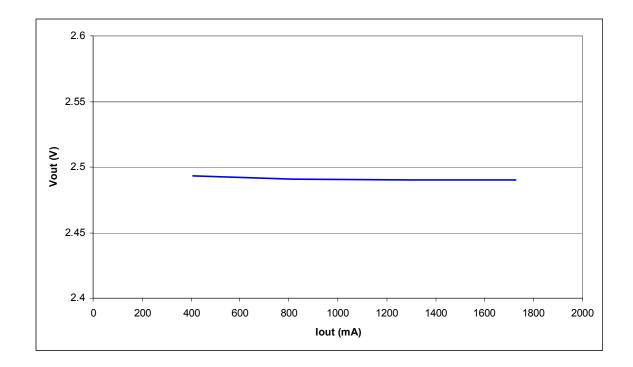
3 Load Regulation

Load regulation of the outputs is shown in the graphs below.

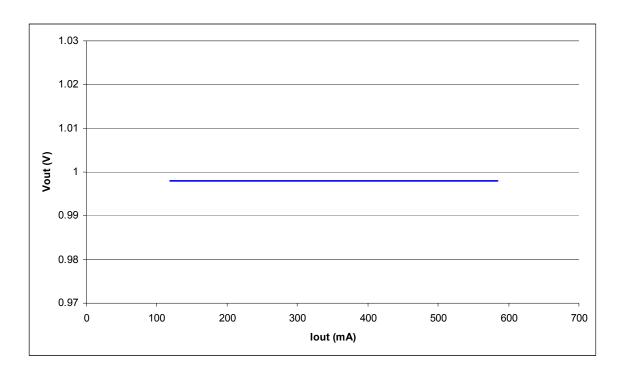
5V0



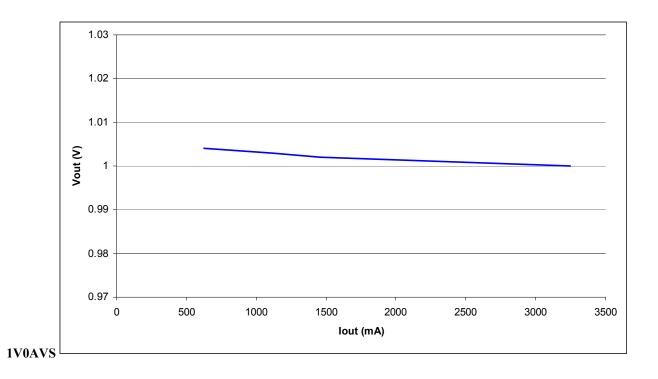




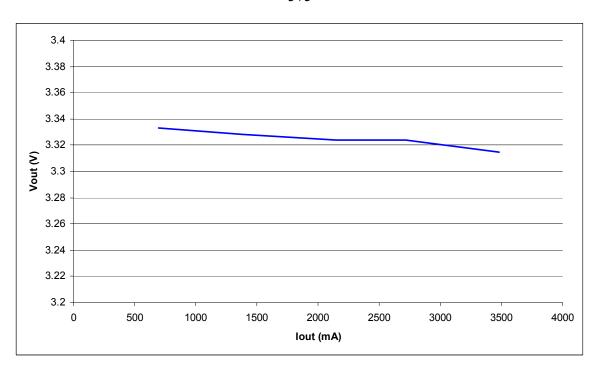




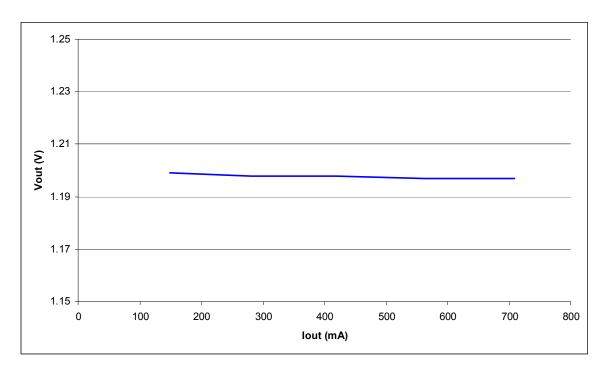








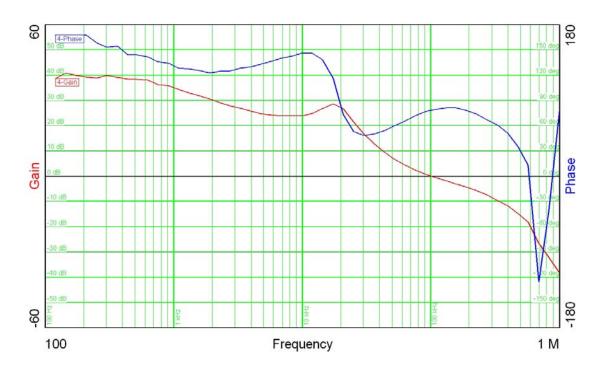




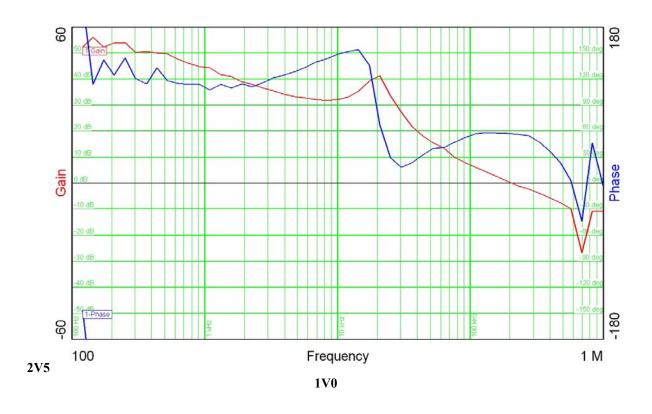


4 Control Loops

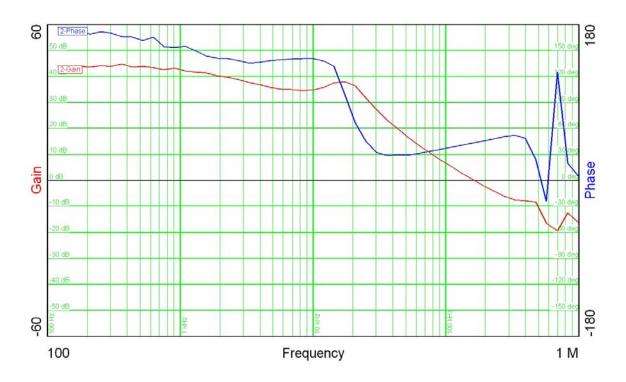
The control loops are shown in the graphs below.



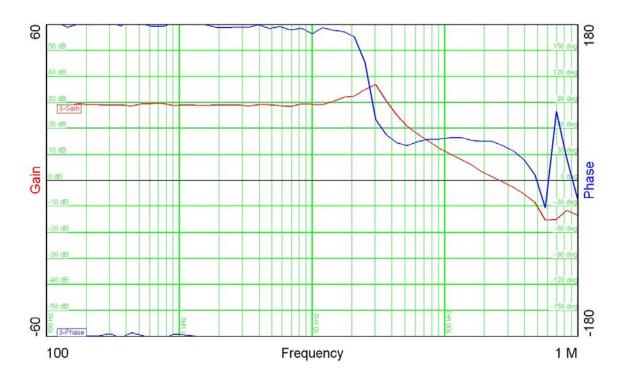




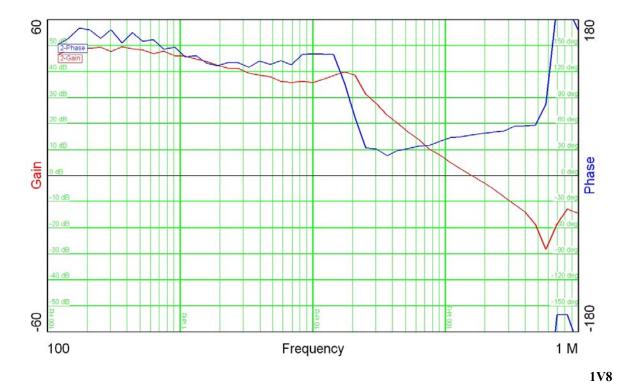






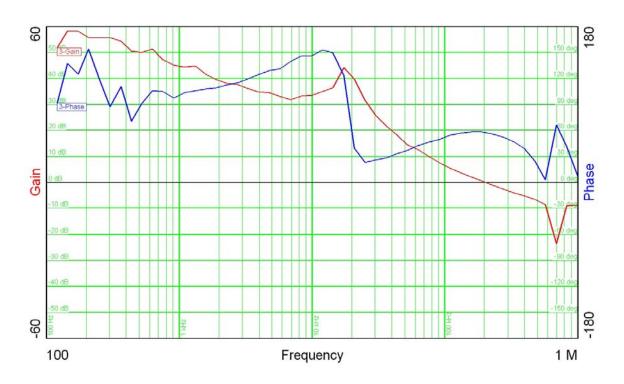






Page 21 of 46

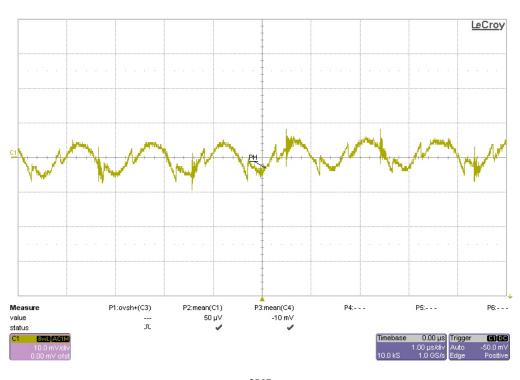




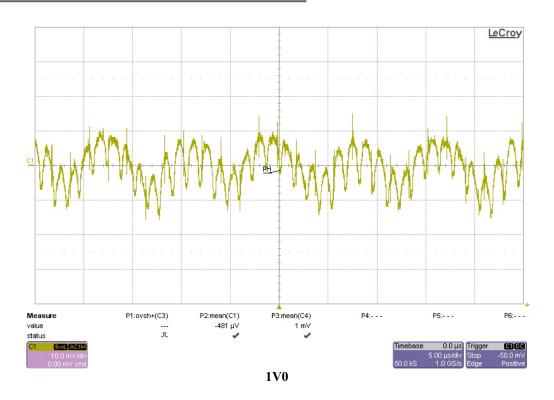
5 Output Ripple Voltage

The output ripple voltage is shown in the figure below. The image was taken at Full Load (2A) and No Load, respectively. 10 mV/div Vertical and 1 uS/div Horizontal.

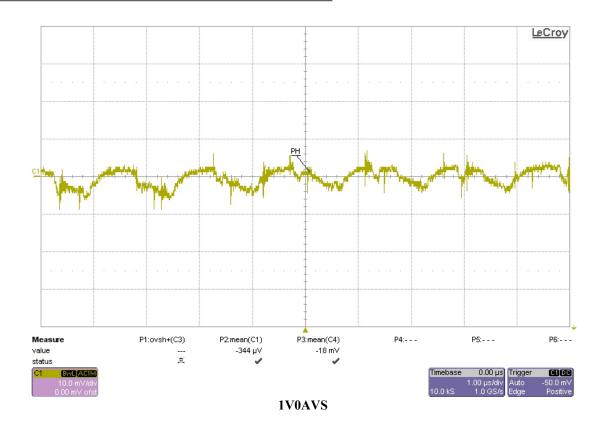




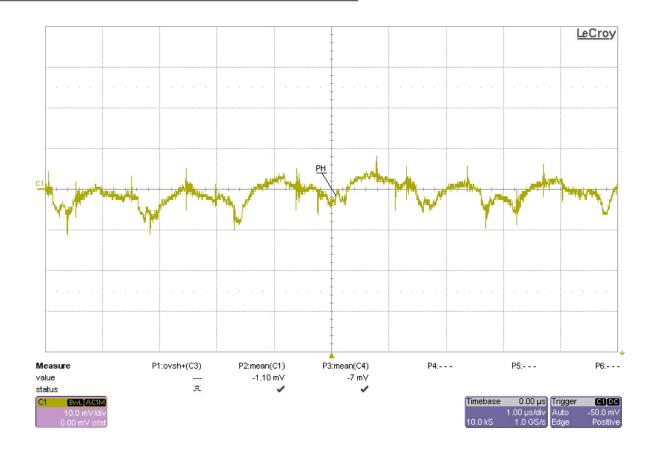




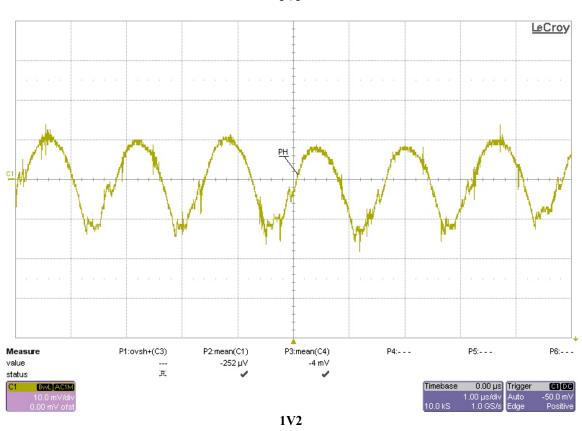




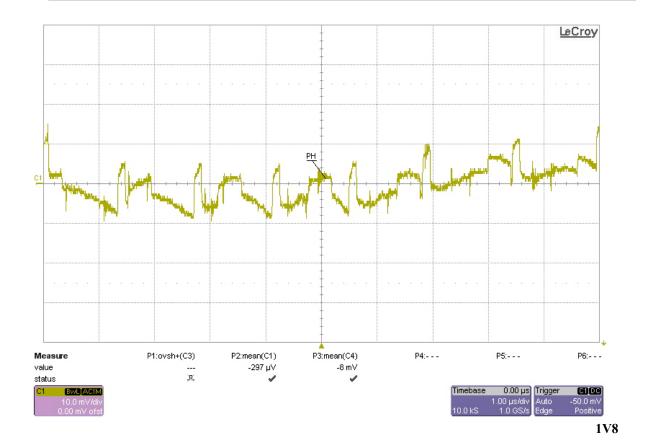






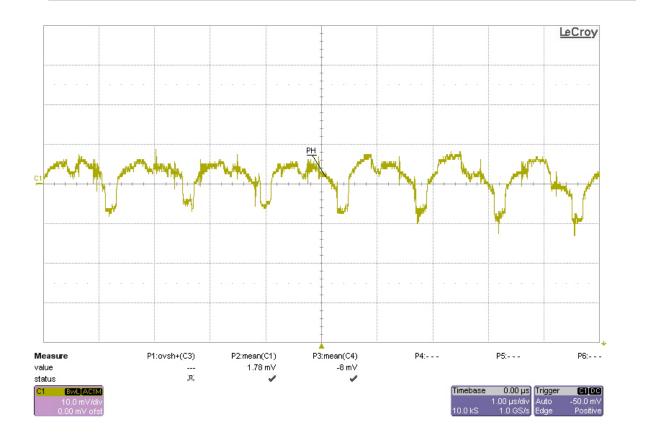






Page 28 of 46







6 Load Transients

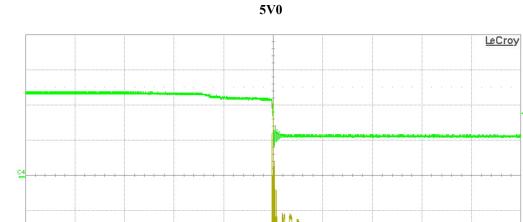
The figures below show the volt output response to load transients. The input voltage was set to 12V.

Measure

value

status

Channel 1 : Vout (AC coupled) Channel 4 : Load current



P3:mean(C4)

350.9 mA

P4:---

P5:---

P6:---

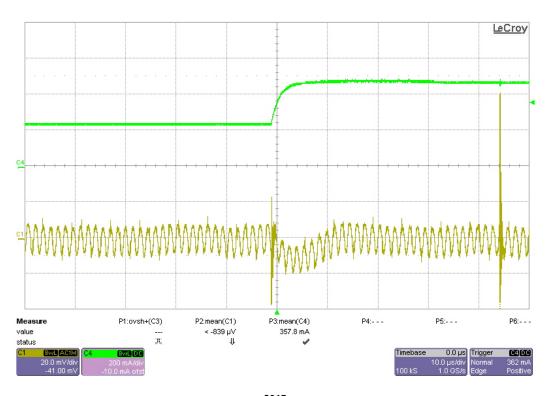


P2:mean(C1)

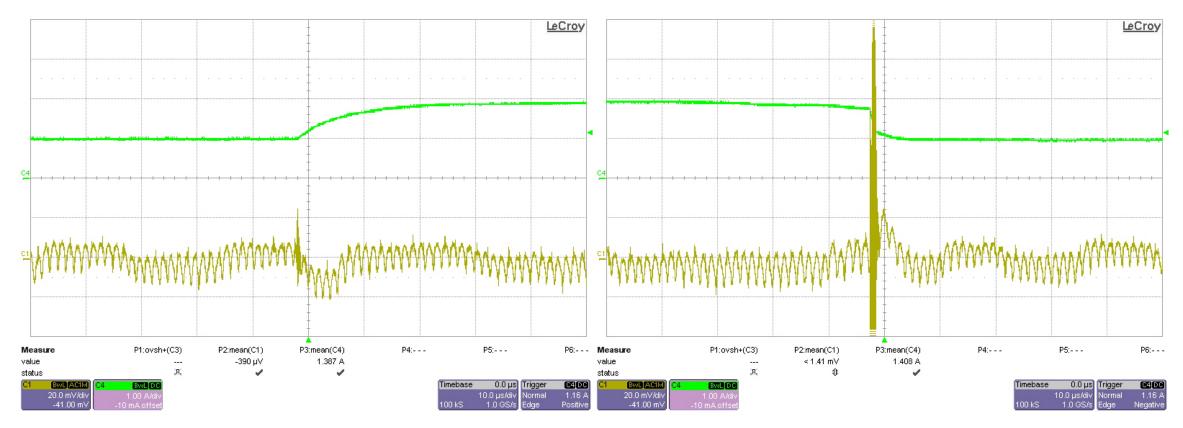
< 1.18 mV

P1:ovsh+(C3)

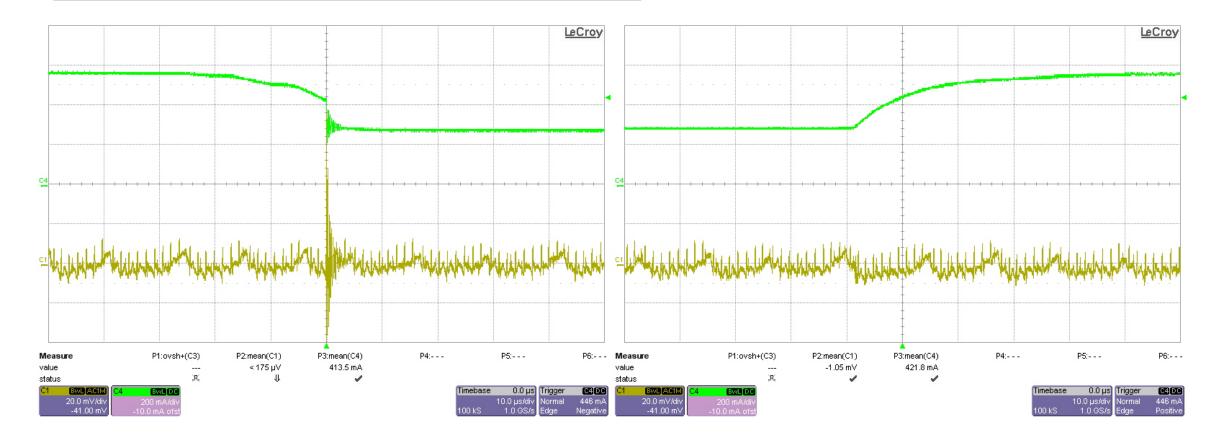






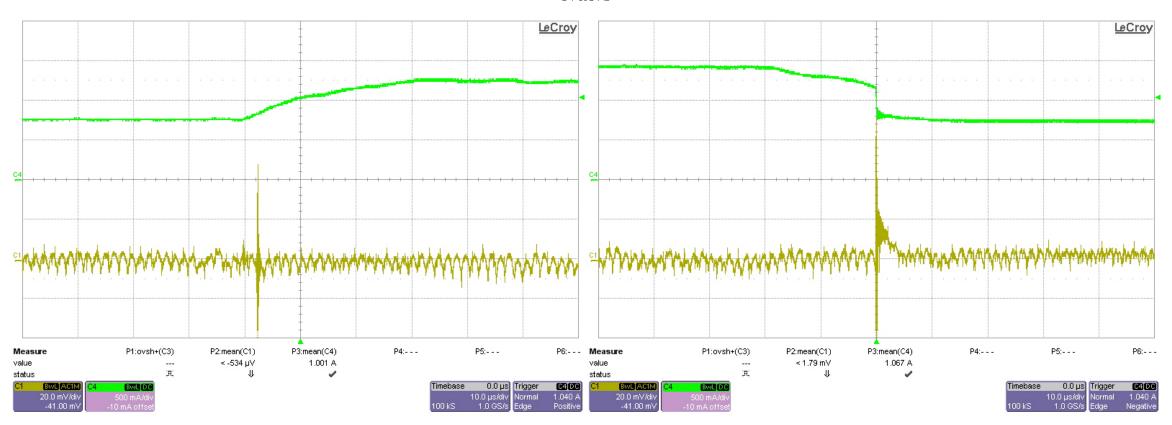




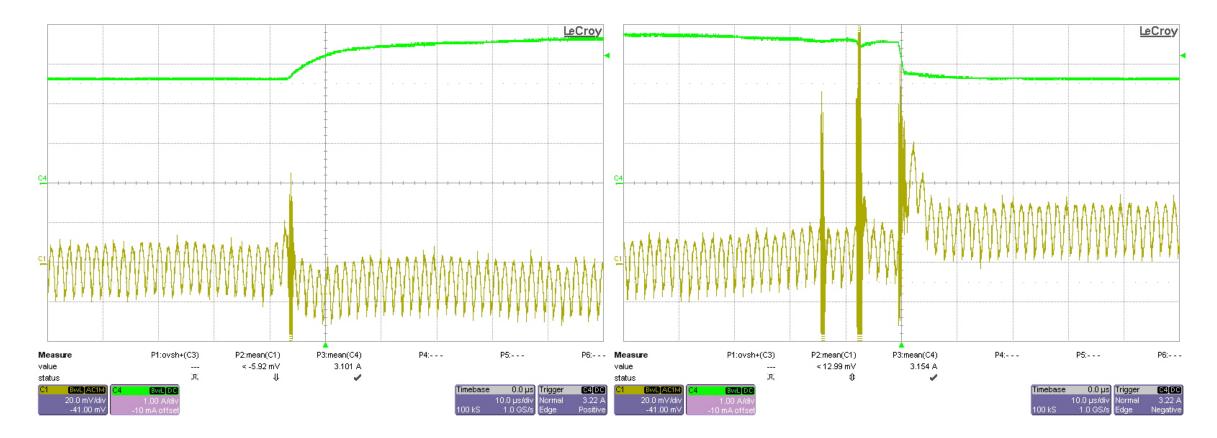




1V0AVS

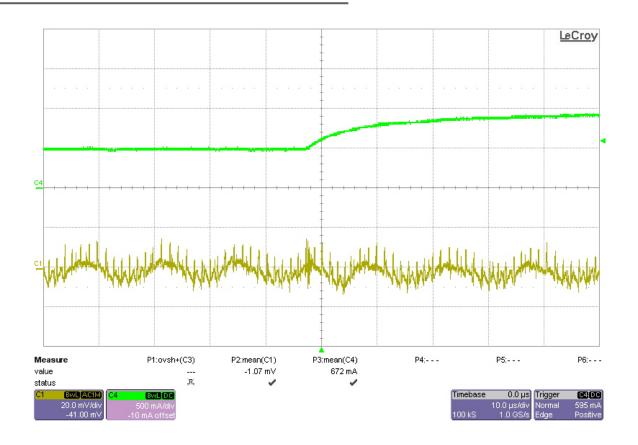




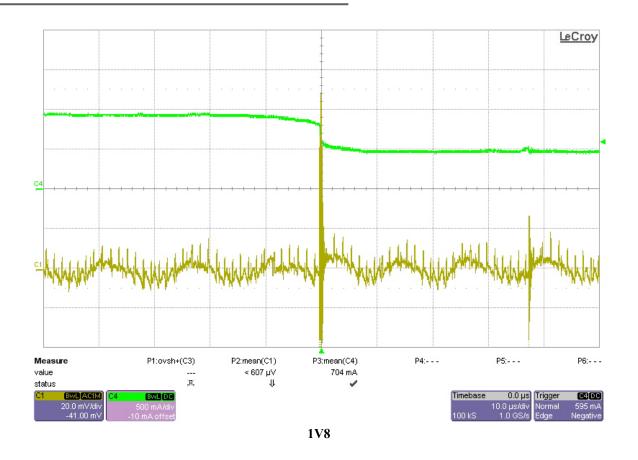


1V2

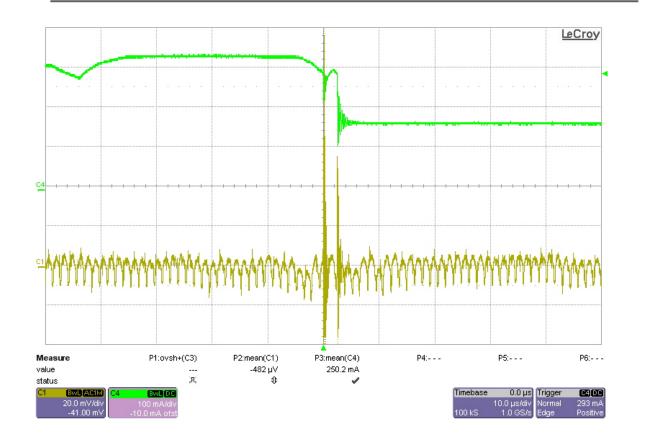




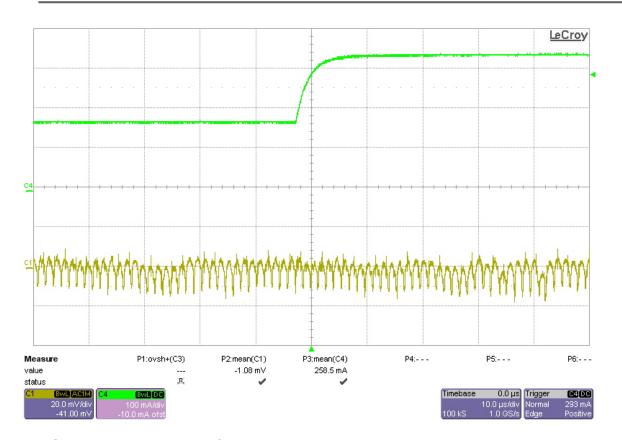










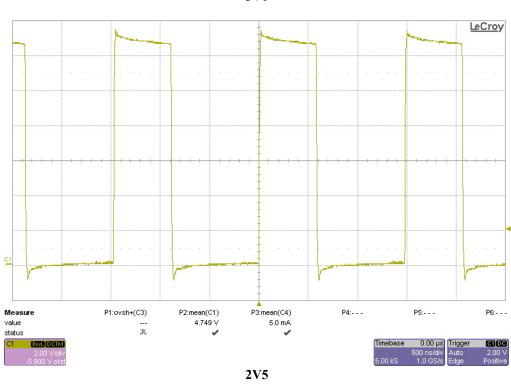


7 Switch Node Waveforms

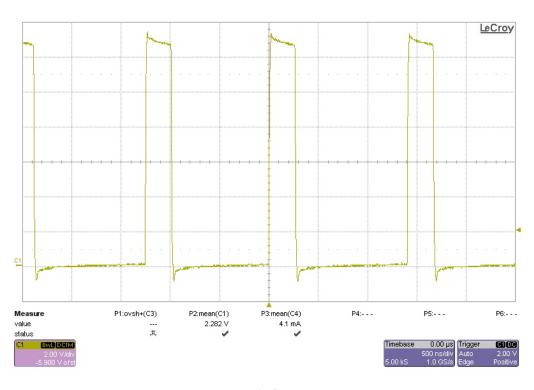
The following figures show the switch node waveforms.





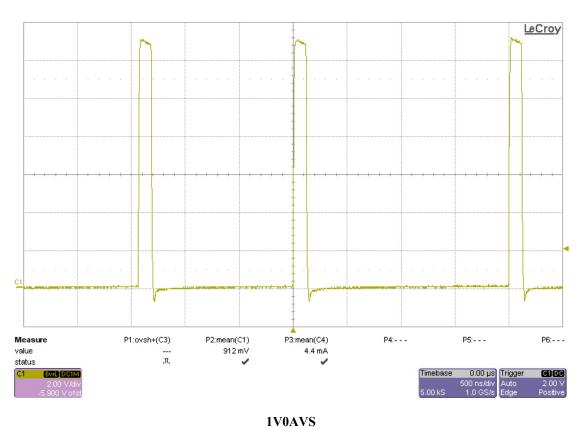




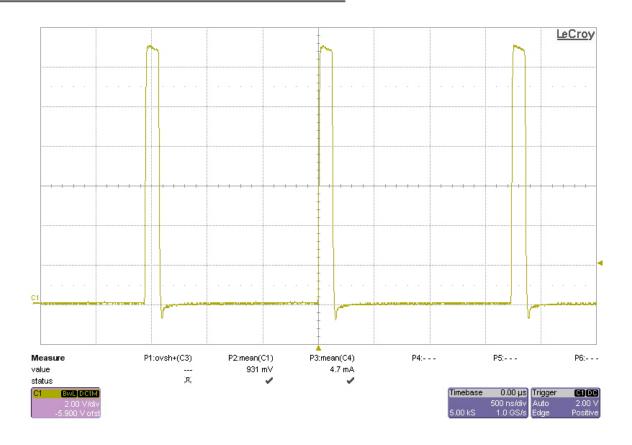


1V0



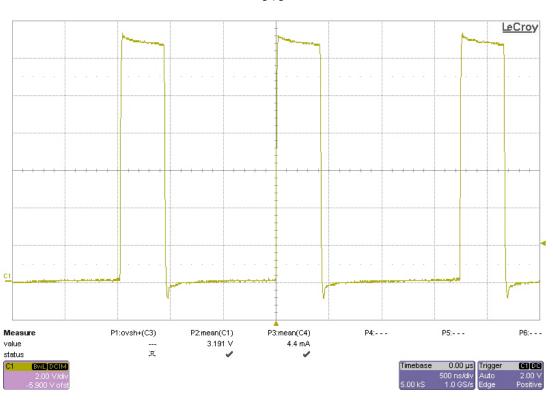






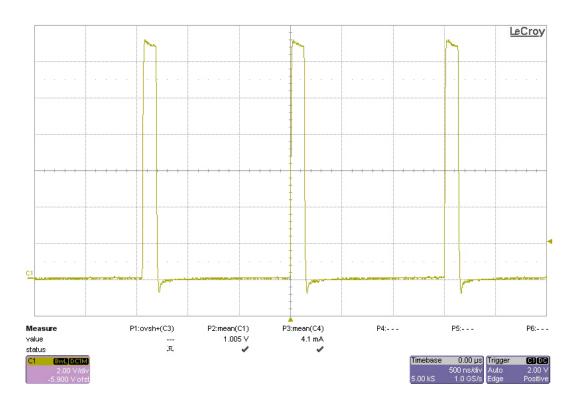




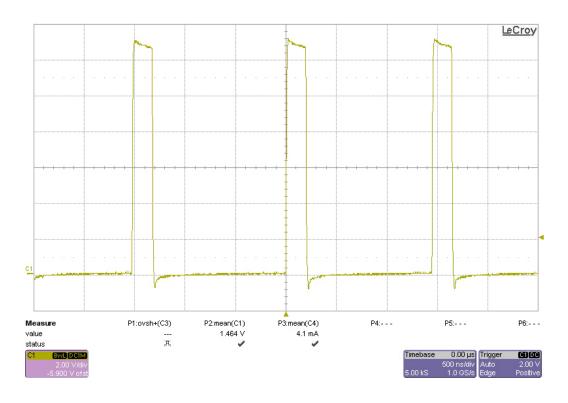




1V2







IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (https://www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2021, Texas Instruments Incorporated