Analog Engineer's Circuit High-Side, Bidirectional Current-Sensing Circuit with Transient Protection

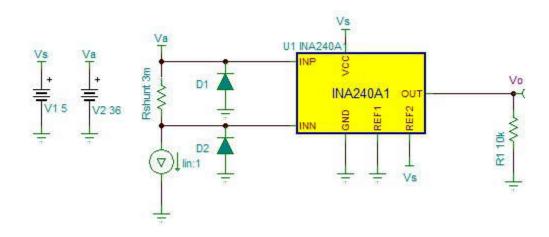


Design Goals

Input		Output		Supply			Standoff and Clamp Voltages		EFT Level
l _{inMin}	I _{inMax}	V _{oMin}	V _{oMax}	Vs	GND	V _{ref}	Vwm	Vc	Vpp
-40 A	40 A	100 mV	4.9 V	5 V	0 V	2.5 V	36 V	80 V	2 kV 8/20 µs

Design Description

This high-side, bidirectional current sensing solution can accurately measure current in the range of -40 A to 40 A for a 36 V voltage bus. The linear voltage output is 100 mV to 4.90 V. This solution is also designed to survive IEC61000-4-4 level 4 EFT stress (Voc = 2 kV; Isc = 40 A; 8/20 µs).



Design Notes

- 1. This solution is targeted toward high-side current sensing.
- 2. The sense resistor value is determined by minimum and maximum load currents, power dissipation and Current Shunt Amplifier (CSA) gain.
- 3. Bidirectional current sensing requires an output reference voltage (Vref). Device gain is achieved through internal precision matched resistor network.
- 4. The expected maximum and minimum output voltage must be within the device linear range.
- 5. The TVS diode must be selected based on bus voltage, the CSA common-mode voltage specification, and EFT pulse characteristics.



Design Steps

1. Determine the maximum output swing:

VswN = Vref - VoMin = 2.5V - 0.1V = 2.4V

VswP = VoMax - Vref = 4.9V - 2.5V = 2.4V

2. Determine the maximum value of the sense resistor based on maximum load current, swing and device gain. In this example, a gain of 20 was chosen to illustrate the calculation, alternative gain versions may be selected as well:

 $Rshunt \leq \frac{Vswp}{Iin_max \times Gain} = \frac{2.4V}{40A \times 20} = 3m \ \Omega$

3. Calculate the peak power rating of the sense resistor:

Pshunt = Iin_max² × Rshunt = $40A^2 \times 3m \Omega = 5W$

4. Determine TVS standoff voltage and clamp voltage:

Vwm = 36V and $Vc \le 80V$

5. Select a TVS diode.

For example, SMBJ36A from Littelfuse[™] satisfies the previous requirement, with peak pulse power of 600 W (10/1000 µs) and current of 10.4 A.

6. Make sure the TVS diode satisfies the design requirement based on the TVS operating curve.

Peak pulse power at given excitation (8/20 μ s) is estimated to be around 3.5 kW, which translates to peak pulse current:

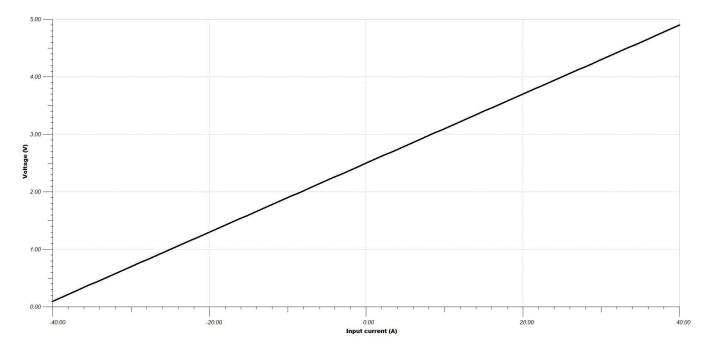
$$Ipp = \frac{3.5kW}{600W} \times 10.4A = 60A$$

This is above the maximum excitation (short circuit) current of 40 A. The select TVS effectively protects the circuit against the specified EFT strike.

Design Simulations

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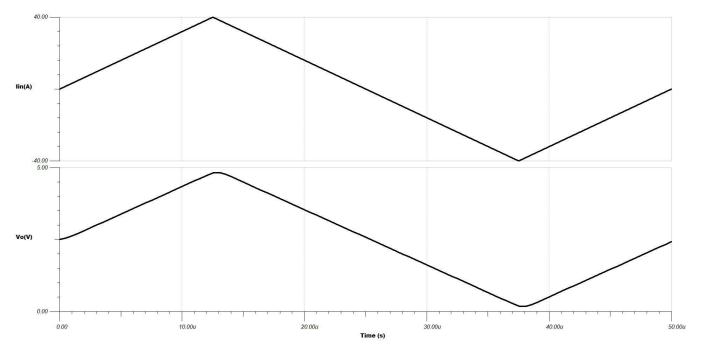
DC Transfer Characteristics



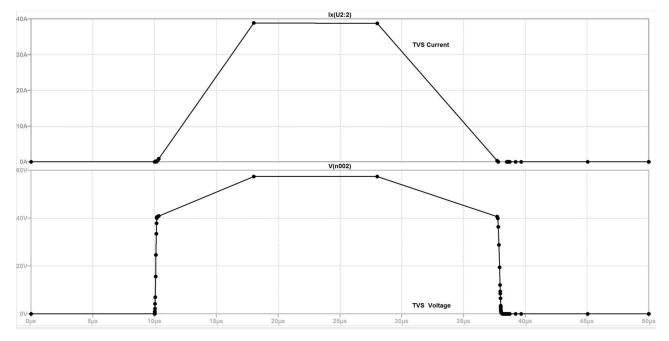


Transient Simulation Results

The output is a scaled version of the input.



TVS Diode Transient Response Under EFT Excitation



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Design References

See Analog Engineer's Circuit Cookbooks for TI's comprehensive circuit library.

For more information on transient protection of the current sense amplifiers, see *TIDA-00302* and the *Current Sense Amplifier Training Videos*.

Design Featured Current Sense Amplifier

INA240A1					
Vs	2.7 V to 5.5 V				
V _{CM}	-4 V to 80 V				
V _{os}	Rail-to-rail				
V _{os}	5 μV				
Ι _Β	80 μΑ				
BW	400 kHz				
Vos Drift	50 nV/°C				
INA2	240A1				

Design Alternate

INA282					
Vs	2.7 V to 18 V				
V _{CM}	-14 V to 80 V				
V _{os}	20 µV				
I _B	25 μΑ				
BW	10 kHz				
Vos Drift	0.3 µV/°C				
INA282					

Revision History

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NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from May 15, 2018 to February 19, 2019

•	Changed VinMin and VinMax in the Design Goals table to linMin and linMax, respectively

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